

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
1	Ok		memory_oct_rzqin	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
2	Ok		memory_mem_dq[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
3	Ok		memory_mem_dq[0]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
4	Ok		memory_mem_dq[0]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
5	Ok		memory_mem_dq[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
6	Ok		memory_mem_dq[1]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
7	Ok		memory_mem_dq[1]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
8	Ok		memory_mem_dq[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
9	Ok		memory_mem_dq[2]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
10	Ok		memory_mem_dq[2]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
11	Ok		memory_mem_dq[3]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
12	Ok		memory_mem_dq[3]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
13	Ok		memory_mem_dq[3]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
14	Ok		memory_mem_dq[I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			4]						
15	Ok		memory_mem_dq[4]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
16	Ok		memory_mem_dq[4]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
17	Ok		memory_mem_dq[5]	I/O Standby	SSTL-15 Class I	Yes	SoCKit_Top		
18	Ok		memory_mem_dq[5]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
19	Ok		memory_mem_dq[5]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
20	Ok		memory_mem_dq[6]	I/O Standby	SSTL-15 Class I	Yes	SoCKit_Top		
21	Ok		memory_mem_dq[6]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
22	Ok		memory_mem_dq[6]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
23	Ok		memory_mem_dq[7]	I/O Standby	SSTL-15 Class I	Yes	SoCKit_Top		
24	Ok		memory_mem_dq[7]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
25	Ok		memory_mem_dq[7]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
26	Ok		memory_mem_dq[8]	I/O Standby	SSTL-15 Class I	Yes	SoCKit_Top		
27	Ok		memory_mem_dq[8]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
					on				
28	Ok		memory_mem_dq[8]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
29	Ok		memory_mem_dq[9]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
30	Ok		memory_mem_dq[9]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
31	Ok		memory_mem_dq[9]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
32	Ok		memory_mem_dq[10]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
33	Ok		memory_mem_dq[10]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
34	Ok		memory_mem_dq[10]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
35	Ok		memory_mem_dq[11]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
36	Ok		memory_mem_dq[11]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
37	Ok		memory_mem_dq[11]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
38	Ok		memory_mem_dq[12]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
39	Ok		memory_mem_dq[12]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
40	Ok		memory_mem_dq[12]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
					n				
41	Ok		memory_mem_dq[13]	I/O Stand	SSTL-15 Class I	Yes	SoCKit_Top		
42	Ok		memory_mem_dq[13]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
43	Ok		memory_mem_dq[13]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
44	Ok		memory_mem_dq[14]	I/O Stand	SSTL-15 Class I	Yes	SoCKit_Top		
45	Ok		memory_mem_dq[14]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
46	Ok		memory_mem_dq[14]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
47	Ok		memory_mem_dq[15]	I/O Stand	SSTL-15 Class I	Yes	SoCKit_Top		
48	Ok		memory_mem_dq[15]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
49	Ok		memory_mem_dq[15]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
50	Ok		memory_mem_dq[16]	I/O Stand	SSTL-15 Class I	Yes	SoCKit_Top		
51	Ok		memory_mem_dq[16]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
52	Ok		memory_mem_dq[16]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
53	Ok		memory_mem_dq[17]	I/O Stand	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
54	Ok		memory_mem_dq[17]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
55	Ok		memory_mem_dq[17]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
56	Ok		memory_mem_dq[18]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
57	Ok		memory_mem_dq[18]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
58	Ok		memory_mem_dq[18]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
59	Ok		memory_mem_dq[19]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
60	Ok		memory_mem_dq[19]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
61	Ok		memory_mem_dq[19]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
62	Ok		memory_mem_dq[20]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
63	Ok		memory_mem_dq[20]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
64	Ok		memory_mem_dq[20]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
65	Ok		memory_mem_dq[21]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
66	Ok		memory_mem_dq[21]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
67	Ok		memory_mem_dq[21]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
68	Ok		memory_mem_dq[22]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
69	Ok		memory_mem_dq[22]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
70	Ok		memory_mem_dq[22]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
71	Ok		memory_mem_dq[23]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
72	Ok		memory_mem_dq[23]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
73	Ok		memory_mem_dq[23]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
74	Ok		memory_mem_dq[24]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
75	Ok		memory_mem_dq[24]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
76	Ok		memory_mem_dq[24]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
77	Ok		memory_mem_dq[25]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
78	Ok		memory_mem_dq[25]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
79	Ok		memory_mem_dq[25]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
80	Ok		memory_mem_dq[26]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
81	Ok		memory_mem_dq[26]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
82	Ok		memory_mem_dq[26]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
83	Ok		memory_mem_dq[27]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
84	Ok		memory_mem_dq[27]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
85	Ok		memory_mem_dq[27]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
86	Ok		memory_mem_dq[28]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
87	Ok		memory_mem_dq[28]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
88	Ok		memory_mem_dq[28]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
89	Ok		memory_mem_dq[29]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
90	Ok		memory_mem_dq[29]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
91	Ok		memory_mem_dq[29]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
92	Ok		memory_mem_dq[30]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
93	Ok		memory_	Input Ter	Parallel 5	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			mem_dq[30]	mination	0 Ohm with Calibration		p		
94	Ok		memory_mem_dq[30]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
95	Ok		memory_mem_dq[31]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
96	Ok		memory_mem_dq[31]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
97	Ok		memory_mem_dq[31]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
98	Ok		memory_mem_dqs[0]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
99	Ok		memory_mem_dqs[0]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
100	Ok		memory_mem_dqs[0]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
101	Ok		memory_mem_dqs[1]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
102	Ok		memory_mem_dqs[1]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
103	Ok		memory_mem_dqs[1]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
104	Ok		memory_mem_dqs[2]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
105	Ok		memory_mem_dqs[2]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
					on				
106	Ok		memory_mem_dqs[2]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
107	Ok		memory_mem_dqs[3]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
108	Ok		memory_mem_dqs[3]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
109	Ok		memory_mem_dqs[3]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
110	Ok		memory_mem_dqs_n[0]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
111	Ok		memory_mem_dqs_n[0]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
112	Ok		memory_mem_dqs_n[0]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
113	Ok		memory_mem_dqs_n[1]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
114	Ok		memory_mem_dqs_n[1]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
115	Ok		memory_mem_dqs_n[1]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
116	Ok		memory_mem_dqs_n[2]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
117	Ok		memory_mem_dqs_n[2]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
118	Ok		memory_mem_dqs_n[2]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
119	Ok		memory_mem_dqs_n[3]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
120	Ok		memory_mem_dqs_n[3]	Input Termination	Parallel 50 Ohm with Calibration	Yes	SoCKit_Top		
121	Ok		memory_mem_dqs_n[3]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
122	Ok		memory_mem_ck	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
123	Ok		memory_mem_ck	Output Termination	Series 50 Ohm without Calibration	Yes	SoCKit_Top		
124	Ok		memory_mem_ck	D5 Delay (output register to io buffer)	2	Yes	SoCKit_Top		
125	Ok		memory_mem_ck_n	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
126	Ok		memory_mem_ck_n	Output Termination	Series 50 Ohm without Calibration	Yes	SoCKit_Top		
127	Ok		memory_mem_ck_n	D5 Delay (output register to io buffer)	2	Yes	SoCKit_Top		
128	Ok		memory_mem_a[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
129	Ok		memory_mem_a[0]	Current Strength	Maximum Current	Yes	SoCKit_Top		
130	Ok		memory_mem_a[10]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
131	Ok		memory_	Current Strength	Maximum	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			mem_a[10]	length	Current		p		
132	Ok		memory_mem_a[11]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
133	Ok		memory_mem_a[11]	Current Strength	Maximum Current	Yes	SoCKit_Top		
134	Ok		memory_mem_a[12]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
135	Ok		memory_mem_a[12]	Current Strength	Maximum Current	Yes	SoCKit_Top		
136	Ok		memory_mem_a[13]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
137	Ok		memory_mem_a[13]	Current Strength	Maximum Current	Yes	SoCKit_Top		
138	Ok		memory_mem_a[14]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
139	Ok		memory_mem_a[14]	Current Strength	Maximum Current	Yes	SoCKit_Top		
140	Ok		memory_mem_a[15]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
141	Ok		memory_mem_a[15]	Current Strength	Maximum Current	Yes	SoCKit_Top		
142	Ok		memory_mem_a[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
143	Ok		memory_mem_a[2]	Current Strength	Maximum Current	Yes	SoCKit_Top		
144	Ok		memory_mem_a[3]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
145	Ok		memory_mem_a[3]	Current Strength	Maximum Current	Yes	SoCKit_Top		
146	Ok		memory_mem_a[4]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
147	Ok		memory_mem_a[4]	Current Strength	Maximum Current	Yes	SoCKit_Top		
148	Ok		memory_mem_a[5]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
149	Ok		memory_mem_a[5]	Current Strength	Maximum Current	Yes	SoCKit_Top		
150	Ok		memory_mem_a[6]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
151	Ok		memory_mem_a[6]	Current Strength	Maximum Current	Yes	SoCKit_Top		
152	Ok		memory_mem_a[7]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
153	Ok		memory_mem_a[7]	Current Strength	Maximum Current	Yes	SoCKit_Top		
154	Ok		memory_mem_a[8]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
155	Ok		memory_mem_a[8]	Current Strength	Maximum Current	Yes	SoCKit_Top		
156	Ok		memory_mem_a[9]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
157	Ok		memory_mem_a[9]	Current Strength	Maximum Current	Yes	SoCKit_Top		
158	Ok		memory_mem_ba[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
159	Ok		memory_mem_ba[0]	Current Strength	Maximum Current	Yes	SoCKit_Top		
160	Ok		memory_mem_ba[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
161	Ok		memory_mem_ba[1]	Current Strength	Maximum Current	Yes	SoCKit_Top		
162	Ok		memory_mem_ba[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
163	Ok		memory_mem_ba[2]	Current Strength	Maximum Current	Yes	SoCKit_Top		
164	Ok		memory_mem_cas_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
165	Ok		memory_mem_cas_n	Current Strength	Maximum Current	Yes	SoCKit_Top		
166	Ok		memory_mem_cke	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
167	Ok		memory_mem_cke	Current Strength	Maximum Current	Yes	SoCKit_Top		
168	Ok		memory_mem_cs_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
169	Ok		memory_mem_cs_n	Current Strength	Maximum Current	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
170	Ok		memory_mem_odt	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
171	Ok		memory_mem_odt	Current Strength	Maximum Current	Yes	SoCKit_Top		
172	Ok		memory_mem_ras_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
173	Ok		memory_mem_ras_n	Current Strength	Maximum Current	Yes	SoCKit_Top		
174	Ok		memory_mem_we_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
175	Ok		memory_mem_we_n	Current Strength	Maximum Current	Yes	SoCKit_Top		
176	Ok		memory_mem_reset_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
177	Ok		memory_mem_reset_n	Current Strength	Maximum Current	Yes	SoCKit_Top		
178	Ok		memory_mem_dm[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
179	Ok		memory_mem_dm[0]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
180	Ok		memory_mem_dm[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
181	Ok		memory_mem_dm[1]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
182	Ok		memory_mem_dm[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
183	Ok		memory_mem_dm[2]	Output Termination	Series 50 Ohm with Calibration	Yes	SoCKit_Top		
184	Ok		memory_mem_dm[3]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
185	Ok		memory_mem_dm[3]	Output Termination	Series 50 Ohm with	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			3]		Calibration				
186	Ok		memory_mem_dq[0]	Package Slew Compensation	Off	Yes	SoCKit_Top		
187	Ok		memory_mem_dq[1]	Package Slew Compensation	Off	Yes	SoCKit_Top		
188	Ok		memory_mem_dq[2]	Package Slew Compensation	Off	Yes	SoCKit_Top		
189	Ok		memory_mem_dq[3]	Package Slew Compensation	Off	Yes	SoCKit_Top		
190	Ok		memory_mem_dq[4]	Package Slew Compensation	Off	Yes	SoCKit_Top		
191	Ok		memory_mem_dq[5]	Package Slew Compensation	Off	Yes	SoCKit_Top		
192	Ok		memory_mem_dq[6]	Package Slew Compensation	Off	Yes	SoCKit_Top		
193	Ok		memory_mem_dq[7]	Package Slew Compensation	Off	Yes	SoCKit_Top		
194	Ok		memory_mem_dq[8]	Package Slew Compensation	Off	Yes	SoCKit_Top		
195	Ok		memory_mem_dq[9]	Package Slew Compensation	Off	Yes	SoCKit_Top		
196	Ok		memory_mem_dq[10]	Package Slew Compensation	Off	Yes	SoCKit_Top		
197	Ok		memory_mem_dq[11]	Package Slew Compensation	Off	Yes	SoCKit_Top		
198	Ok		memory_mem_dq[12]	Package Slew Compensation	Off	Yes	SoCKit_Top		
199	Ok		memory_mem_dq[13]	Package Slew Compensation	Off	Yes	SoCKit_Top		
200	Ok		memory_mem_dq[14]	Package Slew Compensation	Off	Yes	SoCKit_Top		
201	Ok		memory_	Package S	Off	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			mem_dq[15]	kew Compensation			p		
202	Ok		memory_mem_dq[16]	Package S kew Compensation	Off	Yes	SoCKit_Top		
203	Ok		memory_mem_dq[17]	Package S kew Compensation	Off	Yes	SoCKit_Top		
204	Ok		memory_mem_dq[18]	Package S kew Compensation	Off	Yes	SoCKit_Top		
205	Ok		memory_mem_dq[19]	Package S kew Compensation	Off	Yes	SoCKit_Top		
206	Ok		memory_mem_dq[20]	Package S kew Compensation	Off	Yes	SoCKit_Top		
207	Ok		memory_mem_dq[21]	Package S kew Compensation	Off	Yes	SoCKit_Top		
208	Ok		memory_mem_dq[22]	Package S kew Compensation	Off	Yes	SoCKit_Top		
209	Ok		memory_mem_dq[23]	Package S kew Compensation	Off	Yes	SoCKit_Top		
210	Ok		memory_mem_dq[24]	Package S kew Compensation	Off	Yes	SoCKit_Top		
211	Ok		memory_mem_dq[25]	Package S kew Compensation	Off	Yes	SoCKit_Top		
212	Ok		memory_mem_dq[26]	Package S kew Compensation	Off	Yes	SoCKit_Top		
213	Ok		memory_mem_dq[27]	Package S kew Compensation	Off	Yes	SoCKit_Top		
214	Ok		memory_mem_dq[28]	Package S kew Compensation	Off	Yes	SoCKit_Top		
215	Ok		memory_mem_dq[29]	Package S kew Compensation	Off	Yes	SoCKit_Top		
216	Ok		memory_mem_dq[30]	Package S kew Compensation	Off	Yes	SoCKit_Top		
217	Ok		memory_	Package S	Off	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			mem_dq[31]	kew Compensation			p		
218	Ok		memory_mem_dm[0]	Package S kew Compensation	Off	Yes	SoCKit_Top		
219	Ok		memory_mem_dm[1]	Package S kew Compensation	Off	Yes	SoCKit_Top		
220	Ok		memory_mem_dm[2]	Package S kew Compensation	Off	Yes	SoCKit_Top		
221	Ok		memory_mem_dm[3]	Package S kew Compensation	Off	Yes	SoCKit_Top		
222	Ok		memory_mem_dqs[0]	Package S kew Compensation	Off	Yes	SoCKit_Top		
223	Ok		memory_mem_dqs[1]	Package S kew Compensation	Off	Yes	SoCKit_Top		
224	Ok		memory_mem_dqs[2]	Package S kew Compensation	Off	Yes	SoCKit_Top		
225	Ok		memory_mem_dqs[3]	Package S kew Compensation	Off	Yes	SoCKit_Top		
226	Ok		memory_mem_dqs_n[0]	Package S kew Compensation	Off	Yes	SoCKit_Top		
227	Ok		memory_mem_dqs_n[1]	Package S kew Compensation	Off	Yes	SoCKit_Top		
228	Ok		memory_mem_dqs_n[2]	Package S kew Compensation	Off	Yes	SoCKit_Top		
229	Ok		memory_mem_dqs_n[3]	Package S kew Compensation	Off	Yes	SoCKit_Top		
230	Ok		memory_mem_a[0]	Package S kew Compensation	Off	Yes	SoCKit_Top		
231	Ok		memory_mem_a[10]	Package S kew Compensation	Off	Yes	SoCKit_Top		
232	Ok		memory_mem_a[11]	Package S kew Compensation	Off	Yes	SoCKit_Top		
233	Ok		memory_	Package S	Off	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			mem_a[12]	kew Compensation			p		
234	Ok		memory_mem_a[13]	Package S kew Compensation	Off	Yes	SoCKit_To p		
235	Ok		memory_mem_a[14]	Package S kew Compensation	Off	Yes	SoCKit_To p		
236	Ok		memory_mem_a[15]	Package S kew Compensation	Off	Yes	SoCKit_To p		
237	Ok		memory_mem_a[16]	Package S kew Compensation	Off	Yes	SoCKit_To p		
238	Ok		memory_mem_a[17]	Package S kew Compensation	Off	Yes	SoCKit_To p		
239	Ok		memory_mem_a[18]	Package S kew Compensation	Off	Yes	SoCKit_To p		
240	Ok		memory_mem_a[19]	Package S kew Compensation	Off	Yes	SoCKit_To p		
241	Ok		memory_mem_a[20]	Package S kew Compensation	Off	Yes	SoCKit_To p		
242	Ok		memory_mem_a[21]	Package S kew Compensation	Off	Yes	SoCKit_To p		
243	Ok		memory_mem_a[22]	Package S kew Compensation	Off	Yes	SoCKit_To p		
244	Ok		memory_mem_a[23]	Package S kew Compensation	Off	Yes	SoCKit_To p		
245	Ok		memory_mem_ba[0]	Package S kew Compensation	Off	Yes	SoCKit_To p		
246	Ok		memory_mem_ba[1]	Package S kew Compensation	Off	Yes	SoCKit_To p		
247	Ok		memory_mem_ba[2]	Package S kew Compensation	Off	Yes	SoCKit_To p		
248	Ok		memory_mem_cas_n	Package S kew Compensation	Off	Yes	SoCKit_To p		
249	Ok		memory_	Package S	Off	Yes	SoCKit_To		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			mem_cke	key Compensation			p		
250	Ok		memory_mem_cs_n	Package Skey Compensation	Off	Yes	SoCKit_Top		
251	Ok		memory_mem_odt	Package Skey Compensation	Off	Yes	SoCKit_Top		
252	Ok		memory_mem_ras_n	Package Skey Compensation	Off	Yes	SoCKit_Top		
253	Ok		memory_mem_we_n	Package Skey Compensation	Off	Yes	SoCKit_Top		
254	Ok		memory_mem_reset_n	Package Skey Compensation	Off	Yes	SoCKit_Top		
255	Ok		memory_mem_ck	Package Skey Compensation	Off	Yes	SoCKit_Top		
256	Ok		memory_mem_ck_n	Package Skey Compensation	Off	Yes	SoCKit_Top		
257	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy ureset phy_reset_mem_stable_n	Global Signal	Off	Yes	SoCKit_Top		
258	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy ureset phy_reset_n	Global Signal	Off	Yes	SoCKit_Top		
259	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uio_pads	Global Signal	Off	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			dq_ddio[0].read_capture_clk_buffer						
260	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_datapath reset_n_fifo_write_side[0]	Global Signal	Off	Yes	SoCKit_Top		
261	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_datapath reset_n_fifo_wraddress[0]	Global Signal	Off	Yes	SoCKit_Top		
262	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uio_pads dq_ddio[1].read_capture_clk_buffer	Global Signal	Off	Yes	SoCKit_Top		
263	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_datapath reset_n_fifo_write_si	Global Signal	Off	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			de[1]						
264	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_dat apath reset_n_fifo_wraddress[1]	Global Signal	Off	Yes	SoCKit_Top		
265	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uio_pads dq_ddio[2].read_capture_clk_buffer	Global Signal	Off	Yes	SoCKit_Top		
266	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_dat apath reset_n_fifo_write_side[2]	Global Signal	Off	Yes	SoCKit_Top		
267	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_dat apath reset_n_fifo_wraddress[2]	Global Signal	Off	Yes	SoCKit_Top		
268	Ok		u0 hps_0 hps_io	Global Signal	Off	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			border hps_sdram_inst p0 umemphy uio_pads dq_ddio[3].read_capture_clk_buffer						
269	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_datapath reset_n_fifo_write_side[3]	Global Signal	Off	Yes	SoCKit_Top		
270	Ok		u0 hps_0 hps_io border hps_sdram_inst p0 umemphy uread_datapath reset_n_fifo_wraddress[3]	Global Signal	Off	Yes	SoCKit_Top		
271	Ok		u0 hps_0 hps_io border hps_sdram_inst pll0 fbout	PLL Compensation Mode	Direct	Yes	SoCKit_Top		
272	Ok		AUD_ADC DAT	Location	PIN_AC27	Yes			
273	Ok		AUD_ADC LRCK	Location	PIN_AG30	Yes			
274	Ok		AUD_BCLK	Location	PIN_AE7	Yes			
275	Ok		AUD_DAC DAT	Location	PIN_AG3	Yes			
276	Ok		AUD_DAC LRCK	Location	PIN_AH4	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
277	Ok		AUD_I2C_SCLK	Location	PIN_AH30	Yes			
278	Ok		AUD_I2C_SDA	Location	PIN_AF30	Yes			
279	Ok		AUD_MUTE	Location	PIN_AD26	Yes			
280	Ok		AUD_XCK	Location	PIN_AC9	Yes			
281	Ok		DDR3_A[0]	Location	PIN_AJ14	Yes			
282	Ok		DDR3_A[1]	Location	PIN_AK14	Yes			
283	Ok		DDR3_A[2]	Location	PIN_AH12	Yes			
284	Ok		DDR3_A[3]	Location	PIN_AJ12	Yes			
285	Ok		DDR3_A[4]	Location	PIN_AG15	Yes			
286	Ok		DDR3_A[5]	Location	PIN_AH15	Yes			
287	Ok		DDR3_A[6]	Location	PIN_AK12	Yes			
288	Ok		DDR3_A[7]	Location	PIN_AK13	Yes			
289	Ok		DDR3_A[8]	Location	PIN_AH13	Yes			
290	Ok		DDR3_A[9]	Location	PIN_AH14	Yes			
291	Ok		DDR3_A[10]	Location	PIN_AJ9	Yes			
292	Ok		DDR3_A[11]	Location	PIN_AK9	Yes			
293	Ok		DDR3_A[12]	Location	PIN_AK7	Yes			
294	Ok		DDR3_A[13]	Location	PIN_AK8	Yes			
295	Ok		DDR3_A[14]	Location	PIN_AG12	Yes			
296	Ok		DDR3_BA[0]	Location	PIN_AH10	Yes			
297	Ok		DDR3_BA[1]	Location	PIN_AJ11	Yes			
298	Ok		DDR3_BA[2]	Location	PIN_AK11	Yes			
299	Ok		DDR3_CAS_n	Location	PIN_AH7	Yes			
300	Ok		DDR3_CKE	Location	PIN_AJ21	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
301	Ok		DDR3_CK_n	Location	PIN_AA15	Yes			
302	Ok		DDR3_CK_p	Location	PIN_AA14	Yes			
303	Ok		DDR3_CS_n	Location	PIN_AB15	Yes			
304	Ok		DDR3_DM[0]	Location	PIN_AH17	Yes			
305	Ok		DDR3_DM[1]	Location	PIN_AG23	Yes			
306	Ok		DDR3_DM[2]	Location	PIN_AK23	Yes			
307	Ok		DDR3_DM[3]	Location	PIN_AJ27	Yes			
308	Ok		DDR3_DQ[0]	Location	PIN_AF18	Yes			
309	Ok		DDR3_DQ[1]	Location	PIN_AE17	Yes			
310	Ok		DDR3_DQ[2]	Location	PIN_AG16	Yes			
311	Ok		DDR3_DQ[3]	Location	PIN_AF16	Yes			
312	Ok		DDR3_DQ[4]	Location	PIN_AH20	Yes			
313	Ok		DDR3_DQ[5]	Location	PIN_AG21	Yes			
314	Ok		DDR3_DQ[6]	Location	PIN_AJ16	Yes			
315	Ok		DDR3_DQ[7]	Location	PIN_AH18	Yes			
316	Ok		DDR3_DQ[8]	Location	PIN_AK18	Yes			
317	Ok		DDR3_DQ[9]	Location	PIN_AJ17	Yes			
318	Ok		DDR3_DQ[10]	Location	PIN_AG18	Yes			
319	Ok		DDR3_DQ[11]	Location	PIN_AK19	Yes			
320	Ok		DDR3_DQ[12]	Location	PIN_AG20	Yes			
321	Ok		DDR3_DQ[13]	Location	PIN_AF19	Yes			
322	Ok		DDR3_DQ[14]	Location	PIN_AJ20	Yes			
323	Ok		DDR3_DQ[15]	Location	PIN_AH24	Yes			
324	Ok		DDR3_DQ	Location	PIN_AE19	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			[16]						
325	Ok		DDR3_DQ [17]	Location	PIN_AE18	Yes			
326	Ok		DDR3_DQ [18]	Location	PIN_AG22	Yes			
327	Ok		DDR3_DQ [19]	Location	PIN_AK22	Yes			
328	Ok		DDR3_DQ [20]	Location	PIN_AF21	Yes			
329	Ok		DDR3_DQ [21]	Location	PIN_AF20	Yes			
330	Ok		DDR3_DQ [22]	Location	PIN_AH23	Yes			
331	Ok		DDR3_DQ [23]	Location	PIN_AK24	Yes			
332	Ok		DDR3_DQ [24]	Location	PIN_AF24	Yes			
333	Ok		DDR3_DQ [25]	Location	PIN_AF23	Yes			
334	Ok		DDR3_DQ [26]	Location	PIN_AJ24	Yes			
335	Ok		DDR3_DQ [27]	Location	PIN_AK26	Yes			
336	Ok		DDR3_DQ [28]	Location	PIN_AE23	Yes			
337	Ok		DDR3_DQ [29]	Location	PIN_AE22	Yes			
338	Ok		DDR3_DQ [30]	Location	PIN_AG25	Yes			
339	Ok		DDR3_DQ [31]	Location	PIN_AK27	Yes			
340	Ok		DDR3_DQ S_n[0]	Location	PIN_W16	Yes			
341	Ok		DDR3_DQ S_n[1]	Location	PIN_W17	Yes			
342	Ok		DDR3_DQ S_n[2]	Location	PIN_AA18	Yes			
343	Ok		DDR3_DQ S_n[3]	Location	PIN_AD19	Yes			
344	Ok		DDR3_DQ S_p[0]	Location	PIN_V16	Yes			
345	Ok		DDR3_DQ S_p[1]	Location	PIN_V17	Yes			
346	Ok		DDR3_DQ S_p[2]	Location	PIN_Y17	Yes			
347	Ok		DDR3_DQ S_p[3]	Location	PIN_AC20	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
348	Ok		DDR3_ODT	Location	PIN_AE16	Yes			
349	Ok		DDR3_RAS_n	Location	PIN_AH8	Yes			
350	Ok		DDR3_RESET_n	Location	PIN_AK21	Yes			
351	Ok		DDR3_RZQ	Location	PIN_AG17	Yes			
352	Ok		DDR3_WEn	Location	PIN_AJ6	Yes			
353	Ok		FAN_CTRL	Location	PIN_AG27	Yes			
354	Ok		HSMC_CLKIN_n[1]	Location	PIN_AB27	Yes			
355	Ok		HSMC_CLKIN_n[2]	Location	PIN_G15	Yes			
356	Ok		HSMC_CLKIN_p[1]	Location	PIN_AA26	Yes			
357	Ok		HSMC_CLKIN_p[2]	Location	PIN_H15	Yes			
358	Ok		HSMC_CLKOUT_n[1]	Location	PIN_E6	Yes			
359	Ok		HSMC_CLKOUT_n[2]	Location	PIN_A10	Yes			
360	Ok		HSMC_CLKOUT_p[1]	Location	PIN_E7	Yes			
361	Ok		HSMC_CLKOUT_p[2]	Location	PIN_A11	Yes			
362	Ok		HSMC_CLK_IN0	Location	PIN_J14	Yes			
363	Ok		HSMC_CLK_OUT0	Location	PIN_AD29	Yes			
364	Ok		HSMC_D[0]	Location	PIN_C10	Yes			
365	Ok		HSMC_D[1]	Location	PIN_H13	Yes			
366	Ok		HSMC_D[2]	Location	PIN_C9	Yes			
367	Ok		HSMC_D[3]	Location	PIN_H12	Yes			
368	Ok		HSMC_GXB_RX_p[0]	Location	PIN_AE2	Yes			
369	Ok		HSMC_GXB_RX_p[1]	Location	PIN_AC2	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
370	Ok		HSMC_GX B_RX_p[2]	Location	PIN_AA2	Yes			
371	Ok		HSMC_GX B_RX_p[3]	Location	PIN_W2	Yes			
372	Ok		HSMC_GX B_RX_p[4]	Location	PIN_U2	Yes			
373	Ok		HSMC_GX B_RX_p[5]	Location	PIN_R2	Yes			
374	Ok		HSMC_GX B_RX_p[6]	Location	PIN_N2	Yes			
375	Ok		HSMC_GX B_RX_p[7]	Location	PIN_J2	Yes			
376	Ok		HSMC_GX B_TX_p[0]	Location	PIN_AD4	Yes			
377	Ok		HSMC_GX B_TX_p[1]	Location	PIN_AB4	Yes			
378	Ok		HSMC_GX B_TX_p[2]	Location	PIN_Y4	Yes			
379	Ok		HSMC_GX B_TX_p[3]	Location	PIN_V4	Yes			
380	Ok		HSMC_GX B_TX_p[4]	Location	PIN_T4	Yes			
381	Ok		HSMC_GX B_TX_p[5]	Location	PIN_P4	Yes			
382	Ok		HSMC_GX B_TX_p[6]	Location	PIN_M4	Yes			
383	Ok		HSMC_GX B_TX_p[7]	Location	PIN_H4	Yes			
384	Ok		HSMC_RE F_CLK_p	Location	PIN_P9	Yes			
385	Ok		HSMC_RX n[0]	Location	PIN_G11	Yes			
386	Ok		HSMC_RX n[1]	Location	PIN_J12	Yes			
387	Ok		HSMC_RX n[2]	Location	PIN_F10	Yes			
388	Ok		HSMC_RX n[3]	Location	PIN_J9	Yes			
389	Ok		HSMC_RX n[4]	Location	PIN_K8	Yes			
390	Ok		HSMC_RX n[5]	Location	PIN_H7	Yes			
391	Ok		HSMC_RX n[6]	Location	PIN_G8	Yes			
392	Ok		HSMC_RX n[7]	Location	PIN_F8	Yes			
393	Ok		HSMC_RX	Location	PIN_E11	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			n[8]						
394	Ok		HSMC_RX n[9]	Location	PIN_B5	Yes			
395	Ok		HSMC_RX n[10]	Location	PIN_D9	Yes			
396	Ok		HSMC_RX n[11]	Location	PIN_D12	Yes			
397	Ok		HSMC_RX n[12]	Location	PIN_D10	Yes			
398	Ok		HSMC_RX n[13]	Location	PIN_B12	Yes			
399	Ok		HSMC_RX n[14]	Location	PIN_E13	Yes			
400	Ok		HSMC_RX n[15]	Location	PIN_G13	Yes			
401	Ok		HSMC_RX n[16]	Location	PIN_F14	Yes			
402	Ok		HSMC_RX p[0]	Location	PIN_G12	Yes			
403	Ok		HSMC_RX p[1]	Location	PIN_K12	Yes			
404	Ok		HSMC_RX p[2]	Location	PIN_G10	Yes			
405	Ok		HSMC_RX p[3]	Location	PIN_J10	Yes			
406	Ok		HSMC_RX p[4]	Location	PIN_K7	Yes			
407	Ok		HSMC_RX p[5]	Location	PIN_J7	Yes			
408	Ok		HSMC_RX p[6]	Location	PIN_H8	Yes			
409	Ok		HSMC_RX p[7]	Location	PIN_F9	Yes			
410	Ok		HSMC_RX p[8]	Location	PIN_F11	Yes			
411	Ok		HSMC_RX p[9]	Location	PIN_B6	Yes			
412	Ok		HSMC_RX p[10]	Location	PIN_E9	Yes			
413	Ok		HSMC_RX p[11]	Location	PIN_E12	Yes			
414	Ok		HSMC_RX p[12]	Location	PIN_D11	Yes			
415	Ok		HSMC_RX p[13]	Location	PIN_C13	Yes			
416	Ok		HSMC_RX p[14]	Location	PIN_F13	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
417	Ok		HSMC_RX_p[15]	Location	PIN_H14	Yes			
418	Ok		HSMC_RX_p[16]	Location	PIN_F15	Yes			
419	Ok		HSMC_SCL	Location	PIN_AA28	Yes			
420	Ok		HSMC_SDA	Location	PIN_AE29	Yes			
421	Ok		HSMC_TX_n[0]	Location	PIN_A8	Yes			
422	Ok		HSMC_TX_n[1]	Location	PIN_D7	Yes			
423	Ok		HSMC_TX_n[2]	Location	PIN_F6	Yes			
424	Ok		HSMC_TX_n[3]	Location	PIN_C5	Yes			
425	Ok		HSMC_TX_n[4]	Location	PIN_C4	Yes			
426	Ok		HSMC_TX_n[5]	Location	PIN_E2	Yes			
427	Ok		HSMC_TX_n[6]	Location	PIN_D4	Yes			
428	Ok		HSMC_TX_n[7]	Location	PIN_B3	Yes			
429	Ok		HSMC_TX_n[8]	Location	PIN_D1	Yes			
430	Ok		HSMC_TX_n[9]	Location	PIN_C2	Yes			
431	Ok		HSMC_TX_n[10]	Location	PIN_B1	Yes			
432	Ok		HSMC_TX_n[11]	Location	PIN_A3	Yes			
433	Ok		HSMC_TX_n[12]	Location	PIN_A5	Yes			
434	Ok		HSMC_TX_n[13]	Location	PIN_B7	Yes			
435	Ok		HSMC_TX_n[14]	Location	PIN_B8	Yes			
436	Ok		HSMC_TX_n[15]	Location	PIN_B11	Yes			
437	Ok		HSMC_TX_n[16]	Location	PIN_A13	Yes			
438	Ok		HSMC_TX_p[0]	Location	PIN_A9	Yes			
439	Ok		HSMC_TX_p[1]	Location	PIN_E8	Yes			
440	Ok		HSMC_TX	Location	PIN_G7	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			p[2]						
441	Ok		HSMC_TX_p[3]	Location	PIN_D6	Yes			
442	Ok		HSMC_TX_p[4]	Location	PIN_D5	Yes			
443	Ok		HSMC_TX_p[5]	Location	PIN_E3	Yes			
444	Ok		HSMC_TX_p[6]	Location	PIN_E4	Yes			
445	Ok		HSMC_TX_p[7]	Location	PIN_C3	Yes			
446	Ok		HSMC_TX_p[8]	Location	PIN_E1	Yes			
447	Ok		HSMC_TX_p[9]	Location	PIN_D2	Yes			
448	Ok		HSMC_TX_p[10]	Location	PIN_B2	Yes			
449	Ok		HSMC_TX_p[11]	Location	PIN_A4	Yes			
450	Ok		HSMC_TX_p[12]	Location	PIN_A6	Yes			
451	Ok		HSMC_TX_p[13]	Location	PIN_C7	Yes			
452	Ok		HSMC_TX_p[14]	Location	PIN_C8	Yes			
453	Ok		HSMC_TX_p[15]	Location	PIN_C12	Yes			
454	Ok		HSMC_TX_p[16]	Location	PIN_B13	Yes			
455	Ok		IRDA_RXD	Location	PIN_AH2	Yes			
456	Ok		KEY[0]	Location	PIN_AE9	Yes			
457	Ok		KEY[1]	Location	PIN_AE12	Yes			
458	Ok		KEY[2]	Location	PIN_AD9	Yes			
459	Ok		KEY[3]	Location	PIN_AD11	Yes			
460	Ok		LED[0]	Location	PIN_AF10	Yes			
461	Ok		LED[1]	Location	PIN_AD10	Yes			
462	Ok		LED[2]	Location	PIN_AE11	Yes			
463	Ok		LED[3]	Location	PIN_AD7	Yes			
464	Ok		OSC_50_B3B	Location	PIN_AF14	Yes			
465	Ok		OSC_50_B4A	Location	PIN_AA16	Yes			
466	Ok		OSC_50_B5B	Location	PIN_Y26	Yes			
467	Ok		OSC_50_B8A	Location	PIN_K14	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
468	Ok		PCIE_PERST_n	Location	PIN_W22	Yes			
469	Ok		PCIE_WAKE_n	Location	PIN_W21	Yes			
470	Ok		RESET_n	Location	PIN_AD27	Yes			
471	Ok		SI5338_SCL	Location	PIN_AE26	Yes			
472	Ok		SI5338_SDA	Location	PIN_AJ29	Yes			
473	Ok		SW[0]	Location	PIN_W25	Yes			
474	Ok		SW[1]	Location	PIN_V25	Yes			
475	Ok		SW[2]	Location	PIN_AC28	Yes			
476	Ok		SW[3]	Location	PIN_AC29	Yes			
477	Ok		TEMP_CS_n	Location	PIN_AF8	Yes			
478	Ok		TEMP_DIN	Location	PIN_AG7	Yes			
479	Ok		TEMP_DO UT	Location	PIN_AG1	Yes			
480	Ok		TEMP_SCLK	Location	PIN_AF9	Yes			
481	Ok		USB_B2_CLK	Location	PIN_AF13	Yes			
482	Ok		USB_B2_DATA[0]	Location	PIN_AK28	Yes			
483	Ok		USB_B2_DATA[1]	Location	PIN_AD20	Yes			
484	Ok		USB_B2_DATA[2]	Location	PIN_AD21	Yes			
485	Ok		USB_B2_DATA[3]	Location	PIN_Y19	Yes			
486	Ok		USB_B2_DATA[4]	Location	PIN_AA20	Yes			
487	Ok		USB_B2_DATA[5]	Location	PIN_AH27	Yes			
488	Ok		USB_B2_DATA[6]	Location	PIN_AF25	Yes			
489	Ok		USB_B2_DATA[7]	Location	PIN_AC22	Yes			
490	Ok		USB_EMPTY	Location	PIN_AJ4	Yes			
491	Ok		USB_FULL	Location	PIN_AK3	Yes			
492	Ok		USB_OE_n	Location	PIN_AE14	Yes			
493	Ok		USB_RD_n	Location	PIN_AJ5	Yes			
494	Ok		USB_RESET_n	Location	PIN_AD14	Yes			
495	Ok		USB_SCL	Location	PIN_AK4	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
496	Ok		USB_SDA	Location	PIN_AE13	Yes			
497	Ok		USB_WR_n	Location	PIN_AK6	Yes			
498	Ok		VGA_B[0]	Location	PIN_AE28	Yes			
499	Ok		VGA_B[1]	Location	PIN_Y23	Yes			
500	Ok		VGA_B[2]	Location	PIN_Y24	Yes			
501	Ok		VGA_B[3]	Location	PIN_AG28	Yes			
502	Ok		VGA_B[4]	Location	PIN_AF28	Yes			
503	Ok		VGA_B[5]	Location	PIN_V23	Yes			
504	Ok		VGA_B[6]	Location	PIN_W24	Yes			
505	Ok		VGA_B[7]	Location	PIN_AF29	Yes			
506	Ok		VGA_BLANK_n	Location	PIN_AH3	Yes			
507	Ok		VGA_CLK	Location	PIN_W20	Yes			
508	Ok		VGA_G[0]	Location	PIN_Y21	Yes			
509	Ok		VGA_G[1]	Location	PIN_AA25	Yes			
510	Ok		VGA_G[2]	Location	PIN_AB26	Yes			
511	Ok		VGA_G[3]	Location	PIN_AB22	Yes			
512	Ok		VGA_G[4]	Location	PIN_AB23	Yes			
513	Ok		VGA_G[5]	Location	PIN_AA24	Yes			
514	Ok		VGA_G[6]	Location	PIN_AB25	Yes			
515	Ok		VGA_G[7]	Location	PIN_AE27	Yes			
516	Ok		VGA_HS	Location	PIN_AD12	Yes			
517	Ok		VGA_R[0]	Location	PIN_AG5	Yes			
518	Ok		VGA_R[1]	Location	PIN_AA12	Yes			
519	Ok		VGA_R[2]	Location	PIN_AB12	Yes			
520	Ok		VGA_R[3]	Location	PIN_AF6	Yes			
521	Ok		VGA_R[4]	Location	PIN_AG6	Yes			
522	Ok		VGA_R[5]	Location	PIN_AJ2	Yes			
523	Ok		VGA_R[6]	Location	PIN_AH5	Yes			
524	Ok		VGA_R[7]	Location	PIN_AJ1	Yes			
525	Ok		VGA_SYNC_n	Location	PIN_AG2	Yes			
526	Ok		VGA_VS	Location	PIN_AC12	Yes			
527	Ok		CONFIG0_1	Location	PIN_A6	Yes			
528	Ok		CONFIG0_1	I/O Standard	2.5 V	Yes	SoCKit_Top		
529	Ok		CONFIG0_2	Location	PIN_A5	Yes			
530	Ok		CONFIG0_2	I/O Standard	2.5 V	Yes	SoCKit_Top		
531	Ok		CONFIG1_1	Location	PIN_C7	Yes			
532	Ok		CONFIG1_1	I/O Standard	2.5 V	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			1	rd			p		
533	Ok		CONFIG1_2	Location	PIN_B7	Yes			
534	Ok		CONFIG1_2	I/O Standalone	2.5 V	Yes	SoCKit_Top		
535	Ok		GPIO0_1	Location	PIN_C8	Yes			
536	Ok		GPIO0_1	I/O Standalone	2.5 V	Yes	SoCKit_Top		
537	Ok		GPIO0_2	Location	PIN_B5	Yes			
538	Ok		GPIO0_2	I/O Standalone	2.5 V	Yes	SoCKit_Top		
539	Ok		GPIO1_1	Location	PIN_B8	Yes			
540	Ok		GPIO1_1	I/O Standalone	2.5 V	Yes	SoCKit_Top		
541	Ok		GPIO1_2	Location	PIN_E13	Yes			
542	Ok		GPIO1_2	I/O Standalone	2.5 V	Yes	SoCKit_Top		
543	Ok		HPS_CONFIG_USB_n	I/O Standalone	3.3-V LVTTTL	Yes	SoCKit_Top		
544	Ok		HPS_DDR3_A[0]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
545	Ok		HPS_DDR3_A[1]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
546	Ok		HPS_DDR3_A[2]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
547	Ok		HPS_DDR3_A[3]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
548	Ok		HPS_DDR3_A[4]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
549	Ok		HPS_DDR3_A[5]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
550	Ok		HPS_DDR3_A[6]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
551	Ok		HPS_DDR3_A[7]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
552	Ok		HPS_DDR3_A[8]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
553	Ok		HPS_DDR3_A[9]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
554	Ok		HPS_DDR3_A[10]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
555	Ok		HPS_DDR3_A[11]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
556	Ok		HPS_DDR3_A[12]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		
557	Ok		HPS_DDR3_A[13]	I/O Standalone	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
558	Ok		HPS_DDR3_A[14]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
559	Ok		HPS_DDR3_BA[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
560	Ok		HPS_DDR3_BA[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
561	Ok		HPS_DDR3_BA[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
562	Ok		HPS_DDR3_CAS_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
563	Ok		HPS_DDR3_CKE	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
564	Ok		HPS_DDR3_CK_n	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
565	Ok		HPS_DDR3_CK_p	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
566	Ok		HPS_DDR3_CS_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
567	Ok		HPS_DDR3_DM[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
568	Ok		HPS_DDR3_DM[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
569	Ok		HPS_DDR3_DM[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
570	Ok		HPS_DDR3_DM[3]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
571	Ok		HPS_DDR3_DQS_n[0]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
572	Ok		HPS_DDR3_DQS_n[1]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
573	Ok		HPS_DDR3_DQS_n[2]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
574	Ok		HPS_DDR3_DQS_n[3]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
575	Ok		HPS_DDR3_DQS_p[I/O Standard	Differential 1.5-	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			0]		V SSTL Class I				
576	Ok		HPS_DDR3_DQS_p[1]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
577	Ok		HPS_DDR3_DQS_p[2]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
578	Ok		HPS_DDR3_DQS_p[3]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
579	Ok		HPS_DDR3_DQ[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
580	Ok		HPS_DDR3_DQ[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
581	Ok		HPS_DDR3_DQ[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
582	Ok		HPS_DDR3_DQ[3]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
583	Ok		HPS_DDR3_DQ[4]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
584	Ok		HPS_DDR3_DQ[5]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
585	Ok		HPS_DDR3_DQ[6]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
586	Ok		HPS_DDR3_DQ[7]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
587	Ok		HPS_DDR3_DQ[8]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
588	Ok		HPS_DDR3_DQ[9]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
589	Ok		HPS_DDR3_DQ[10]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
590	Ok		HPS_DDR3_DQ[11]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
591	Ok		HPS_DDR3_DQ[12]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
592	Ok		HPS_DDR3_DQ[13]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
593	Ok		HPS_DDR3_DQ[14]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
594	Ok		HPS_DDR3_DQ[15]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
595	Ok		HPS_DDR3_DQ[16]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
596	Ok		HPS_DDR3_DQ[17]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
597	Ok		HPS_DDR3_DQ[18]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
598	Ok		HPS_DDR3_DQ[19]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
599	Ok		HPS_DDR3_DQ[20]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
600	Ok		HPS_DDR3_DQ[21]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
601	Ok		HPS_DDR3_DQ[22]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
602	Ok		HPS_DDR3_DQ[23]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
603	Ok		HPS_DDR3_DQ[24]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
604	Ok		HPS_DDR3_DQ[25]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
605	Ok		HPS_DDR3_DQ[26]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
606	Ok		HPS_DDR3_DQ[27]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
607	Ok		HPS_DDR3_DQ[28]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
608	Ok		HPS_DDR3_DQ[29]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
609	Ok		HPS_DDR3_DQ[30]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
610	Ok		HPS_DDR3_DQ[31]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
611	Ok		HPS_DDR3_ODT	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
612	Ok		HPS_DDR3_RAS_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
613	Ok		HPS_DDR3_RESET_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
614	Ok		HPS_DDR3_RZQ	I/O Standard	1.5 V	Yes	SoCKit_Top		
615	Ok		HPS_DDR3_WE_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
616	Ok		HPS_ENET_GTX_CLK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
617	Ok		HPS_ENET_INT_n	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
618	Ok		HPS_ENET_MDC	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
619	Ok		HPS_ENET_MDIO	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
620	Ok		HPS_ENET_RX_CLK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
621	Ok		HPS_ENET_RX_DATA[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
622	Ok		HPS_ENET_RX_DATA[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
623	Ok		HPS_ENET_RX_DATA[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
624	Ok		HPS_ENET_RX_DATA[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
625	Ok		HPS_ENET_RX_DV	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
626	Ok		HPS_ENET_TX_DATA[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
627	Ok		HPS_ENET_TX_DATA[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
628	Ok		HPS_ENET_TX_DATA[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
629	Ok		HPS_ENET_TX_DATA[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
630	Ok		HPS_ENET_TX_EN	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
631	Ok		HPS_FLASH_DATA[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
632	Ok		HPS_FLASH_DATA[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
633	Ok		HPS_FLASH_DATA[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
634	Ok		HPS_FLASH_DATA[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
635	Ok		HPS_FLASH_DCLK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
636	Ok		HPS_FLASH_NCSO	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
637	Ok		HPS_GSE NSOR_INT	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
638	Ok		HPS_I2C_ CLK	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
639	Ok		HPS_I2C_S DA	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
640	Ok		HPS_KEY[0]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
641	Ok		HPS_KEY[1]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
642	Ok		HPS_KEY[2]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
643	Ok		HPS_KEY[3]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
644	Ok		HPS_LCM_ BK	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
645	Ok		HPS_LCM_ D_C	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
646	Ok		HPS_LCM_ RST_N	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
647	Ok		HPS_LCM_ SPIM_CLK	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
648	Ok		HPS_LCM_ SPIM_MOS I	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
649	Ok		HPS_LCM_ SPIM_SS	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
650	Ok		HPS_LED[0]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
651	Ok		HPS_LED[1]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
652	Ok		HPS_LED[2]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
653	Ok		HPS_LED[3]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
654	Ok		HPS_LTC_ GPIO	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
655	Ok		HPS_SD_C LK	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
656	Ok		HPS_SD_C MD	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
657	Ok		HPS_SD_D ATA[0]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
658	Ok		HPS_SD_D ATA[1]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		
659	Ok		HPS_SD_D ATA[2]	I/O Stand rd	3.3- V LVTTTL	Yes	SoCKit_To p		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
660	Ok		HPS_SD_D ATA[3]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
661	Ok		HPS_SPIM CLK	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
662	Ok		HPS_SPIM MISO	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
663	Ok		HPS_SPIM MOSI	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
664	Ok		HPS_SPIM SS	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
665	Ok		HPS_SW[0]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
666	Ok		HPS_SW[1]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
667	Ok		HPS_SW[2]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
668	Ok		HPS_SW[3]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
669	Ok		HPS_UART RX	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
670	Ok		HPS_UART TX	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
671	Ok		HPS_USB_ CLKOUT	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
672	Ok		HPS_USB_ DATA[0]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
673	Ok		HPS_USB_ DATA[1]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
674	Ok		HPS_USB_ DATA[2]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
675	Ok		HPS_USB_ DATA[3]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
676	Ok		HPS_USB_ DATA[4]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
677	Ok		HPS_USB_ DATA[5]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
678	Ok		HPS_USB_ DATA[6]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
679	Ok		HPS_USB_ DATA[7]	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
680	Ok		HPS_USB_ DIR	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
681	Ok		HPS_USB_ NXT	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
682	Ok		HPS_USB_ STP	I/O Stand ard	3.3- V LVTTTL	Yes	SoCKit_To p		
683	Ok		HSMC_CL	I/O Stand	2.5 V	Yes	SoCKit_To		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			KIN_p	rd			p		
684	Ok		HSMC_CLKIN_p	Location	PIN_H15	Yes			
685	Ok		HSMC_D[0]	I/O Standard	2.5 V	Yes	SoCKit_Top		
686	Ok		HSMC_D[1]	I/O Standard	2.5 V	Yes	SoCKit_Top		
687	Ok		HSMC_D[2]	I/O Standard	2.5 V	Yes	SoCKit_Top		
688	Ok		HSMC_D[3]	I/O Standard	2.5 V	Yes	SoCKit_Top		
689	Ok		HSMC_REF_CLK_p	I/O Standard	HCSL	Yes	SoCKit_Top		
690	Ok		HSMC_TX_p[0]	I/O Standard	2.5 V	Yes	SoCKit_Top		
691	Ok		HSMC_TX_p[1]	I/O Standard	2.5 V	Yes	SoCKit_Top		
692	Ok		HSMC_TX_p[8]	I/O Standard	2.5 V	Yes	SoCKit_Top		
693	Ok		HSMC_TX_p[9]	I/O Standard	2.5 V	Yes	SoCKit_Top		
694	Ok		HSMC_SCL	I/O Standard	2.5 V	Yes	SoCKit_Top		
695	Ok		HSMC_SDA	I/O Standard	2.5 V	Yes	SoCKit_Top		
696	Ok		IRDA_RXD	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
697	Ok		KEY[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
698	Ok		KEY[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
699	Ok		KEY[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
700	Ok		KEY[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
701	Ok		LAS12	Location	PIN_D10	Yes			
702	Ok		LAS12	I/O Standard	2.5 V	Yes	SoCKit_Top		
703	Ok		LED[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
704	Ok		LED[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
705	Ok		LED[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
706	Ok		LED[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
707	Ok		MDC1	Location	PIN_J12	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
708	Ok		MDC1	I/O Standard	2.5 V	Yes	SoCKit_Top		
709	Ok		MDC2	Location	PIN_D9	Yes			
710	Ok		MDC2	I/O Standard	2.5 V	Yes	SoCKit_Top		
711	Ok		MDIO1	Location	PIN_K12	Yes			
712	Ok		MDIO1	I/O Standard	2.5 V	Yes	SoCKit_Top		
713	Ok		MDIO2	Location	PIN_E9	Yes			
714	Ok		MDIO2	I/O Standard	2.5 V	Yes	SoCKit_Top		
715	Ok		NVMA1SEL	Location	PIN_A3	Yes			
716	Ok		NVMA1SEL	I/O Standard	2.5 V	Yes	SoCKit_Top		
717	Ok		NVMPROT	Location	PIN_C13	Yes			
718	Ok		NVMPROT	I/O Standard	2.5 V	Yes	SoCKit_Top		
719	Ok		OPINLVL	Location	PIN_F6	Yes			
720	Ok		OPINLVL	I/O Standard	2.5 V	Yes	SoCKit_Top		
721	Ok		OPOUTLVL	Location	PIN_G7	Yes			
722	Ok		OPOUTLVL	I/O Standard	2.5 V	Yes	SoCKit_Top		
723	Ok		OPRXLOS2	Location	PIN_F11	Yes			
724	Ok		OPRXLOS2	I/O Standard	2.5 V	Yes	SoCKit_Top		
725	Ok		OPTXFLT2	Location	PIN_E11	Yes			
726	Ok		OPTXFLT2	I/O Standard	2.5 V	Yes	SoCKit_Top		
727	Ok		OSC_50_B3B	I/O Standard	1.5 V	Yes	SoCKit_Top		
728	Ok		OSC_50_B4A	I/O Standard	1.5 V	Yes	SoCKit_Top		
729	Ok		OSC_50_B5B	I/O Standard	2.5 V	Yes	SoCKit_Top		
730	Ok		OSC_50_B8A	I/O Standard	2.5 V	Yes	SoCKit_Top		
731	Ok		HSMC_XAUI_RX_p0[0]	Location	PIN_AE2	Yes			
732	Ok		HSMC_XAUI_RX_p0[1]	Location	PIN_AC2	Yes			
733	Ok		HSMC_XA	Location	PIN_AA2	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			UI_RX_p0[2]						
734	Ok		HSMC_XA UI_RX_p0[3]	Location	PIN_W2	Yes			
735	Ok		HSMC_XA UI_RX_p1[0]	Location	PIN_U2	Yes			
736	Ok		HSMC_XA UI_RX_p1[1]	Location	PIN_R2	Yes			
737	Ok		HSMC_XA UI_RX_p1[2]	Location	PIN_N2	Yes			
738	Ok		HSMC_XA UI_RX_p1[3]	Location	PIN_J2	Yes			
739	Ok		HSMC_XA UI_TX_p0[0]	Location	PIN_AD4	Yes			
740	Ok		HSMC_XA UI_TX_p0[1]	Location	PIN_AB4	Yes			
741	Ok		HSMC_XA UI_TX_p0[2]	Location	PIN_Y4	Yes			
742	Ok		HSMC_XA UI_TX_p0[3]	Location	PIN_V4	Yes			
743	Ok		HSMC_XA UI_TX_p1[0]	Location	PIN_T4	Yes			
744	Ok		HSMC_XA UI_TX_p1[1]	Location	PIN_P4	Yes			
745	Ok		HSMC_XA UI_TX_p1[2]	Location	PIN_M4	Yes			
746	Ok		HSMC_XA UI_TX_p1[3]	Location	PIN_H4	Yes			
747	Ok		HSMC_XA UI_RX_n0[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
748	Ok		HSMC_XA UI_RX_n0[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
749	Ok		HSMC_XA	I/O Standard	1.5-	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			UI_RX_n0[2]	rd	V PCML		p		
750	Ok		HSMC_XA UI_RX_n0[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
751	Ok		HSMC_XA UI_RX_n1[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
752	Ok		HSMC_XA UI_RX_n1[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
753	Ok		HSMC_XA UI_RX_n1[2]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
754	Ok		HSMC_XA UI_RX_n1[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
755	Ok		HSMC_XA UI_TX_n0[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
756	Ok		HSMC_XA UI_TX_n0[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
757	Ok		HSMC_XA UI_TX_n0[2]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
758	Ok		HSMC_XA UI_TX_n0[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
759	Ok		HSMC_XA UI_TX_n1[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
760	Ok		HSMC_XA UI_TX_n1[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
761	Ok		HSMC_XA UI_TX_n1[2]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
762	Ok		HSMC_XA UI_TX_n1[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
763	Ok		HSMC_XA UI_RX_n0[0]	Location	PIN_AE1	Yes			
764	Ok		HSMC_XA UI_RX_n0[1]	Location	PIN_AC1	Yes			
765	Ok		HSMC_XA	Location	PIN_AA1	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			UI_RX_n0[2]						
766	Ok		HSMC_XA UI_RX_n0[3]	Location	PIN_W1	Yes			
767	Ok		HSMC_XA UI_RX_n1[0]	Location	PIN_U1	Yes			
768	Ok		HSMC_XA UI_RX_n1[1]	Location	PIN_R1	Yes			
769	Ok		HSMC_XA UI_RX_n1[2]	Location	PIN_N1	Yes			
770	Ok		HSMC_XA UI_RX_n1[3]	Location	PIN_J1	Yes			
771	Ok		HSMC_XA UI_TX_n0[0]	Location	PIN_AD3	Yes			
772	Ok		HSMC_XA UI_TX_n0[1]	Location	PIN_AB3	Yes			
773	Ok		HSMC_XA UI_TX_n0[2]	Location	PIN_Y3	Yes			
774	Ok		HSMC_XA UI_TX_n0[3]	Location	PIN_V3	Yes			
775	Ok		HSMC_XA UI_TX_n1[0]	Location	PIN_T3	Yes			
776	Ok		HSMC_XA UI_TX_n1[1]	Location	PIN_P3	Yes			
777	Ok		HSMC_XA UI_TX_n1[2]	Location	PIN_M3	Yes			
778	Ok		HSMC_XA UI_TX_n1[3]	Location	PIN_H3	Yes			
779	Ok		SS338_CL KIN	Location	PIN_E7	Yes			
780	Ok		SER_BOO T	Location	PIN_G11	Yes			
781	Ok		TXONOFF 1	Location	PIN_K8	Yes			
782	Ok		TXONOFF	Location	PIN_B12	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			2						
783	Ok		SMBSPDS EL1	Location	PIN_G12	Yes			
784	Ok		SMBSPDS EL2	Location	PIN_B6	Yes			
785	Ok		PRTAD3	Location	PIN_C5	Yes			
786	Ok		PRTAD1	Location	PIN_C4	Yes			
787	Ok		PRTAD02	Location	PIN_E2	Yes			
788	Ok		PRTAD4	Location	PIN_D6	Yes			
789	Ok		PRTAD2	Location	PIN_D5	Yes			
790	Ok		PRTAD01	Location	PIN_E3	Yes			
791	Ok		SMBWEN	Location	PIN_A4	Yes			
792	Ok		SS338_CL KIN	I/O Stand	2.5 V	Yes	SoCKit_Top		
793	Ok		SER_BOOT	I/O Stand	2.5 V	Yes	SoCKit_Top		
794	Ok		TXONOFF 1	I/O Stand	2.5 V	Yes	SoCKit_Top		
795	Ok		TXONOFF 2	I/O Stand	2.5 V	Yes	SoCKit_Top		
796	Ok		SMBSPDS EL1	I/O Stand	2.5 V	Yes	SoCKit_Top		
797	Ok		SMBSPDS EL2	I/O Stand	2.5 V	Yes	SoCKit_Top		
798	Ok		SFP_TXRS 20	I/O Stand	2.5 V	Yes	SoCKit_Top		
799	Ok		PRTAD3	I/O Stand	2.5 V	Yes	SoCKit_Top		
800	Ok		PRTAD1	I/O Stand	2.5 V	Yes	SoCKit_Top		
801	Ok		PRTAD02	I/O Stand	2.5 V	Yes	SoCKit_Top		
802	Ok		PRTAD4	I/O Stand	2.5 V	Yes	SoCKit_Top		
803	Ok		PRTAD2	I/O Stand	2.5 V	Yes	SoCKit_Top		
804	Ok		PRTAD01	I/O Stand	2.5 V	Yes	SoCKit_Top		
805	Ok		SMBWEN	I/O Stand	2.5 V	Yes	SoCKit_Top		
806	Ok		USER_LED_G[6]	Location	PIN_G13	Yes			
807	Ok		USER_LED_G[7]	Location	PIN_H14	Yes			
808	Ok		USER_LED_G[5]	Location	PIN_F15	Yes			
809	Ok		USER_LED	I/O Stand	2.5 V	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			G[6]	rd			p		
810	Ok		USER_LED_G[7]	I/O Standard	2.5 V	Yes	SoCKit_Top		
811	Ok		USER_LED_G[5]	I/O Standard	2.5 V	Yes	SoCKit_Top		
812	Ok		USER_LED_G[4]	Location	PIN_F14	Yes			
813	Ok		USER_LED_G[4]	I/O Standard	2.5 V	Yes	SoCKit_Top		
814	Ok		USER_LED_G[2]	Location	PIN_G8	Yes			
815	Ok		USER_LED_G[0]	Location	PIN_F8	Yes			
816	Ok		USER_LED_G[3]	Location	PIN_H8	Yes			
817	Ok		USER_LED_G[1]	Location	PIN_F9	Yes			
818	Ok		USER_LED_R[7]	Location	PIN_C12	Yes			
819	Ok		USER_LED_G[2]	I/O Standard	2.5 V	Yes	SoCKit_Top		
820	Ok		USER_LED_G[0]	I/O Standard	2.5 V	Yes	SoCKit_Top		
821	Ok		USER_LED_G[3]	I/O Standard	2.5 V	Yes	SoCKit_Top		
822	Ok		USER_LED_G[1]	I/O Standard	2.5 V	Yes	SoCKit_Top		
823	Ok		USER_LED_R[7]	I/O Standard	2.5 V	Yes	SoCKit_Top		
824	Ok		USER_LED_R[6]	Location	PIN_B11	Yes			
825	Ok		USER_LED_R[4]	Location	PIN_A13	Yes			
826	Ok		USER_LED_R[3]	Location	PIN_E4	Yes			
827	Ok		USER_LED_R[5]	Location	PIN_B13	Yes			
828	Ok		USER_LED_R[6]	I/O Standard	2.5 V	Yes	SoCKit_Top		
829	Ok		USER_LED_R[4]	I/O Standard	2.5 V	Yes	SoCKit_Top		
830	Ok		USER_LED_R[3]	I/O Standard	2.5 V	Yes	SoCKit_Top		
831	Ok		USER_LED_R[5]	I/O Standard	2.5 V	Yes	SoCKit_Top		
832	Ok		USER_LED_R[2]	Location	PIN_D4	Yes			

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
833	Ok		USER_LED_R[0]	Location	PIN_B3	Yes			
834	Ok		USER_LED_R[1]	Location	PIN_C3	Yes			
835	Ok		USER_LED_R[2]	I/O Standard	2.5 V	Yes	SoCKit_Top		
836	Ok		USER_LED_R[0]	I/O Standard	2.5 V	Yes	SoCKit_Top		
837	Ok		USER_LED_R[1]	I/O Standard	2.5 V	Yes	SoCKit_Top		
838	Ok		SFP_TXRS20	Location	PIN_B1	Yes			
839	Ok		SFP_TXDIS2	I/O Standard	2.5 V	Yes	SoCKit_Top		
840	Ok		SFP_TXDIS2	Location	PIN_B2	Yes			
841	Ok		PHYRESET	Location	PIN_K7	Yes			
842	Ok		PHYRESET	I/O Standard	2.5 V	Yes	SoCKit_Top		
843	Ok		*pll_0002* altera_pll: altera_pll_i* *	PLL Compensation Mode	Direct	Yes	SoCKit_Top		
844	Ok		*pll_0002* altera_pll: altera_pll_i* *	PLL Channel Spacing	0 kHz	Yes	SoCKit_Top		
845	Ok		*pll_0002* altera_pll: altera_pll_i* *	PLL Automatic Self-Reset	Off	Yes	SoCKit_Top		
846	Ok		*pll_0002* altera_pll: altera_pll_i* *	PLL Bandwidth Preset	Auto	Yes	SoCKit_Top		
847	Ok		VGA_B[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
848	Ok		VGA_B[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
849	Ok		VGA_B[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
850	Ok		VGA_B[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
851	Ok		VGA_B[4]	I/O Standard	3.3-	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
				rd	V LVTTTL		p		
852	Ok		VGA_B[5]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
853	Ok		VGA_B[6]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
854	Ok		VGA_B[7]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
855	Ok		VGA_BLACK_n	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
856	Ok		VGA_CLK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
857	Ok		VGA_G[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
858	Ok		VGA_G[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
859	Ok		VGA_G[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
860	Ok		VGA_G[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
861	Ok		VGA_G[4]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
862	Ok		VGA_G[5]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
863	Ok		VGA_G[6]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
864	Ok		VGA_G[7]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
865	Ok		VGA_HS	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
866	Ok		VGA_R[0]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
867	Ok		VGA_R[1]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
868	Ok		VGA_R[2]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
869	Ok		VGA_R[3]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
870	Ok		VGA_R[4]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
871	Ok		VGA_R[5]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
872	Ok		VGA_R[6]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
873	Ok		VGA_R[7]	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
874	Ok		VGA_SYNC_n	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
875	Ok		VGA_VS	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
876	Ok		TEMP_CS_n	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
877	Ok		TEMP_DIN	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
878	Ok		TEMP_DO UT	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
879	Ok		TEMP_SCLK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
880	Ok		AUD_ADC DAT	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
881	Ok		AUD_ADC LRCK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
882	Ok		AUD_BCLK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
883	Ok		AUD_DAC DAT	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
884	Ok		AUD_DAC LRCK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
885	Ok		AUD_I2C_SCLK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
886	Ok		AUD_I2C_SDAT	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
887	Ok		AUD_MUTE	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
888	Ok		AUD_XCK	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
889	Ok		FAN_CTRL	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
890	Ok		PCIE_PERST_n	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
891	Ok		PCIE_WAKE_n	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
892	Ok		RESET_n	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
893	Ok		SI5338_SCL	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
894	Ok		SI5338_SDA	I/O Standard	3.3-V LVTTTL	Yes	SoCKit_Top		
895	Ok		HSMC_XAUI_RX_p0[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
896	Ok		HSMC_XAUI_RX_p0[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
897	Ok		HSMC_XA	I/O Standard	1.5-	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			UI_RX_p0[2]	rd	V PCML		p		
898	Ok		HSMC_XA UI_RX_p0[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
899	Ok		HSMC_XA UI_TX_p0[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
900	Ok		HSMC_XA UI_TX_p0[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
901	Ok		HSMC_XA UI_TX_p0[2]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
902	Ok		HSMC_XA UI_TX_p0[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
903	Ok		USB_EMPTY	I/O Standard	1.5 V	Yes	SoCKit_Top		
904	Ok		USB_FULL	I/O Standard	1.5 V	Yes	SoCKit_Top		
905	Ok		USB_OE_n	I/O Standard	1.5 V	Yes	SoCKit_Top		
906	Ok		USB_RD_n	I/O Standard	1.5 V	Yes	SoCKit_Top		
907	Ok		USB_RESET_n	I/O Standard	1.5 V	Yes	SoCKit_Top		
908	Ok		USB_SCL	I/O Standard	1.5 V	Yes	SoCKit_Top		
909	Ok		USB_SDA	I/O Standard	1.5 V	Yes	SoCKit_Top		
910	Ok		USB_WR_n	I/O Standard	1.5 V	Yes	SoCKit_Top		
911	Ok		DDR3_A[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
912	Ok		DDR3_A[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
913	Ok		DDR3_A[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
914	Ok		DDR3_A[3]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
915	Ok		DDR3_A[4]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
916	Ok		DDR3_A[5]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
917	Ok		DDR3_A[6]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
918	Ok		DDR3_A[7]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
919	Ok		DDR3_A[8]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
920	Ok		DDR3_A[9]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
921	Ok		DDR3_A[10]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
922	Ok		DDR3_A[11]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
923	Ok		DDR3_A[12]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
924	Ok		DDR3_A[13]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
925	Ok		DDR3_A[14]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
926	Ok		DDR3_BA[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
927	Ok		DDR3_BA[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
928	Ok		DDR3_BA[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
929	Ok		DDR3_CAS_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
930	Ok		DDR3_CKE	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
931	Ok		DDR3_CSn	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
932	Ok		DDR3_Ck_p	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
933	Ok		DDR3_CS_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
934	Ok		DDR3_DM[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
935	Ok		DDR3_DM[1]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
936	Ok		DDR3_DM[2]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
937	Ok		DDR3_DM[3]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
938	Ok		DDR3_DQ[0]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
939	Ok		DDR3_DQ	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			[1]	rd	lass l		p		
940	Ok		DDR3_DQ [2]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
941	Ok		DDR3_DQ [3]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
942	Ok		DDR3_DQ [4]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
943	Ok		DDR3_DQ [5]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
944	Ok		DDR3_DQ [6]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
945	Ok		DDR3_DQ [7]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
946	Ok		DDR3_DQ [8]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
947	Ok		DDR3_DQ [9]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
948	Ok		DDR3_DQ [10]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
949	Ok		DDR3_DQ [11]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
950	Ok		DDR3_DQ [12]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
951	Ok		DDR3_DQ [13]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
952	Ok		DDR3_DQ [14]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
953	Ok		DDR3_DQ [15]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
954	Ok		DDR3_DQ [16]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
955	Ok		DDR3_DQ [17]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
956	Ok		DDR3_DQ [18]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
957	Ok		DDR3_DQ [19]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
958	Ok		DDR3_DQ [20]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
959	Ok		DDR3_DQ [21]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
960	Ok		DDR3_DQ [22]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
961	Ok		DDR3_DQ [23]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		
962	Ok		DDR3_DQ [24]	I/O Standard	SSTL-15 C lass l	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
963	Ok		DDR3_DQ[25]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
964	Ok		DDR3_DQ[26]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
965	Ok		DDR3_DQ[27]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
966	Ok		DDR3_DQ[28]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
967	Ok		DDR3_DQ[29]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
968	Ok		DDR3_DQ[30]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
969	Ok		DDR3_DQ[31]	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
970	Ok		DDR3_DS_n[0]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
971	Ok		DDR3_DS_n[1]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
972	Ok		DDR3_DS_n[2]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
973	Ok		DDR3_DS_n[3]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
974	Ok		DDR3_DS_p[0]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
975	Ok		DDR3_DS_p[1]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
976	Ok		DDR3_DS_p[2]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
977	Ok		DDR3_DS_p[3]	I/O Standard	Differential 1.5-V SSTL Class I	Yes	SoCKit_Top		
978	Ok		DDR3_ODT	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
979	Ok		DDR3_RAS_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
980	Ok		DDR3_RESET_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
981	Ok		DDR3_RZQ	I/O Standard	1.5 V	Yes	SoCKit_Top		
982	Ok		DDR3_WE_n	I/O Standard	SSTL-15 Class I	Yes	SoCKit_Top		
983	Ok		HSMC_CLKOUT_n[1]	I/O Standard	2.5 V	Yes	SoCKit_Top		
984	Ok		HSMC_CLKOUT_n[2]	I/O Standard	2.5 V	Yes	SoCKit_Top		
985	Ok		HSMC_CLKOUT_p[2]	I/O Standard	2.5 V	Yes	SoCKit_Top		
986	Ok		HSMC_CLK_OUT0	I/O Standard	2.5 V	Yes	SoCKit_Top		
987	Ok		HSMC_XAUI_RX_p1[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
988	Ok		HSMC_XAUI_RX_p1[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
989	Ok		HSMC_XAUI_RX_p1[2]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
990	Ok		HSMC_XAUI_RX_p1[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
991	Ok		HSMC_XAUI_TX_p1[0]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
992	Ok		HSMC_XAUI_TX_p1[1]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
993	Ok		HSMC_XAUI_TX_p1[2]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
994	Ok		HSMC_XAUI_TX_p1[3]	I/O Standard	1.5-V PCML	Yes	SoCKit_Top		
995	Ok		HSMC_RX_n[2]	I/O Standard	2.5 V	Yes	SoCKit_Top		
996	Ok		HSMC_RX_n[3]	I/O Standard	2.5 V	Yes	SoCKit_Top		
997	Ok		HSMC_RX	I/O Standard	2.5 V	Yes	SoCKit_Top		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
			n[5]	rd			p		
998	Ok		HSMC_RX_n[11]	I/O Stand	2.5 V	Yes	SoCKit_To		
999	Ok		HSMC_RX_p[2]	rd	2.5 V	Yes	SoCKit_To		
1000	Ok		HSMC_RX_p[3]	rd	2.5 V	Yes	SoCKit_To		
1001	Ok		HSMC_RX_p[4]	rd	2.5 V	Yes	SoCKit_To		
1002	Ok		HSMC_RX_p[5]	rd	2.5 V	Yes	SoCKit_To		
1003	Ok		HSMC_RX_p[11]	rd	2.5 V	Yes	SoCKit_To		
1004	Ok		HSMC_RX_p[12]	rd	2.5 V	Yes	SoCKit_To		
1005	Ok		HSMC_RX_p[14]	rd	2.5 V	Yes	SoCKit_To		
1006	Ok		HSMC_TX_n[0]	I/O Stand	2.5 V	Yes	SoCKit_To		
1007	Ok		HSMC_TX_n[1]	rd	2.5 V	Yes	SoCKit_To		
1008	Ok		HSMC_TX_n[8]	rd	2.5 V	Yes	SoCKit_To		
1009	Ok		HSMC_TX_n[9]	rd	2.5 V	Yes	SoCKit_To		
1010	Ok		SW[0]	I/O Stand	2.5 V	Yes	SoCKit_To		
1011	Ok		SW[1]	rd	2.5 V	Yes	SoCKit_To		
1012	Ok		SW[2]	rd	2.5 V	Yes	SoCKit_To		
1013	Ok		SW[3]	rd	2.5 V	Yes	SoCKit_To		
1014	Ok		USB_B2_C_LK	I/O Stand	1.5 V	Yes	SoCKit_To		
1015	Ok		USB_B2_D_ATA[0]	rd	1.5 V	Yes	SoCKit_To		
1016	Ok		USB_B2_D_ATA[1]	rd	1.5 V	Yes	SoCKit_To		
1017	Ok		USB_B2_D_ATA[2]	rd	1.5 V	Yes	SoCKit_To		
1018	Ok		USB_B2_D_ATA[3]	rd	1.5 V	Yes	SoCKit_To		
1019	Ok		USB_B2_D_ATA[4]	rd	1.5 V	Yes	SoCKit_To		
1020	Ok		USB_B2_D_ATA[5]	rd	1.5 V	Yes	SoCKit_To		

	Status	From	To	Assignment Name	Value	Enabled	Entity	Comment	Tag
1021	Ok		USB_B2_D ATA[6]	I/O Stand ard	1.5 V	Yes	SoCKit_To p		
1022	Ok		USB_B2_D ATA[7]	I/O Stand ard	1.5 V	Yes	SoCKit_To p		
1023	Ok		HSMC_CL KIN_n	I/O Stand ard	2.5 V	Yes	SoCKit_To p		
1024	Ok		HSMC_CL KIN_n	Location	PIN_G15	Yes			
1025	Ok		*pll_0002* altera_pll: altera_pll_ i* *	PLL Comp ensation Mode	Direct	Yes	SoCKit_To p		
1026	Ok		*pll_0002* altera_pll: altera_pll_ i* *	PLL Chann el Spacing	0 kHz	Yes	SoCKit_To p		
1027	Ok		*pll_0002* altera_pll: altera_pll_ i* *	PLL Auto matic Self -Reset	Off	Yes	SoCKit_To p		
1028	Ok		*pll_0002* altera_pll: altera_pll_ i* *	PLL Band width Pres et	Auto	Yes	SoCKit_To p		
1029		<<new> >	<<new> >	<<new> >					