

MP3 Player

CSEE 4840 SPRING 2010 PROJECT DESIGN

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I. Introduction

The purpose of the project is to implement a MP3 decoder on DE2 board using Altera FPGA tool chains. Additionally, the decoder extracts the frequency information from the decoded data and uses these data to exhibits a visual effect, "floating histograms", on the screen, which looks similar to those on classic music player. Our work mainly focuses on building the hardware environment required by this project, implementing the Mpeg 1 Layer III decoding process using low level C programming, and some optimization schemes speeding up the decoding computation within the environment of embedded system.

II. Hardware Configuration

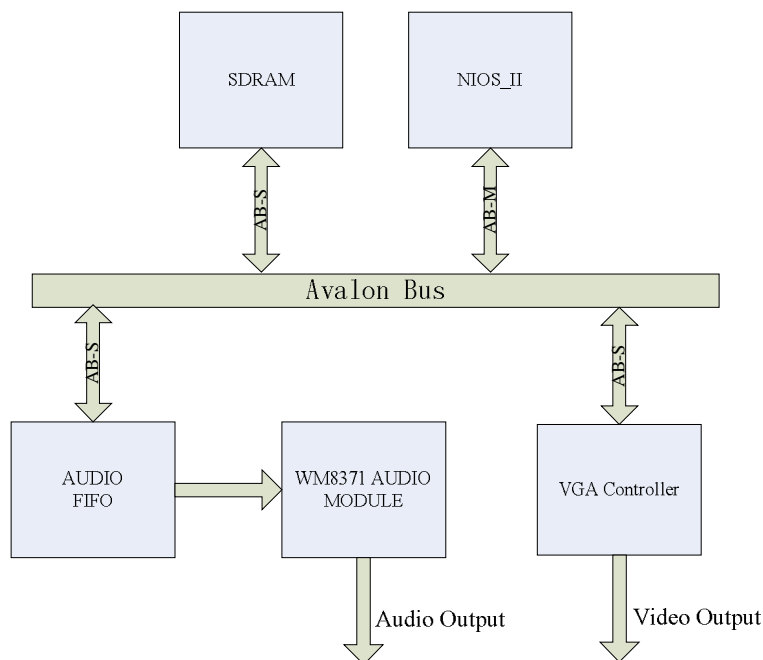


Fig.1 System Architecture

Fig.1 shows the hardware architecture used in this design. Configuration for each module will be discussed as follows.

2.1 Nios II

Due to high computation demand induced by the Mpeg decoding process, we decided to implement the fastest version of NIOS-II available in SOPC environment, accompanied with a embedded multipliers to speed up the large amount of multiplication in the software. Furthermore, a custom instruction used to compute single precision floating point is also added to the system.

2.2 Audio CODEC WM8731

ADC task is to be performed by audio chip on DE2 board. At the end of decoding process for each frame, 36 16-bit PCM samples for each sub-band are acquired. These digital PCM samples are then put into audio control unit and eventually the audio chip on board produce a analog voltage level according to its digital input. Three modules are needed to translate the decoded result to analog audio output.

- I WM8731 control unit, serving as an Avalon slave module and providing hardware interface to CPU
- I A serializer, which handles the parallel-to-serial conversion of the data
- I First-In-First-Out (FIFO) serving as the buffer structure between the WM8731 control unit and serializer
- I I2C control unit, used by CPU to configure WM8731
- I PPL that generates clock frequency of 11.2896Mhz

WM8731 is configured when system is reset. Since the mp3 file we are decoding uses 44.1KHZ sampling rate, to accurately output an analog audio data, we implement a PLL which takes 50Mhz clock a produce 3 clock frequencies: a 50Mhz “back up” clock to CPU, a 11.2896Mhz clock to WM8731, and a clock with 3ns delay with regard to the main clock. The last clock is required by SDRAM for a stable operation.

Internal control register need to be set up based on the sampling frequency of the mp3 file we are decoding. The resulting configuration is shown in table. 1

Table .1

Left line in	0x0080
Right Line in	0x0280
Left Head Phone Out	0x047b
Right Head Phone Out	0x067b
Analog Path Control	0x08d0
Digital Path Control	0x0a04
Power Down Control	0x0c07
Digital Audio Interface Format	0x0e01

Sampling Control	0x1020
Active Control	0x1201

Note: The first 9 bits indicates the address of a specific control register.

2.3 VGA

A VGA control unit specified in generating the “floating histogram” effect is built in the project. Its functions are described as follows.

2.3.1 Generation of 64 rectangles

First, to show 64 rectangles on screen, the horizontal starting points, horizontal ending points and vertical ending points of each rectangle are fixed.

The following figure shows the horizontal timing of the VGA: (vertical timing is similar)

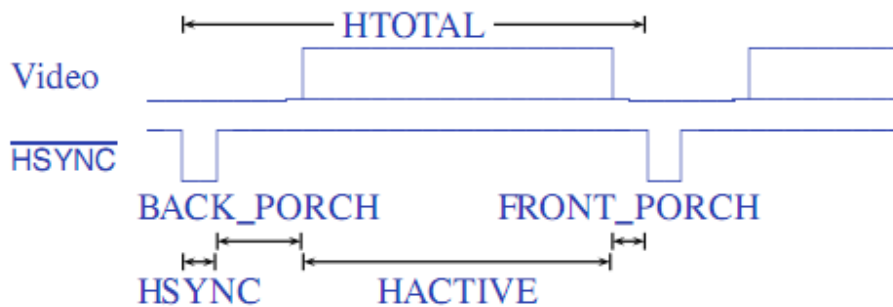


Fig.2

Define a signal of Hcount. At every rising edge of clk25, Hcount add 1, therefore, Hcount works as a horizontal pointer of the scanning position.

When $Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART$, where $RECTANGLE_HSTART$ represents the horizontal starting point of the rectangle, it means the pointer is moving into the horizontal area of rectangles in the histogram. When $Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND$, where $RECTANGLE_HEND$ represents the horizontal ending point of the rectangle, it means the pointer is moving out of the horizontal area of rectangles in the histogram. During the clock cycle and the clock cycles following, the area on the screen will black out.

Having the same function with Hcount, signal Vcount works as a vertical pointer of the scanning position. When $Vcount = VSYNC + VBACK_PORCH + RECTANGLE_VSTART$, where $RECTANGLE_VSTART$ represents the vertical starting point of the rectangle, it means the pointer is moving into the vertical area of rectangles in the histogram. When $Vcount = VSYNC + VBACK_PORCH + RECTANGLE_VEND$, where $RECTANGLE_VEND$ represents the vertical ending point of the rectangle, it means the pointer is moving out of the vertical area of rectangles in the histogram. During the clock cycle and the clock cycles following, the area on the screen will black out.

The necessary condition for a point on screen to light up is

$Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART$, and

$Vcount = VSYNC + VBACK_PORCH + RECTANGLE_VSTART$

Which means the pointer is in both the vertical area and horizontal area of rectangles.

The 64 rectangles in the histogram are all realized in this way.

2.3.2 Controlling the height of each rectangle with the frequency information from the output of the frequency analyzer.

The frequency information is acquired through the decoding software. Specifically, inputs of the function IMDCT (inversed modified discrete cosine transform) are considered to be frequency information for the corresponding PCM samples. We bring these inputs to the VGA controller and adjust the heights, in reality, the vertical starting points for each rectangles based on these inputs.

According to the situation that the active area of the screen is 640*480, the ending point of each rectangle in the histogram is chosen to be 380. Therefore, the frequency information input should be from 0 to 380.

2.4 SDRAM

Since our decoding process bases on a huge array which represents the bit-stream in the MP3 file, the resulting .elf file would occupy a large amount of memory. As a result, SDRAM, which has larger capacity than SRAM, is necessitated by the design.

The Altera DE2 board contains an SDRAM chip that can store 8 Mbytes of data. This memory is organized as 1 M*16 bits*4 banks. The SDRAM chip requires careful timing control. To provide access to the SDRAM chip, the SOPC Builder implements an SDRAM Controller unit. This unit generates the signals needed to deal with SDRAM chip.

All of the control signals, except the clock, can be provided by the SDRAM Controller that can be generated by using the SOPC Builder. For proper operation of the SDRAM chip, it is necessary that its clock signal, DRAM_CLK, leads the Nios II system clock, by 3 nanoseconds. As mentioned in the part of WM8731, this clock is accomplished by using a phase-locked loop (PLL) unit, which is generated from Quartus II Mega function called ALTPLL.

III. MP3 Decoding Algorithm

The MP3 decoding algorithm contains several steps and the flow chart of the algorithm is in the below:

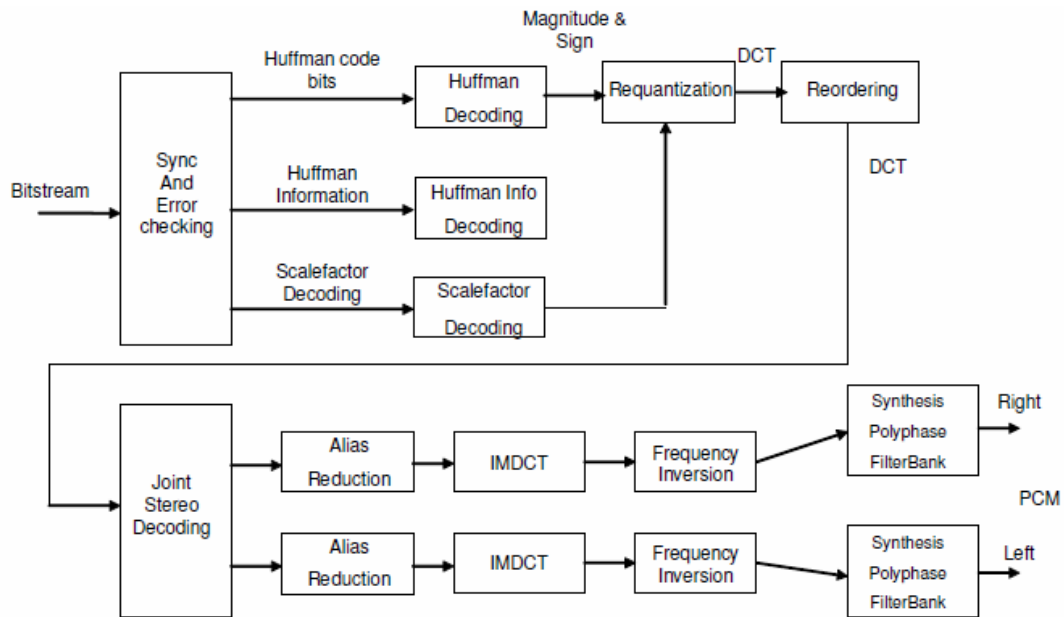


Fig.3 Flow Chart of MP3 Decoding

3.1 MP3 Data Structure

The bit stream data in MP3 file stored is frame by frame. And each frame has the following four parts: Header, Side Information, Main Data, and Ancillary Data.

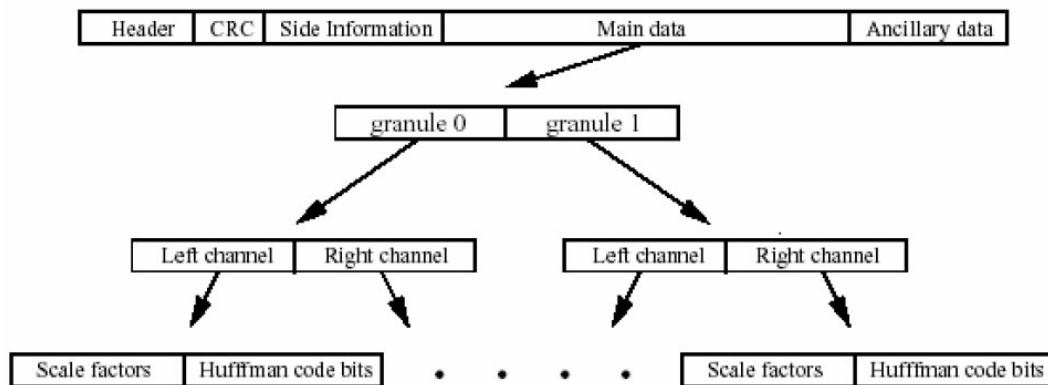


Fig.4 MP3 Frame Data Structure

Header is 32 bits and it confirms the frame. Information such as MPEG version, MPEG layer, bitrate and sampling rate are all contained in the header. Header doesn't have to be in the beginning of each frame therefore each header has a sync-word to mark its position in the file. And usually the information in headers of every frame in one file is consensus.

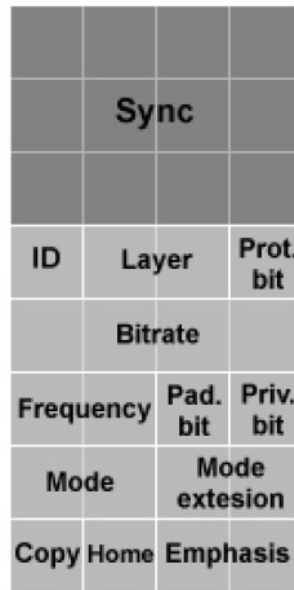


Fig.5 MP3 Frame Header Layout

Side Information can be either 17 bytes or 32 bytes depending on whether it is single-channel or dual-channel. It usually follows the header and contains important information of the frame that will be used for further decoding steps including scalefactor decoding, Huffman decoding, requantization and etc.

In the main data of MPEG-1 Layer III file, there are two granules for each channel while in MPEG-2 Layer III file there is only one for each channel. Our program is just for decoding the MPEG-1 Layer III files. (It means in MPEG-1 if the frame is single-channel it has two granules while if the frame is dual-channel it has four granules) Each granule is made up of two parts: scalefactor data and Huffman data. The Huffman data is actually the encoded PCM waveform samples and scalefactor is used for requantization. Huffman data of each granule contains data of 576 values which is evenly divided as 32 subbands.

What also worth mentioned is that a granule can be classified as a long block granule, a short block granule or a mixed block granule (a mixed block contains both long block parts and short block parts). The information of block type is stored in the side information. In long blocks, 576 values stands for 576 frequency lines while in short blocks, they stands for 192 frequency lines. Different block types also divide these 576 values into different number of scalefactor bands. Scalefactor band is a different concept from subband and it is used in Huffman decode and requantization. And unlike subband, scalefactor bands of one granule may have different number of values. Moreover, there are a lot of differences in the future decoding step with different type of granules.

Ancillary data can be in or not in a frame, which can be defined by the user and it follows the Huffman data.

3.2 Read Header and Side Information

To decode a frame of MP3 file, the first step is to read in and translate the header and side information. The algorithm locates a frame by searching the sync-word. Usually more than one sync-word is checked to make sure it is really a MP3 file. After locating a frame header, the program then can read in the header and side information.

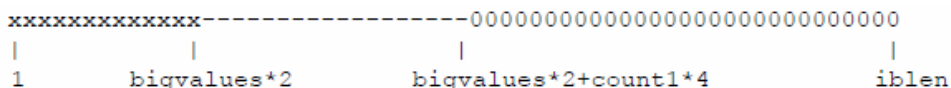
We also need to calculate the frame length since frame length is the distance between the headers of current frame and consecutive frame.

3.2 Scalefactor Decoding

As mentioned in the above, each granule contains scalefactor data and Huffman data. Also there are three block types for granule and different block type define different division of scalefactor bands of each granule. The bit lengths of scalefactor of different scalefactor bands and different block type granules are different. The program use *scalefactor_compress*, which is in the side information to check a predefined table, the data of which are the bit lengths of scalefactor of each scalefactor band. Then according to the lengths in the table, the program can read in the scalefactor from the bit stream of main data for each subband of the granule. We also have to calculate the length of whole length of scalefactor data of a granule.

3.3 Huffman Decoding

The Huffman decoding process is complicated in MP3 decoding. There are many tables generated by encoding for the reason of the encoding efficiency. So when decoding transition happens from one scalefactor band to another, the table may be changed. The 576 values are divided into three parts. What's more, the big value region can be divided into three different regions. (Big value of each frame is stored in the side information) Different parts use different tables to decode. When decoding the big-values, every entry returns two values. When in the small value decoding, every entry return four values. All the rest values are zeros.



The values 000 are all zero.
The values --- are -1,0 or +1. Their number is a multiple of 4.
The values xxx are not bound.
Ibilen is 576.

3.4 Requantization and Reordering

Quantization is the process of converting a real number with almost infinite precision into an integer number and it is applied in the MP3 encoding. Therefore in decoding we have to do the reverse. The Huffman data of 576 values should be requantized. The formulas of requantization of long blocks and short blocks are slightly different, which are in the below :

$$xr_i = \text{sign}(is_i) * |is_i|^{\frac{4}{3}} * 2^{\frac{1}{4}} (global_gain[gr] - 210 - 8 * subblock_gain[window][gr]) * 2^{-(scalefac_multiplier * scalefac_s[gr][ch][sfb][window])}$$

Fig.6 Requantization Formula for Short Block

$$xr_i = \text{sign}(is_i) * |is_i|^{\frac{4}{3}} * 2^{\frac{1}{4}} (global_gain[gr] - 210) * 2^{-(scalefac_multiplier * (scalefac_l[sfb][ch][gr] + preflag[gr] * pretab[sfb]))}$$

Fig.7 Requantization Formula for long Block

The parameters in the formula are in the side information or can be check in a predefined table. And notice that scalefactor is used in requantization.

Also if the data are in short block type granule or in the short block part of the mixed block type granule, the frequency line should be reordered.

3.5 Stereo Processing

The two channels of typical stereo signal are not independent and joint stereo tries to exploit the existing similarities. Joint stereo processing is complicated because short blocks are handled differently than long blocks. Also, granules can contain a mixture of long and short blocks and the bands in the granule can be combined with different stereo modes.

3.6 Alias reduction

Alias reduction is for long block types. It is to reduce the aliasing that is introduced by the use of ideal non-band pass filter. Alias reduction is done by eight butterfly calculation for each subband. The coefficients for the butterfly calculation are calculated using the values in a predefined table.

3.7 IMDCT

IMDCT is short for inversed modified discrete cosine transform. There are four types of discrete cosine transform (DCT). Modified discrete cosine transform (MDCT) is a Fourier-related transform based on the type-IV DCT, with the additional property of being lapped: it is designed to be performed on consecutive blocks of a larger dataset, where subsequent blocks are overlapped so that the last half of one block coincides with the first half of the next block. MDCT is widely used in signal processing including MP3 encoding. The IMDCT is the inversed computing of MDCT. IMDCT takes in 18 or 6 frequency lines (long or short block) of each subband and generate 36 outputs. The formula is in the below:

$$x(i) = \sum_{k=0}^{(n/2)-1} X(k) \cos\left(\frac{\pi}{2n}\left(2i+1+\frac{n}{2}\right)(2k+1)\right).$$

Fig.8 Formula of IMDCT in MP3 Decoding

These output samples are multiplied with a 36-point window before they can be passed on to the next step in the decoding process. Windowing contains four different types of windows namely, normal, short, start and stop. Information on what type to use is found in the side information part of each frame.

Then the samples also have to be divided into two halves and the first half of which will be overlapped with the second half of the previous block since MDCT is overlapping the data.

3.8 Subband Synthesis

This is the last part of MP3 decoding. It is in charge of turning the IMDCT outputs into final PCM samples. Subband synthesis has five sub-steps: First do matrix computing, taking one of the 18 values of each subband, which in total 32 values as input and receive 64 outputs; then putting these outputs into a FIFO buffer; take half of them and form a 512-element vector; window the vector by 512 coefficients; finally overlap these results and generate PCM output. The flowchart of subband synthesis is below:

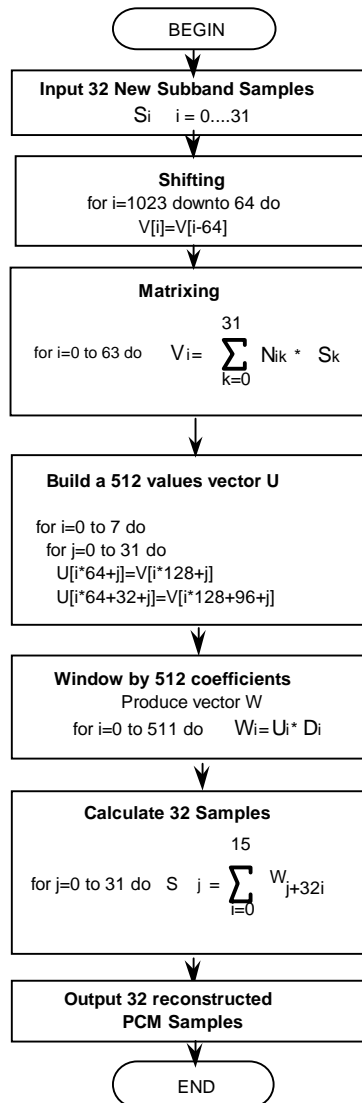


Fig.9 Flow chart of Subband Synthesis

IV. Software Analysis and Optimization

4.1 Observation and Analysis

When we first implement our MP3 decoder on the DE2 board, we found the decoding speed is extremely slow. It is more than 20 minutes per frame decoding. We then set the Nios II to higher version Nios II/f. Then the decoding is about 4 times faster than before but still too slow. We use timer to monitor which functions consumes most time.

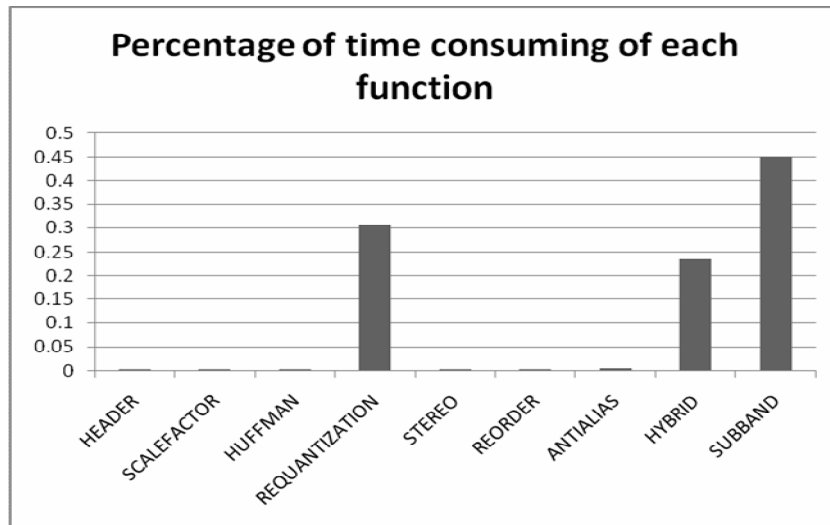


Fig.10 The percentage of time consuming of each function

We found the function “ requantization” , “hybrid” and “ subband” takes most of the decoding time. We analyzed that the speed is limited by the floating point operation and mathematic operation (such as power, sin, cos). Since up to 100 thousand times of floating point multiplication and, up to 1 thousand times of mathematic operations (power, sin, cos) are involved in decoding process in each frame, manual optimization of these operations instead of using the standard c library will be expected to gain a huge boost in decoding speed.

4.2 Optimization

4.2.1 Look-up tables

The operation of power as well as sin and cos consume a lot of time when decoding MP3. Since the variable may be in a certain range, we can calculate the possible values first and put them in tables.

For example:

```
xr[sb][ss] = pow( 2.0 , (0.25 * (gr_info->global_gain - 210.0)));
```

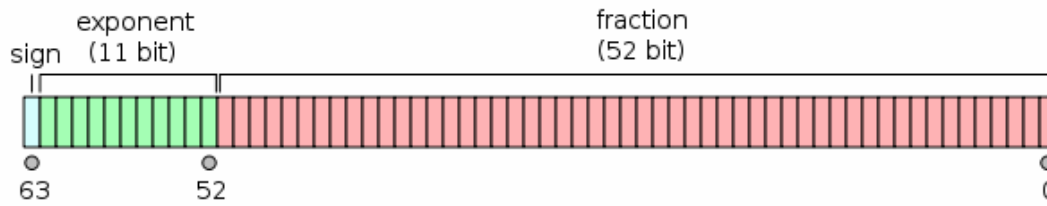
gr_info->global_gain is 8 bits. So we can calculate all the 256 possible values first and generate the table for this power operation.

After that, we can use the variable to loop up the table. In this way, we significantly accelerate the operation of power, sin, cos . The data is in the following section.

4.2.2 Custom floating point operation function

Since the double -precision floating point operations on the DE2 board are extremely slow, we design function using bit operation to do the multiplication, addition/subtraction of double -precision floating point.

As a fundamental, we introduce IEEE 754 double -precision binary format first:



$$(-1)^{\text{signbit}} \times 2^{\text{exponentbits}-1023} \times 1.\text{fractions}$$

The above is the format of double -precision floating point stored in the memory. Our idea is to extract all the components (sign, exponent, and fraction) and then use long integers and bit operations to do the floating point operation.

For example, we can design the double -precision floating point multiplication in this ways.

$$A = (-1)^{\text{signbit}_A} \times 2^{\text{exps}_A-1023} \times 1.\text{fractions}_A$$

$$= (-1)^{\text{signbit}_A} \times 2^{\text{exps}_A-1078} \times (0x1FFFFFFFFFFFFFFF | | \text{fractions}_A)$$

$$B = (-1)^{\text{signbit}_B} \times 2^{\text{exp}_B-1023} \times 1.\text{fractions}_B$$

$$= (-1)^{\text{signbit}_B} \times 2^{\text{exps}_B-1078} \times (0x1FFFFFFFFFFFFFFF | | \text{fractions}_B)$$

We use the long integer multiplication to calculate the fraction part of the result. And the exponent of the result need to contain a new_exp offset corresponding to the new_fractions, because of the fraction occupying 52 bits in the memory.

$$A * B = (-1)^{\text{signbit}_A + \text{signbit}_B} \times 2^{\text{exp}_A + \text{exp}_B - 2046 + \text{new_exp}} \times 1.\text{new_fractions}$$

We can use integer to calculate the sign bit and exponents of the result. After that , we use bit operation to combine them into IEEE 754 format and make coercion transformation into floating point to get returned.

Pseudo codes for floating point addition and multiplication are shown in Appendix.

4.2.3 Custom floating point operation Instruction

A more efficient way to alleviate the heavy computation task for floating point can be realized by introducing custom floating point instruction under the environment of SOPC. After NIOS-II is configured with this floating point computation instruction, the compiler will automatically generates appropriate assembly codes with this instruction, thus avoiding using the standard C library and hence yield great enhancement in the decoding speed.

4.3 Optimization Result

Table 2 and fig.11 shows the optimization result using the technique discussed above.

Table 2 The time used per frame's decoding

	Decoding time (s) per frame (timed by timer)	Decoding time (s) per frame (timed by watch)
Initial implement	-	1560
Un-optimized	46.7	300
Software Optz.	12.8	30
Hardware Optz.	0.58	1

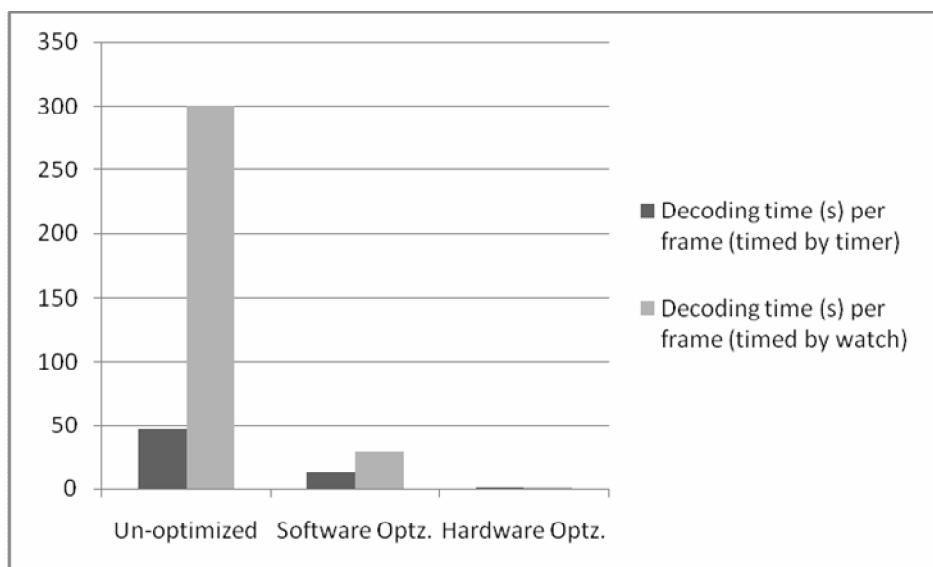


Fig.11 Speed up of optimization

The above graph shows the speed up of the optimization. The "un-optimized" column is the original implementation with setting to Nios II/f. The "software optz." column is the optimization including look-up tables and custom floating point optimization. The last column is the optimization of the custom instruction for Nios II Processor.

V. Conclusion

After understanding the principle of mp3 decoding, we realize the bottleneck of the decoding process lies in heavy computation task of floating point. A few methods are attempted to alleviate the problems, including switching to faster processor, using look-up table, and improving the algorithms of floating point multiplication. At the end of the design, we are able to execute the MP3 decoding process at the speed of 0.6 second per frame, which is roughly 1500 times faster than the original version, while still producing the same PCM outputs compared with the results generated on Dell server.

However, the weakness of our algorithms is the overemphasis of accuracy. Each optimization we attempted is based on the motivation of lossless decoding. In reality the audio quality and hence the accuracy may not be that important under portable environment. Thus future efforts can be put into using fixed point computation instead of floating point.

VI. Appendix

6.1 hello_world.c

```
#define TESTFRAME 800

#include "basic_io.h"
#include "basic.h"
#include "header_reader.h"
#include "get_scalefactor.h"
#include "requantize.h"
#include "huffman_decode.h"
#include "reorder.h"
#include "joint_stereo.h"
#include "antialias.h"
#include "hybrid.h"
#include "subbandsynthesis.h"
#include "math.h"
#include "alt_types.h"
#include "io.h"
#include "system.h"
#include "stdio.h"

#define IOWR_LED_DATA(base, offset, data) \
    IOWR_16DIRECT(base, (offset) * 2, data)
#define IORD_LED_DATA(base, offset) \
    IORD_16DIRECT(base, (offset) * 2)
#define IOWR_LED_SPEED(base, data) \
    IOWR_16DIRECT(base + 32, 0, data)

unsigned long F32ToHex(float valueF)
{
    unsigned long* valueL = (unsigned long *) &valueF;
    return *valueL;
}

inline int gen_wave32 (float flt)
{
    unsigned long  hex=0;
    unsigned int  exp,outcome;
    unsigned long  fraction;
    unsigned char sign_whole;
    unsigned char sign_exp;
    unsigned long  result;
    unsigned long  result2;
    hex=F32ToHex(flt);
    fraction=hex&0x007FFFFFFF;
    exp=127-((hex&0x7F800000)>>23);
```

```

//sign_exp=((exp&0x800)>>11);
//exp=exp&0x7FF;
sign_whole=(hex&0x80000000)>>31;
fraction=fraction|0x00800000;

    result=(fraction<<7)>>(exp-1);

result&=0x00FF0000;
result>>=16;
// outcome=result;
return result;
}

```

```
void main()
```

```

{
    unsigned char *address_of_first_frame;
    unsigned char *index;
    unsigned char valid[2]={1,1};
    unsigned long int syncword_count=0;
    unsigned char ii=0; //This variable is used to denote the number of frame that are being analyzed.
    int clip=0;
    int vga,vga_temp;

    printf("OK start.\n");//test;
    find_the_first_frame(&address_of_first_frame,&syncword_count);
    frame_stat=0;//test;
    syncword_count=syncword_count-2;
    block_type_0_count=0;//test;
    //printf("address_of_first_frame is %d\n",address_of_first_frame - data_header);//test;
    index=address_of_first_frame;
    si[0].main_data_end=0;si[1].main_data_end=0;

    initialize_huffman();

    for(frameNum=1;frameNum<TESTFRAME+1;frameNum++)
    {

        ii++;
        if(ii>1) ii=0;

        header_reader(&index,&syncword_count,&header_info[ii],&si[ii],&si[bit_add(ii)],&main_data_beg_bs[ii],&valid[ii]);
        /*if((valid[1]==1)&&(valid[0]==1))
        {
            //printf("I have found the %dst frame starting at this address:%d\n",frameNum,index-header_info[ii].frame_length-data_header);
        }
        else
        {
            //printf("bad frame detected.I quit.\n");exit(0);
        }
        */
    }
}

```



```

int ch,gr;
for(gr=0;gr< grNum;gr++)
{gr_global=gr;
for(ch=0;ch< stereo;ch++)
{
    get_scale_factors(&si[ii],&scalefac,gr,ch,main_data_beg_bs[ii]);
    //printf("start of huffman\n");
    huffman_decode(is, &si[ii], ch, gr,&header_info[ii],&main_data_beg_bs[ii]);

    main_data_beg_bs[ii].byte_idx_ptr+=(si[ii].ch[ch].gr[gr].part2_3_length/8);
    if(main_data_beg_bs[ii].bit_idx>=(si[ii].ch[ch].gr[gr].part2_3_length%8)
    {
        main_data_beg_bs[ii].bit_idx-=(si[ii].ch[ch].gr[gr].part2_3_length%8);
    }
    else
    {
        main_data_beg_bs[ii].byte_idx_ptr++;
        main_data_beg_bs[ii].bit_idx=main_data_beg_bs[ii].bit_idx+8-si[ii].ch[ch].gr[gr].part2_3_length%8;
    }
    //printf("start of requantize\n");

    requantize(&header_info[ii],&scalefac,&si[ii],is,ro[ch],gr,ch);
    //printf("end of requantize\n");
    //print_scalefac(frameNum, gr, ch, si[ii]);//test;
    //print_requantize(frameNum,gr,ch,ro[ch]);//test;

}
//printf("start of stereo\n");
join_stereo(ro,lr,&scalefac,&(si[ii].ch[ch].gr[gr]),&header_info[ii]);

for(ch=0;ch< stereo;ch++)
{
    ch_global=ch;
    reorder (lr[ch],re,&(si[ii].ch[ch].gr[gr]),&header_info[ii]);
    antialias(re, hybridIn, &(si[ii].ch[ch].gr[gr]), &header_info[ii]);

    for(vga=0;vga<7;vga++){
        vga_temp=(vga<<3)+vga;
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp,350-gen_wave32(hybridIn[vga][1]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+1,350-gen_wave32(hybridIn[vga][3]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+2,350-gen_wave32(hybridIn[vga][5]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+3,350-gen_wave32(hybridIn[vga][7]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+4,350-gen_wave32(hybridIn[vga][9]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+5,350-gen_wave32(hybridIn[vga][11]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+6,350-gen_wave32(hybridIn[vga][13]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+7,350-gen_wave32(hybridIn[vga][15]));
        IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, vga_temp+8,350-gen_wave32(hybridIn[vga][17]));
    }
    IOWR_LED_DATA(DE2_VGA_RASTER_INST_BASE, 63,350-gen_wave32(hybridIn[7][7]));

    unsigned char sb=0;
    unsigned char ss=0;
    for (sb=0; sb<SBLIMIT; sb++)

```

```

        {
            hybrid(hybridIn[sb], hybridOut[sb], sb, ch, &(si[ii].ch[ch].gr[gr]),&header_info[ii]);
        }
//printf("done.\n");
for (ss=0;ss<18;ss++)
    for (sb=0; sb<SBLIMIT; sb++)
        if ((ss%2) && (sb%2))
            hybridOut[sb][ss] = -hybridOut[sb][ss];

//print_hybrid_output(frameNum,gr,ch,hybridOut);//test;
//printf("synthesis start\n");
for (ss=0;ss<18;ss++)
{
    ss_global=ss;
    for (sb=0; sb<SBLIMIT; sb++)
        polyPhaseIn[sb] = hybridOut[sb][ss];
    SubBandSynthesis (polyPhaseIn, ch,&(pcm_output[frameNum-1][gr_global][ch_global][ss_global][0]));
}
//printf("synthesis done\n");
}

//print_pcm_sample(frameNum,gr,pcm_sample);//test;

//file_output(pcm_sample,fp,&sample_frames);
}
    printf("%d ",frameNum);
}
printf("done\n");

for(frameNum=0;frameNum<TESTFRAME;frameNum++)
    {
//printf("\nframe%d\n",frameNum);
for(gr_global=0;gr_global<2;gr_global++)
    {
//printf("\ngr%d\n",gr_global);
for(ch_global=0;ch_global<stereo;ch_global++)
    {
//printf("\nchannel%d\n",ch_global);
for (ss_global=0;ss_global<18;ss_global++)
    {
//printf("\nss%d\n",ss_global);
for(pcm_cnt_global=0;pcm_cnt_global<SBLIMIT;)
    {
        if(!IORD_16DIRECT(0x00681104,0))
        {
            usleep(15);
//            short monitor;
//            monitor=pcm_output[frameNum][gr_global][ch_global][ss_global][pcm_cnt_global];
//            printf(" %d ",pcm_output[frameNum][gr_global][ch_global][ss_global][pcm_cnt_global]);
            IOWR_16DIRECT(0x00681104,0,(unsigned
short)pcm_output[frameNum][gr_global][ch_global][ss_global][pcm_cnt_global]);
            pcm_cnt_global+=2;
        }
    }
    }
}
}
}
}
}

```

```

}
//-----test information print out-----;
/* printf("Frame_stat is %d\n",frame_stat);printf("bad_block_count is %d\n",bad_block_count);
printf("big_value_0_count is %d\n",big_value_0_count);
printf("big_value_1_count is %d\n",big_value_1_count);
printf("block_type_0_count is %d\n",block_type_0_count);*/
//printf("index taken by next circulation is %d\n",index);
//printf("ptr in main_data_bs structure, bit_idx is %d, byte_idx(offset) is %d.\n", main_data_bs.bit_idx, (unsigned long
int)(main_data_bs.byte_idx_ptr-data_header));printf("\n");}
//byte_idx tells you the location of maindata in forms of offset from the starting point of data_header
/*printf("ht[33] info :-----\n");
{
printf("table name is %s" ,ht[33].tablename);
printf("xlen is:%d ",ht[33].xlen);
printf("ylen is:%d ",ht[33].ylen);
printf("lintbis is:%d ",ht[33].linbits);
printf("treelen is:%d ",ht[33].treelen);
printf("the first 2 values are:0x%x 0x%x ",ht[33].val[0][0],ht[33].val[0][1]);
printf("the second 2 values are:0x%x 0x%x ",ht[33].val[1][0],ht[33].val[1][1]);
printf("the third 2 values are:0x%x 0x%x \n",ht[33].val[2][0],ht[33].val[2][1]);

}*/
}

```

6.2 basic.h

```

#include <stdio.h>
#include <stdlib.h>

#include "dataheader.h"
#include "math.h"

#define get1bit(bs) getbits(bs,1)
#define SBLIMIT 32
#define SSLIMIT 18
#define PI 3.14159265358979
unsigned int frameNum=0;//use to count the frame
unsigned int pcm_cnt_global;
unsigned char ss_global=0;
unsigned char ch_global=0;
unsigned char gr_global=0;
short pcm_output[TESTFRAME][2][2][SSLIMIT][SBLIMIT];
//-----Header_Info Definition-----;
typedef struct
{
unsigned char ID;//1 bit
unsigned char layer;//2 bit
unsigned char protection_bit;//1 bit. If it is 1, CRC check exists. CRC is 16-bit long and follows frame header.
unsigned int bitrate_index;//4 bit
unsigned int sampling_frequency_index;//2 bit
unsigned char pading_bit;//1 bit
unsigned char private_bit;//1 bit
unsigned char mode;//2 bit.00:Stero,11:Joint Stero,10 Dual Channel,01 Single Channel
unsigned char mode_extension;//2 bit

```

```

unsigned char copy_right;//1 bit
unsigned char original_home;//1 bit
unsigned char emphasis;//2 bit
unsigned int frame_length;

}header_info_type;

//-----Frame_Info Definition-----;
unsigned char stereo=0;
unsigned char grNum=2;

//-----Side_Info Definition-----;
typedef struct
{
    unsigned main_data_end;//9bits
    unsigned private_bits;//5bits
    struct {
        unsigned scfsi[4];//4bits
        struct gr_info_type {
            unsigned part2_3_length;//12bits
            unsigned big_values;//9bits
            unsigned global_gain;//8bits
            unsigned scalefac_compress;//4bits
            unsigned window_switching_flag;//1bits
            unsigned block_type;//2bits
            unsigned mixed_block_flag;//1bit
            unsigned table_select[3];//5 bits each
            unsigned subblock_gain[3];//3 bits each
            unsigned region0_count;//4bits
            unsigned region1_count;//3bits
            unsigned preflag;//1bits
            unsigned scalefac_scale;//1bits
            unsigned count1_table_select;//1bits
        } gr[2];
    } ch[2];
}side_info_type;

//-----Scalefactor Definition-----;
typedef struct
{
    struct {
        int l[22];        // [cb]
        int s[3][13];    // [window][cb]
    } ch[2];
} scalefac_type;

//-----Bit Stream Definitions & Functions-----;

typedef struct bit_stream_struct {
    unsigned char *byte_idx_ptr; /* pointer to current byte in the array */
    char bit_idx; /* pointer to leftmost bit of current byte */
                /* the bit_idx for leftmost bit =7*/
} bit_stream_struct;

```

```
unsigned char mask[8]={0x1, 0x2, 0x4, 0x8, 0x10, 0x20, 0x40, 0x80};
```

```
void bs_stucture_init(bit_stream_struct *bs)
```

```
{  
    bs->byte_idx_ptr=data_header;  
    bs->bit_idx=7;  
}
```

```
unsigned long int getbits(bit_stream_struct *bs,unsigned long n) /*read n bits*/
```

```
{  
    unsigned long int val=0;  
    while(n!=0)  
    {  
        val=(((*(bs->byte_idx_ptr)&mask[bs->bit_idx])>>bs->bit_idx)<<(n-1))|val;  
        bs->bit_idx=bs->bit_idx-1;  
        if(bs->bit_idx<0) {bs->bit_idx=7;bs->byte_idx_ptr++;}  
        n--;  
    }  
    return val;  
}
```

```
unsigned char bit_add(unsigned char i)
```

```
{  
    i=i+1;  
    if(i>1) i=0;  
    return i;  
}
```

```
//-----File Output Functions-----;
```

```
void SwapBytesInWords( short *loc, int words )
```

```
{  
    int i;  
    short thisval;  
    char *dst, *src;  
    src = (char *) &thisval;  
    for ( i = 0; i < words; i++)  
    {  
        thisval = *loc;  
        dst = (char *) loc++;  
        dst[0] = src[1];  
        dst[1] = src[0];  
    }  
}
```

```
void file_output(pcm_sample,outFile,psampFrames)
```

```
short pcm_sample[2][SSLIMIT][SBLIMIT];
```

```
FILE *outFile;
```

```
unsigned long *psampFrames;
```

```
{  
    int i,j,l;  
    static short int outsamp[1600];  
    static long k = 0;
```

```

for (i=0;i<SSLIMIT;i++)
  for (j=0;j<SBLIMIT;j++)
  {
    (*psampFrames)++;
    for (l=0;l<stereo;l++)
    {
      if (!(k%1600) && k)
      {
        //Samples are big-endian. If this is a little-endian machine we must swap
        SwapBytesInWords( outsamp, 1600 );
        fwrite(outsamp,2,1600,outFile);
        k = 0;
      }
      outsamp[k++] = pcm_sample[l][i][j];
    }
  }
}

//-----Tables&Preset Data-----;

int slen[2][16] = {{0, 0, 0, 0, 3, 1, 1, 1, 2, 2, 2, 3, 3, 3, 4, 4},
                  {0, 1, 2, 3, 0, 1, 2, 3, 1, 2, 3, 1, 2, 3, 2, 3}};

struct {
  int l[23];
  int s[14];
} sfBandIndex[6] =
  {{{0,6,12,18,24,30,36,44,54,66,80,96,116,140,168,200,238,284,336,396,464,522,576},
    {0,4,8,12,18,24,32,42,56,74,100,132,174,192}},
  {{0,6,12,18,24,30,36,44,54,66,80,96,114,136,162,194,232,278,330,394,464,540,576},
    {0,4,8,12,18,26,36,48,62,80,104,136,180,192}},
  {{0,6,12,18,24,30,36,44,54,66,80,96,116,140,168,200,238,284,336,396,464,522,576},
    {0,4,8,12,18,26,36,48,62,80,104,134,174,192}},

  {{0,4,8,12,16,20,24,30,36,44,52,62,74,90,110,134,162,196,238,288,342,418,576},
    {0,4,8,12,16,22,30,40,52,66,84,106,136,192}},
  {{0,4,8,12,16,20,24,30,36,42,50,60,72,88,106,128,156,190,230,276,330,384,576},
    {0,4,8,12,16,22,28,38,50,64,80,100,126,192}},
  {{0,4,8,12,16,20,24,30,36,44,54,66,82,102,126,156,194,240,296,364,448,550,576},
    {0,4,8,12,16,22,30,42,58,78,104,138,180,192}}};

//-----Global Data-----;

bit_stream_struct main_data_beg_bs[2];
header_info_type header_info[2]; //header_info[0] store the side information for previous frame.
                                //header_info[1] store the side information for current frame.
side_info_type si[2]; //si[0] store the side information for previous frame.
                    //si[1] store the side information for current frame.

scalefac_type scalefac;
long int is[SBLIMIT][SSLIMIT];
float xr[SBLIMIT][SSLIMIT];
float lr[2][SBLIMIT][SSLIMIT];

```

```
float ro[2][SBLIMIT][SSLIMIT];
float re[SBLIMIT][SSLIMIT];
float hybridIn[SBLIMIT][SSLIMIT];/* Hybrid filter input */
float hybridOut[SBLIMIT][SSLIMIT];/* Hybrid filter out */
float polyPhaseIn[SBLIMIT]; /* PolyPhase Input. */
short pcm_sample[2][SSLIMIT][SBLIMIT];
```

```
//typedef short PCM[2][SSLIMIT][SBLIMIT];
//PCM *pcm_sample;
```

6.3 header_reader.h

```
//-----test variables-----;
```

```
unsigned int frame_stat=0;
unsigned int bad_block_count=0;
unsigned char bad_block_type_flag=0;
int big_value_0_count=0;
int big_value_1_count=0;
long int block_type_0_count=0;
//char window_switching_flag_test=0;
```

```
//-----Bitrate_table-----;
```

```
unsigned int bitrate_table[2][4][16]=
{
{
{0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0},//blank for 1,0,0
{0,32,48,56,64,80,96,112,128,144,160,176,192,224,256,0},//table for 1,3,0
{0,8,16,24,32,40,48,56,64,80,96,112,128,144,160,0},//table for 1,2,0
{0,8,16,24,32,40,48,56,64,80,96,112,128,144,160,0},//table for 1,1,0
},
{
{0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0},//blank for 0,0,0
{0,32,64,96,128,160,192,224,256,288,320,352,384,416,448,0},//table for 0,1,0
{0,32,48,56,64,80,96,112,128,160,192,224,256,320,384,0},//table for 0,2,0
{0,32,40,48,56,64,80,96,112,128,160,192,224,256,320,0},//table for 0,3,0
}
};
```

//Note:

```
//first index refers to MPEG version. 1 -> MPEG1, 0 -> MPEG2 .Agree with definition in frame header
//Second index refers to layer number. Agree with definition in frame header.01->layer3,10->layer2,11->layer1.
//Third refers to birate index.
/*birate[0][1][]={0,32,64,96,128,160,192,224,256,288,320,352,384,416,448,0},//layer 3, MPEG 1
birate[0][2][]={0,32,48,56,64,80,96,112,128,160,192,224,256,320,384,0},//layer 2, MPEG 1
birate[0][3][]={0,32,40,48,56,64,80,96,112,128,160,192,224,256,320,0},//layer1, MPEG 1
birate[1][1,2][]={0,8,16,24,32,40,48,56,64,80,96,112,128,144,160,0},//layer 3 & layer 2, MPEG2
birate[1][3][]={0,32,48,56,64,80,96,112,128,144,160,176,192,224,256,0},//Mpeg 2 layer 1
{0,0,0,0,0,0,0,0,0,0,0,0,0,0,0,0}<-This is for blank table*/
```

```
//-----Sample_rate_table-----;
```

```
unsigned int sample_rate_table[3][4]=
{
```

```

{22050,24000,16000,0},
{44100,48000,32000,0},
{11025,12000,8000,0}
};
//Note:1.first index refers to MPEG version. 1 -> MPEG1, 0 -> MPEG2, 3 -> MPEG2.5
/*sample_rate_table[0]={44100,48000,32000,0};//MPEG1
sample_rate_table[1]={22050,24000,16000,0};//MPEG2
sample_rate_table[1]={11025,12000,8000,0};//MPEG2.5*/

/*****Function:Header_Reader Definition Starts*****/
/**Function Parameter Distription:"Index" is a pointer referring the position from which the function starts to search for
syncword."FrameCount" is used to document the number of frames in the fils."*header_info" and "*si" is a pointer used by the function to
document the header infomation and side infomation.Finally, when the function is finished, the location of the starting point of main data
is stored in "main_data_bs"*/
/**Function Return Distription: This function return a parameter that tells the location of next frame.This location is caculated based on the
infomarion found in the header**/

inline header_reader(unsigned char **index,
                    unsigned long int *syncword_count,
                    header_info_type *header_info,
                    side_info_type *si_current,
                    side_info_type *si_previous,
                    bit_stream_struct *main_data_beg_bs,
                    unsigned char *valid)
{
    int ch, gr, i;
    unsigned char *starting_index;//this variable is used to locate the postition of syncword in the current frame
    unsigned char *side_info_start_ptr;
    unsigned char position_flag=0;//if 0, indicating the sync_word is in forms of "0x*F 0xFF".Else in the form of"0xFF 0xF*"
                                // ^-starting index          ^-start idx

    unsigned char flag=0;
    bit_stream_struct bs;

/*****Start analyzing header*****/
    while(!flag)
    {
        if((*index)==0xff)
        {
            if(((>(*index-1))&0x0f)==0x0f)
            {
                flag=1;
                starting_index=*index-1;
                position_flag=0;
            }
            if(((>(*index+1))&0xf0)==0xf0)
            {
                flag=1;
                starting_index=*index;
                position_flag=1;
            }
        }
        (*index)++;
    }
}

```



```

    }
    (*index)--;
    (*syncword_count)++;
// printf("\n find the %dst syncword offset is at %d, position_flag is %d.\n",
//      *syncword_count,(unsigned int)(starting_index-data_header),position_flag);
    if(position_flag==0)
    {
        bs.byte_idx_ptr=starting_index+2;
        bs.bit_idx=7;
        //printf("bs.byte_idx_ptr now is %d",bs.byte_idx_ptr-data_header);
    }
    else
    {
        bs.byte_idx_ptr=starting_index+1;
        //printf("bs.byte_idx_ptr now is %d",bs.byte_idx_ptr-data_header);
        bs.bit_idx=3;
    }
    header_info->ID = getbits(&bs,1);
    header_info->layer = 4-getbits(&bs,2);
    header_info->protection_bit = !get1bit(&bs); /* error protect. TRUE/FALSE */
    header_info->bitrate_index = getbits(&bs,4);
    header_info->sampling_frequency_index = getbits(&bs,2);
    header_info->pading_bit = get1bit(&bs);
    header_info->private_bit = get1bit(&bs);
    header_info->mode = getbits(&bs,2);
    header_info->mode_extension = getbits(&bs,2);
    header_info->copy_right = get1bit(&bs);
    header_info->original_home = get1bit(&bs);
    header_info->emphasis = getbits(&bs,2);

    if(header_info->protection_bit==0)
        side_info_start_ptr=starting_index+4;
    else
        side_info_start_ptr=starting_index+6;
    //printf("side_info_start_ptr is now %d",side_info_start_ptr-data_header);

    if(sample_rate_table[header_info->ID][header_info->sampling_frequency_index])
    {
header_info->frame_length=144*1000*bitrate_table[header_info->ID][header_info->layer][header_info->bitrate_index]/sample_rate_table[
header_info->ID][header_info->sampling_frequency_index]+header_info->pading_bit;
    }
    else header_info->frame_length=0;
    if((sample_rate_table[header_info->ID][header_info->sampling_frequency_index]==44100)
        &&(bitrate_table[header_info->ID][header_info->layer][header_info->bitrate_index]==320)
        &&(header_info->ID)
        &&(header_info->layer==3))
    {*valid=1;}
    else
    {*valid=0;goto LP;}
    /*****end of analyzing header*****/
//*****Start Analyzing Side Info*****/

```

```

bs.byte_idx_ptr=side_info_start_ptr;
stereo = (header_info->mode==0x3)?1:2;
if(position_flag==0)
{bs.bit_idx=3;}
else{bs.bit_idx=7;}
if(header_info->ID == 1)
{
    si_current->main_data_end = getbits(&bs, 9);
    if (stereo == 1)
        si_current->private_bits = getbits(&bs,5);
        else si_current->private_bits = getbits(&bs,3);

    for (ch=0; ch<stereo; ch++)
        for (i=0; i<4; i++)
            si_current->ch[ch].scfsi[i] = getbits(&bs,1);

    for (gr=0; gr< grNum ; gr++) {
        for (ch=0; ch<stereo; ch++) {
            si_current->ch[ch].gr[gr].part2_3_length = getbits(&bs, 12);
            si_current->ch[ch].gr[gr].big_values = getbits(&bs, 9);
            si_current->ch[ch].gr[gr].global_gain = getbits(&bs, 8);
            si_current->ch[ch].gr[gr].scalefac_compress = getbits(&bs, 4);
            si_current->ch[ch].gr[gr].window_switching_flag = getbits(&bs,1);
            if (si_current->ch[ch].gr[gr].window_switching_flag) {window_switching_flag_test=1;test;}
            si_current->ch[ch].gr[gr].block_type = getbits(&bs, 2);
            si_current->ch[ch].gr[gr].mixed_block_flag = getbits(&bs,1);
            for (i=0; i<2; i++)
                si_current->ch[ch].gr[gr].table_select[i] = getbits(&bs, 5);
            for (i=0; i<3; i++)
                si_current->ch[ch].gr[gr].subblock_gain[i] = getbits(&bs, 3);

            /* Set region_count parameters since they are implicit in this case. */

            if (si_current->ch[ch].gr[gr].block_type == 0) {
                printf("Side info bad: block_type == 0 in split block.\n");
                bad_block_count++;test;
                exit(0);
                goto LP;
            }
            else if (si_current->ch[ch].gr[gr].block_type == 2
                && si_current->ch[ch].gr[gr].mixed_block_flag == 0)
                si_current->ch[ch].gr[gr].region0_count = 8; /* MI 9; */
            else si_current->ch[ch].gr[gr].region0_count = 7; /* MI 8; */
            si_current->ch[ch].gr[gr].region1_count = 20 -
                si_current->ch[ch].gr[gr].region0_count;
        }
    }
    else {
        for (i=0; i<3; i++)
            si_current->ch[ch].gr[gr].table_select[i] = getbits(&bs, 5);
            si_current->ch[ch].gr[gr].region0_count = getbits(&bs, 4);
            si_current->ch[ch].gr[gr].region1_count = getbits(&bs, 3);
            si_current->ch[ch].gr[gr].block_type = 0;
    }
}

```

```

    }
    si_current->ch[ch].gr[gr].preflag = getbits(&bs,1);
    si_current->ch[ch].gr[gr].scalefac_scale = getbits(&bs,1);
    si_current->ch[ch].gr[gr].count1 table_select = getbits(&bs,1);
}
}
}
else /* Layer 3 LSF */
{
    exit(0);
}
}

```

```

if(si_previous->main_data_end==0)
{main_data_beg_bs->bit_idx=bs.bit_idx;
main_data_beg_bs->byte_idx_ptr=bs.byte_idx_ptr;}
else
{main_data_beg_bs->bit_idx=7;
main_data_beg_bs->byte_idx_ptr=starting_index-(si_previous->main_data_end)+1;}
//*****End Analyzing Side Info*****//
/*if((si->ch[ch].gr[gr].block_type != 0)
(sample_rate_table[header_info->ID][header_info->sampling_frequency_index]==44100)
&&(bitrate_table[header_info->ID][header_info->layer][header_info->bitrate_index]==192)
&&(!si->main_data_begin)
&&(frame_length==522)
&&(!header_info->ID)
&&(header_info->layer==3)
&&(si->main_data_begin==0))*/
{frame_stat++;//test;
//*****Start printing the header information*****//
/*if(sample_rate_table[header_info->ID][header_info->sampling_frequency_index]
{printf("frame_length is %d\n",header_info->frame_length);}
else
{printf("Sampling freq is 0.\n");goto LP;}
printf("Header information as follows:\n");
printf("ID is 0x%x ",header_info->ID);
if(header_info->layer) printf("layer is 0x%x ",header_info->layer); else printf("Wrong layer number!");
printf("protection_bit is 0x%x ",header_info->protection_bit);
printf("bitrate_index is 0x%x,bitrate is %d ", header_info->bitrate_index,
bitrate_table[header_info->ID][header_info->layer][header_info->bitrate_index]);
printf("sampling_freq_index is 0x%x,sampling_frequency is %d ",header_info->sampling_frequency_index,
sample_rate_table[header_info->ID][header_info->sampling_frequency_index]);
printf("padding_bit is 0x%x ",header_info->padding_bit);
printf("private_bit is 0x%x ",header_info->private_bit);
printf("mode_index is 0x%x ",header_info->mode);
if(header_info->mode==3) printf("Single Channel.SideInfo is 17 bits. ");
else if(header_info->mode==2) printf("Dual Channel. SideInfo is 32 bits. ");
else if(header_info->mode==1) printf("Joint Stero Mode. ");
else if(header_info->mode==0) printf("Stero Mode. ");
printf("mode_extension is 0x%x ",header_info->mode_extension);
printf("copy_right is 0x%x ",header_info->copy_right);
printf("original_home is 0x%x ",header_info->original_home);
printf("emphasis is 0x%x \n",header_info->emphasis);*/
//*****End of printing the header information*****//

```

```

//*****Start of printing Side Info*****//
/*
printf("Side Information as follows:\n");
printf("Main_data_End:%d ",si_current->main_data_end);
printf("Main_data_begin:%d ",main_data_beg_bs->byte_idx_ptr-data_header);
printf("Private_bits:%d ",si_current->private_bits);
for (ch=0; ch<stereo; ch++)
    for (i=0; i<4; i++)
        printf("channel[%d],scfsi[%d] is:%d ",ch,i,si_current->ch[ch].scfsi[i]);printf("\n");
for (gr=0; gr< grNum ; gr++) {
    for (ch=0; ch<stereo; ch++) {
        printf("Info of Granul %d,channel %d is as following: ",gr,ch);
        printf("part2_3_length:%d ",si_current->ch[ch].gr[gr].part2_3_length);
        printf("big_values:%d ",si_current->ch[ch].gr[gr].big_values);
if(si_current->ch[ch].gr[gr].big_values!=0) big_value_1_count++; else big_value_0_count++;//test;
        printf("global_gain:%d ",si_current->ch[ch].gr[gr].global_gain);
        printf("scalefac_compress:%d ",si_current->ch[ch].gr[gr].scalefac_compress);
        printf("window_switching_flag:%d ",si_current->ch[ch].gr[gr].window_switching_flag);
        if (si_current->ch[ch].gr[gr].window_switching_flag) {
            printf("block_type:0x%x ",si_current->ch[ch].gr[gr].block_type);
            if((si_current->ch[ch].gr[gr].block_type)==0) {block_type_0_count++;}//test;
            printf("mixed_block_flag:0x%x ",si_current->ch[ch].gr[gr].mixed_block_flag);
            for (i=0; i<2; i++)
                printf("table_select [%d]:%d ",i,si_current->ch[ch].gr[gr].table_select[i]);
            for (i=0; i<3; i++)
                printf("subblock_gain [%d]:%d ",i,si_current->ch[ch].gr[gr].subblock_gain[i]);
            printf("region0_count:%d ",si_current->ch[ch].gr[gr].region0_count);
            printf("region1_count:%d ",si_current->ch[ch].gr[gr].region1_count);
        }
        else {
            for (i=0; i<3; i++)
                printf("table_select [%d]:%d ",i,si_current->ch[ch].gr[gr].table_select[i]);
            printf("region0_count:%d ",si_current->ch[ch].gr[gr].region0_count);
            printf("region1_count:%d ",si_current->ch[ch].gr[gr].region1_count);
            printf("block_type:0x%x ",si_current->ch[ch].gr[gr].block_type);
            if((si_current->ch[ch].gr[gr].block_type)==0) block_type_0_count++;//test;
        }
        printf("scalefac_scale:%d ",si_current->ch[ch].gr[gr].scalefac_scale);
        printf("count1table_select:%d\n",si_current->ch[ch].gr[gr].count1table_select );
    }
}*/
//printf("\n");
//*****End of printing Side Info*****//
}
LP:;
if(header_info->frame_length)
*index=*index+header_info->frame_length;
else *index=*index+1;
//printf("\nindex now is %d\n",*index-data_header);
//printf("I do return!!\n");
//returning the position of next frame

```

```

}
/*****Header_Reader Definition Ends*****/

/*****Function:Find_the_first_frame*****/
/**input:address of data_header*****/
/**output:address of the first frame, the syncword offset of this frame,*****/
inline void find_the_first_frame
(unsigned char **address_of_first_frame,
 unsigned long int *syncword_count)
{
 unsigned char *index=&data_header;
 unsigned char *previous_frame_index;
 unsigned char *first_frame_index=0;
 unsigned char valid[2]={0,0};
 unsigned char first_time_valid=0;
 unsigned char i=0;
 bit_stream_struct main_data_bs[2];
 header_info_type header_info[2];
 side_info_type si[2];
 header_info[0].frame_length=0;
 header_info[1].frame_length=0;
 for(;;)
 {
  previous_frame_index=index-header_info[i].frame_length;
  i++;
  if(i>1) i=0;
  header_reader(&index,syncword_count,&header_info[i],&si[1],&si[0],&main_data_bs[i],&valid[i]);
  if((!first_time_valid)&&(valid[1])&&(valid[0]))
  {
   first_frame_index=previous_frame_index;
   (*address_of_first_frame)=first_frame_index;
   first_time_valid=1;
  }
  printf("First frame found.\n");
  printf("address_of_first_frame is %d\n",*address_of_first_frame-data_header);
  return;
 }
 }
}

```

6.4 huffman_decode.h

```

#define HUFFBITS unsigned long int
#define HTN 34
#define MXOFF 250
#include "huffarray.h"

HUFFBITS dmask = 1 << (sizeof(HUFFBITS)*8-1);

struct huffcodetab {
 char tablename[3]; /*string, containing table_description */
 unsigned int xlen; /*max. x-index+ */
 unsigned int ylen; /*max. y-index+ */
 unsigned int linbits; /*number of linbits */
 unsigned int linmax; /*max number to be stored in linbits */
 int ref; /*a positive value indicates a reference*/
}

```

```

HUFFBITS *table; /*pointer to array[xlen][ylen] */
unsigned char *hlen; /*pointer to array[xlen][ylen] */
unsigned char (*val)[2];/*decoder tree */
unsigned int treelen; /*length of decoder tree */
};
struct huffcodetab ht[HTN];

/*****huffman definition ends*****/
/* read the huffman decoder table */
inline int read_decoder_table()
{
    unsigned char tab_num_cnt=0;
    for(tab_num_cnt=0;tab_num_cnt<HTN;tab_num_cnt++)
    {
        ht[tab_num_cnt].tablename[2]='\0';
        ht[tab_num_cnt].tablename[1]=48+tab_header[tab_num_cnt][0]%10;
        ht[tab_num_cnt].tablename[0]=48+tab_header[tab_num_cnt][0]/10;
        ht[tab_num_cnt].treelen=tab_header[tab_num_cnt][1];
        ht[tab_num_cnt].xlen=tab_header[tab_num_cnt][2];
        ht[tab_num_cnt].ylen=tab_header[tab_num_cnt][3];
        ht[tab_num_cnt].linbits=tab_header[tab_num_cnt][4];
    }
    ht[0].val=v_tab_0;
    ht[1].val=v_tab_1;
    ht[2].val=v_tab_2;
    ht[3].val=v_tab_3;
    ht[4].val=v_tab_4;
    ht[5].val=v_tab_5;
    ht[6].val=v_tab_6;
    ht[7].val=v_tab_7;
    ht[8].val=v_tab_8;
    ht[9].val=v_tab_9;
    ht[10].val=v_tab_10;
    ht[11].val=v_tab_11;
    ht[12].val=v_tab_12;
    ht[13].val=v_tab_13;
    ht[14].val=v_tab_14;
    ht[15].val=v_tab_15;
    ht[16].val=v_tab_16;
    ht[17].val=v_tab_17;
    ht[18].val=v_tab_18;
    ht[19].val=v_tab_19;
    ht[20].val=v_tab_20;
    ht[21].val=v_tab_21;
    ht[22].val=v_tab_22;
    ht[23].val=v_tab_23;
    ht[24].val=v_tab_24;
    ht[25].val=v_tab_25;
    ht[26].val=v_tab_26;
    ht[27].val=v_tab_27;
    ht[28].val=v_tab_28;
    ht[29].val=v_tab_29;
    ht[30].val=v_tab_30;

```

```

    ht[31].val=v_tab_31;
    ht[32].val=v_tab_32;
    ht[33].val=v_tab_33;
    return 34;
}

/***** huffman initial function *****/
int huffman_initialized = 0;
void initialize_huffman() {
    read_decoder_table();
}

/***** huffman decoder function *****/

inline int huffman_decoder
(bit_stream_struct *main_data_beg_bs,
 struct huffcodetab *h,
 int *x, int *y, int *v, int *w)
{

    HUFFBITS level;
    int point = 0;
    int error = 1;
    int bit_used_count=0;
    level = dmask;
    //printf("1111111\n");
    if (h->val == NULL) return 2;
    //printf("tree legnth read by huffman_decoder is %d \n",h->treelen);
    /* table 0 needs no bits */
    if ( h->treelen == 0)
    {
        *x = *y = 0;
        return bit_used_count;
    }

    /* Lookup in Huffman table. */

    do {
        if (h->val[point][0]==0) { /*end of tree*/
            *x = h->val[point][1] >> 4;
            *y = h->val[point][1] & 0xf;
            //return bit_used_count;
            error = 0;

            break;
        }
        if (get1bit(main_data_beg_bs)) {
            while (h->val[point][1] >= MXOFF) point += h->val[point][1];
            point += h->val[point][1];
        }
    }
}

```

```

    bit_used_count++;
    //printf("\nDecoding Boss:main_data_beg_bs.byte_idx_ptr is %d\n",main_data_beg_bs->byte_idx_ptr-data_header);
    //printf("\nDecoding Boss:main_data_beg_bs.bit_idx %d\n",main_data_beg_bs->bit_idx);
}
else {
    while (h->val[point][0] >= MXOFF) point += h->val[point][0];
    point += h->val[point][0];
    bit_used_count++;
}
level >>= 1;
} while (level || (point < ht->treelen));
/* Check for error. */
if (error) { /* set x and y to a medium value as a simple concealment */
    printf("Illegal Huffman code in data.\n");
    *x = (h->xlen-1 << 1);
    *y = (h->ylen-1 << 1);
}
/* Process sign encodings for quadruples tables. */

if (h->tablename[0] == '3'
    && (h->tablename[1] == '2' || h->tablename[1] == '3')) {
    *v = (*y>>3) & 1;
    *w = (*y>>2) & 1;
    *x = (*y>>1) & 1;
    *y = *y & 1;

    /* v, w, x and y are reversed in the bitstream.
       switch them around to make test bistream work. */

/* {int i=*v; *v=*y; *y=i; i=*w; *w=*x; *x=i;} MI */

    if (*v)
    {
        bit_used_count++;
        if (get1bit(main_data_beg_bs) == 1) *v = -*v;
    }

    if (*w)
    {
        bit_used_count++;
        if (get1bit(main_data_beg_bs) == 1) *w = -*w;
    }
    if (*x)
    {
        bit_used_count++;
        if (get1bit(main_data_beg_bs) == 1) *x = -*x;
    }
    if (*y)
    {
        bit_used_count++;
        if (get1bit(main_data_beg_bs) == 1) *y = -*y;
    }
}

```



```

    return bit_used_count;
}

/* Process sign and escape encodings for dual tables. */

else {

    /* x and y are reversed in the test bitstream.
       Reverse x and y here to make test bitstream work. */

/*    removed 11/11/92 -ag
       {int i=*x; *x=*y; *y=i;}
*/
    printf("22221\n");
    if (h->linbits)
        if ((h->xlen-1) == *x)
            {*x += getbits(main_data_beg_bs,h->linbits); bit_used_count+=h->linbits;}
    if (*x)
    {
        bit_used_count++;
        if (get1bit(main_data_beg_bs) == 1) *x = -*x;
    }
    if (h->linbits)
        if ((h->ylen-1) == *y)
            {*y += getbits(main_data_beg_bs,h->linbits); bit_used_count+=h->linbits;}
    if (*y)
    {
        bit_used_count++;
        if (get1bit(main_data_beg_bs) == 1) *y = -*y;
    }
    return bit_used_count;
}
//main_data_beg_bs->byte_idx_ptr=byte_idx_ptr_temp_2;
//main_data_beg_bs->bit_idx=bit_idx_temp_2;
return error;
}

```

```

/***** mp3_Huffman decoder *****/

```

```

inline void huffman_decode
(long int is[SBLIMIT][SSLIMIT],
 side_info_type *si,
 int ch, int gr,
 header_info_type *header_info,
 bit_stream_struct *main_data_beg_bs)
{
    unsigned char *byte_idx_ptr_temp;
    unsigned char bit_idx_temp;

    byte_idx_ptr_temp = main_data_beg_bs->byte_idx_ptr;
    bit_idx_temp=main_data_beg_bs->bit_idx;
}

```

```

int i, x=1, y=1;
int v, w;
int bit_used=0;
struct huffcodetab *h;
h=malloc(sizeof(struct huffcodetab));
h=(struct huffcodetab *)h;

int region1Start;
int region2Start;
int samplefreq;
int currentBit, grBits;
int part2_length;
struct gr_info_type *gi;
//int bt = (*si).ch[ch].gr[gr].window_switching_flag && ((*si).ch[ch].gr[gr].block_type == 2);

gi = (struct gr_info_type *) &(*si).ch[ch].gr[gr];
samplefreq = header_info->sampling_frequency_index + (header_info->ID * 3);
/*calculate part2_length*/

if (/*gi->>window_switching_flag &&*/ (gi->block_type == 2)) {
    if (gi->mixed_block_flag)
    {
        part2_length=17*slen[0][gi->scalefac_compress]
            +18*slen[1][gi->scalefac_compress];

    }
    else {
        part2_length=18*slen[0][gi->scalefac_compress]
            +18*slen[1][gi->scalefac_compress];
    }
}
else { /* LONG types 0,1,3 */
    part2_length=11*slen[0][gi->scalefac_compress]
        +10*slen[1][gi->scalefac_compress];
}

}

/*
printf("The following is is serving as within one granual:\n");
printf("part2_length is %d\n",part2_length);*/
/*printf("main_data_beg_bs.byte_idx_ptr is %d\n",main_data_beg_bs->byte_idx_ptr-data_header);
printf("main_data_beg_bs.bit_idx %d\n",main_data_beg_bs->bit_idx);*/

/*if((ch==1)&&(gr==1)) main_data_beg_bs->byte_idx_ptr=main_data_beg_bs->byte_idx_ptr+part2_length/8-1;
else*/ main_data_beg_bs->byte_idx_ptr=main_data_beg_bs->byte_idx_ptr+part2_length/8;
if((main_data_beg_bs->bit_idx < (part2_length)%8))
{

    main_data_beg_bs->bit_idx=8-(part2_length)%8+main_data_beg_bs->bit_idx;
    main_data_beg_bs->byte_idx_ptr++;
}
else
main_data_beg_bs->bit_idx=main_data_beg_bs->bit_idx-((part2_length)%8);

```

```

/*printf("-----huffman coded data address for gr[%d],ch[%d] is as follows-----\n",gr,ch);
printf("row is %d,   ",(main_data_beg_bs->byte_idx_ptr-data_header)/39);
printf("column is %d.\n ",(main_data_beg_bs->byte_idx_ptr-data_header)%39);
printf("main_data_beg_bs.byte_idx_ptr is %d   ",main_data_beg_bs->byte_idx_ptr-data_header);
printf("main_data_beg_bs.bit_idx %d\n",main_data_beg_bs->bit_idx);
printf("-----huffman coded data address for gr[%d],ch[%d] Ends-----\n",gr,ch);*/

/* Find region boundary for short block case. */

if ( ((*si).ch[ch].gr[gr].window_switching_flag) &&
      ((*si).ch[ch].gr[gr].block_type == 2) ) {

    /* Region2. */
    region1Start = 36; /* sfb[9/3]*3=36 */
    region2Start = 576; /* No Region2 for short block case. */
}
else { /* Find region boundary for long block case. */

    region1Start = sfBandIndex[samplefreq]
                  .l[( *si).ch[ch].gr[gr].region0_count + 1]; /* MI */
    region2Start = sfBandIndex[samplefreq]
                  .l[( *si).ch[ch].gr[gr].region0_count +
                    ( *si).ch[ch].gr[gr].region1_count + 2]; /* MI */
}

/* Read bigvalues area. */
// printf("big value      is %d",(*si).ch[ch].gr[gr].big_values*2);

for (i=0; i<(*si).ch[ch].gr[gr].big_values*2; i+=2) {

    if (i<region1Start) h = &ht[( *si).ch[ch].gr[gr].table_select[0]];
    else if (i<region2Start) h = &ht[( *si).ch[ch].gr[gr].table_select[1]];
    else h = &ht[( *si).ch[ch].gr[gr].table_select[2]];
    //printf("tree legnth inside the fuction lll_huffman_decode is %d \n\n",h->treelen);
    bit_used+=huffman_decoder(main_data_beg_bs,h, &x, &y, &v, &w);
    is[i/SSLIMIT][i%SSLIMIT] = x;
    is[(i+1)/SSLIMIT][(i+1)%SSLIMIT] = y;
}

/* Read count1 area. */

h = &ht[( *si).ch[ch].gr[gr].count1table_select)+32];
/*printf("count1 table_select is %d\n",(*si).ch[ch].gr[gr].count1table_select)+32);*/
while ((bit_used < (( *si).ch[ch].gr[gr].part2_3_length - part2_length) &&
      ( i < SSLIMIT*SBLIMIT )) {

    bit_used+=huffman_decoder(main_data_beg_bs,h, &x, &y, &v, &w);
    is[i/SSLIMIT][i%SSLIMIT] = v;
    is[(i+1)/SSLIMIT][(i+1)%SSLIMIT] = w;
    is[(i+2)/SSLIMIT][(i+2)%SSLIMIT] = x;
    is[(i+3)/SSLIMIT][(i+3)%SSLIMIT] = y;
    i += 4;
}

```

```

/* Zero out rest. */
for (; i<SSLIMIT*SBLIMIT; i++)
    is[i/SSLIMIT][i%SSLIMIT] = 0;
/*printf("the 576 sampling value is the following:\n");*/
/*
for(x=0;x<SBLIMIT;x++)
{
    printf("subband %d :",x);
    for(y=0;y<SSLIMIT;y++)
        printf("%d ",is[x][y]);

    printf("\n");

}
*/

main_data_beg_bs->byte_idx_ptr=byte_idx_ptr_temp;
main_data_beg_bs->bit_idx=bit_idx_temp;
}

```

```

/*****huffman definition ends*****/

```

6.5get_scalfefactor.h

```

inline void get_scale_factors

```

```

(side_info_type *si,
 scalefac_type *scalefac,
 int gr,
 int ch,
 bit_stream_struct bitstream)

```

```

{
    int sfb, i, window;
    struct gr_info_type *gr_info = &(si->ch[ch].gr[gr]);
    bit_stream_struct *bs=&bitstream;

    if (gr_info->window_switching_flag && (gr_info->block_type == 2))
    {

```

```

        if (gr_info->mixed_block_flag)

```

```

        { // mixed blocks;

```

```

            for (sfb = 0; sfb < 8; sfb++)

```

```

                scalefac->ch[ch].l[sfb] = getbits(bs,slen[0][gr_info->scalefac_compress]);

```

```

            for (sfb = 3; sfb < 6; sfb++)

```

```

                for (window=0; window<3; window++)

```

```

                    scalefac->ch[ch].s[window][sfb] = getbits(bs,slen[0][gr_info->scalefac_compress]);

```

```

            for (sfb = 6; sfb < 12; sfb++)

```

```

                for (window=0; window<3; window++)

```

```

                    scalefac->ch[ch].s[window][sfb] = getbits(bs,slen[1][gr_info->scalefac_compress]);

```

```

            for (sfb=12,window=0; window<3; window++)

```

```

        scalefac->ch[ch].s>window][sfb] = 0;
    }
    else
    { // short blocks;
        for (sfb=0; sfb < 6; sfb++)
            for (window=0; window<3; window++)
                scalefac->ch[ch].s>window][sfb] = getbits(bs,slen[0][gr_info->scalefac_compress]);
        for (sfb=6; sfb < 12; sfb++)
            for (window=0; window<3; window++)
                scalefac->ch[ch].s>window][sfb] = getbits(bs,slen[1][gr_info->scalefac_compress]);
        for (sfb=12,window=0; window<3; window++)
            scalefac->ch[ch].s>window][sfb] = 0;
    }
}
else
{ // long blocks;
    for (sfb=0; sfb < 11; sfb++)
        scalefac->ch[ch].l[sfb] = getbits(bs,slen[0][gr_info->scalefac_compress]);
    for (sfb=11; sfb < 21; sfb++)
        scalefac->ch[ch].l[sfb] = getbits(bs,slen[1][gr_info->scalefac_compress]);
    scalefac->ch[ch].l[21] = 0;
}
}

```

6.7 requantize.h

```
int pretab[22] = {0,0,0,0,0,0,0,0,0,0,0,1,1,1,1,2,2,3,3,3,2,0};
```

```
float powershort1[8]={
    1.0000000000000000,0.2500000000000000,0.0625000000000000,0.0156250000000000,
    0.0039062500000000,0.0009765625000000,0.0002441406250000,0.0000610351562500,
};
```

```
float powershort2[32]={
    1.0000000000000000,0.7071067811865476,0.5000000000000000,0.3535533905932738,
    0.2500000000000000,0.1767766952966369,0.1250000000000000,0.0883883476483184,
    0.0625000000000000,0.0441941738241592,0.0312500000000000,0.0220970869120796,
    0.0156250000000000,0.0110485434560398,0.0078125000000000,0.0055242717280199,
    0.0039062500000000,0.0027621358640100,0.0019531250000000,0.0013810679320050,
    0.0009765625000000,0.0006905339660025,0.0004882812500000,0.0003452669830012,
    0.0002441406250000,0.0001726334915006,0.0001220703125000,0.0000863167457503,
    0.0000610351562500,0.0000431583728752,0.0000305175781250,0.0000215791864376,
};
```

```
float powertable1[256]={
    0.0000000000000002,0.0000000000000002,0.0000000000000002,0.0000000000000003,
    0.0000000000000003,0.0000000000000004,0.0000000000000004,0.0000000000000005,
    0.0000000000000006,0.0000000000000007,0.0000000000000009,0.0000000000000011,
    0.0000000000000013,0.0000000000000015,0.0000000000000018,0.0000000000000021,
    0.0000000000000025,0.0000000000000030,0.0000000000000036,0.0000000000000042,
    0.0000000000000050,0.0000000000000060,0.0000000000000071,0.0000000000000084,
    0.0000000000000100,0.0000000000000119,0.0000000000000142,0.0000000000000169,
};
```

0.000000000000201,0.000000000000239,0.000000000000284,0.000000000000338,
0.000000000000402,0.000000000000478,0.000000000000568,0.000000000000676,
0.000000000000804,0.000000000000956,0.000000000001137,0.000000000001352,
0.000000000001608,0.000000000001912,0.000000000002274,0.000000000002704,
0.000000000003216,0.000000000003824,0.000000000004547,0.000000000005408,
0.000000000006431,0.000000000007648,0.000000000009095,0.000000000010816,
0.000000000012862,0.000000000015296,0.000000000018190,0.000000000021632,
0.000000000025724,0.000000000030592,0.000000000036380,0.000000000043263,
0.000000000051449,0.000000000061183,0.000000000072760,0.000000000086526,
0.000000000102898,0.000000000122367,0.000000000145519,0.000000000173052,
0.000000000205795,0.000000000244733,0.000000000291038,0.000000000346105,
0.000000000411590,0.000000000489466,0.000000000582077,0.000000000692210,
0.000000000823181,0.000000000978932,0.000000001164153,0.000000001384419,
0.000000001646361,0.000000001957865,0.000000002328306,0.000000002768839,
0.000000003292723,0.000000003915729,0.000000004656613,0.000000005537677,
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163207.9734986786497757,163234.7733875660633203,163261.5743765002407599,163288.3764653908729088,
163315.1796541476505809,163341.9839426803810056,163368.7893308987549972,163395.5958187126088887,
163422.4034060317208059,163449.2120927658979781,163476.0218788249767385,163502.8327641188225243,
163529.6447485573007725,163556.4578320502769202,163583.2720145077037159,163610.0872958394465968,
163636.9036759555165190,163663.7211547658371273,163690.5397321804193780,163717.3594081092160195,
163744.1801824622962158,163771.0020551496418193,163797.8250260813802015,163824.6490951675223187,
163851.4742623181955423,163878.3005274434981402,163905.1278904535574839,163931.9563512585300487,
163958.7859097685723100,163985.6165658938698471,164012.4483195446373429,164039.2811706310894806,
164066.1151190634409431,164092.9501647519937251,164119.7863076069916133,164146.6235475387366023,
164173.4618844575597905,164200.3013182737631723,164227.1418488977069501,164253.9834762397804298,
164280.8262002103147097,164307.6700207197864074,164334.5149376785557251,164361.3609509970992804,
164388.2080605858645868,164415.0562663553282619,164441.9055682159960270,164468.7559660783736035,
164495.6074598529958166,164522.4600494503974915,164549.3137347811716609,164576.1685157559113577,
164603.0243922851805110,164629.8813642796594650,164656.7394316499121487,164683.5985943066771142,
164710.4588521606056020,164737.3202051223779563,164764.1826531027036253,164791.0461960123502649,
164817.9108337620564271,164844.7765662625606637,164871.6433934246888384,164898.5113151592377108,
164925.3803313770331442,164952.2504419888718985,164979.1216469056962524,165005.9939460383029655,
165032.8673392976343166,165059.7418265946034808,165086.6174078401527368,165113.4940829451952595,
165140.3718518207024317,165167.2507143777038436,165194.1306705271708779,165221.0117201801331248,
165247.8938632476201747,165274.7770996407198254,165301.6614292704907712,165328.5468520480208099,

};

```

inline void requantize
(header_info_type *header_info,
scalefac_type *scalefac,
side_info_type *si,
long int is[SBLIMIT][SSLIMIT],
float xr[SBLIMIT][SSLIMIT],
int gr,
int ch)
{
    int ss,sb,cb=0,sfreq;
    int next_cb, cb_begin, cb_width, sign;
    float temp;
    struct gr_info_type *gr_info = &(si->ch[ch].gr[gr]);

    sfreq=header_info->sampling_frequency_index + (header_info->ID * 3);

    //-----assign scalefactor band index-----;

    if (gr_info->>window_switching_flag && (gr_info->block_type == 2))
        if (gr_info->mixed_block_flag)
            next_cb = sfBandIndex[sfreq].l[1]; // mixed blocks;
        else
        {
            next_cb = sfBandIndex[sfreq].s[1]*3; // short blocks;
            cb_width = sfBandIndex[sfreq].s[1];
            cb_begin = 0;
        }
    else
        next_cb=sfBandIndex[sfreq].l[1]; // long blocks;

    for (sb=0 ; sb < SBLIMIT ; sb++)
        for (ss=0 ; ss < SSLIMIT ; ss++)
        {
            //-----scalefactor band transition-----;
            if ((sb*SSLIMIT)+ss == next_cb)
            {
                if (gr_info->>window_switching_flag && (gr_info->block_type == 2))
                {
                    if (gr_info->mixed_block_flag) // mixed blocks;
                    {
                        if (((sb*SSLIMIT)+ss) == sfBandIndex[sfreq].l[8])
                        {
                            next_cb = sfBandIndex[sfreq].s[4]*3;
                            cb = 3;
                            cb_width = sfBandIndex[sfreq].s[cb+1] -
                                sfBandIndex[sfreq].s[cb];
                            cb_begin = sfBandIndex[sfreq].s[cb]*3;
                        }
                        else if (((sb*SSLIMIT)+ss) < sfBandIndex[sfreq].l[8])

```

```

        next_cb = sfBandIndex[sfreq].l[(++cb)+1];
    else
    {
        next_cb = sfBandIndex[sfreq].s[(++cb)+1]*3;
        cb_width = sfBandIndex[sfreq].s[cb+1] -
                    sfBandIndex[sfreq].s[cb];
        cb_begin = sfBandIndex[sfreq].s[cb]*3;
    }
}
else // short blocks;
{
    next_cb = sfBandIndex[sfreq].s[(++cb)+1]*3;
    cb_width = sfBandIndex[sfreq].s[cb+1] -
                sfBandIndex[sfreq].s[cb];
    cb_begin = sfBandIndex[sfreq].s[cb]*3;
}
}
else // long blocks;
    next_cb = sfBandIndex[sfreq].l[(++cb)+1];
}

//-----compute scaling for each block-----;

xr[sb][ss] = powertable1[gr_info->global_gain];
//temp = pow(2.0, (0.25 * (gr_info->global_gain - 210)));
//printf(" power for initialization is  %f \n", gr_info->global_gain- 210.0);

if (gr_info->window_switching_flag && (
    ((gr_info->block_type == 2) && (gr_info->mixed_block_flag == 0)) ||
    ((gr_info->block_type == 2) && gr_info->mixed_block_flag && (sb >= 2)))
{ //short blocks;
    //printf("herehere hre \n");

    xr[sb][ss] *= powershort1[gr_info->subblock_gain[(((sb*SSLIMIT)+ss) - cb_begin)/cb_width]];

    // xr[sb][ss] = floatmul(xr[sb][ss], pow(2.0, -floatmul(2.0,
    // gr_info->subblock_gain[(((sb*SSLIMIT)+ss) - cb_begin)/cb_width]])); // -0.25*8

    // printf("gain is %.16f\n", pow(2.0, -floatmul(2.0,
    // gr_info->subblock_gain[(((sb*SSLIMIT)+ss) - cb_begin)/cb_width]]));
    // printf(" power for short block 1st part is  %f \n", gr_info->subblock_gain[(((sb*SSLIMIT)+ss) - cb_begin)/cb_width]);

    xr[sb][ss] *= powershort2[(1+gr_info->scalefac_scale)*(scalefac->ch[ch].s[(((sb*SSLIMIT)+ss) -
cb_begin)/cb_width][cb])]; // -0.25*2
    //printf("gain is %.16f\n", (1.0+gr_info->scalefac_scale)
    // *(scalefac->ch[ch].s[(((sb*SSLIMIT)+ss) - cb_begin)/cb_width][cb]));
    // printf(" power for short block 2nd part is  %f \n", (1.0+gr_info->scalefac_scale)
    // *(scalefac->ch[ch].s[(((sb*SSLIMIT)+ss) - cb_begin)/cb_width][cb]));
}
else { //long blocks;

```

```

        xr[sb][ss] *=powertable2[(1+gr_info->scalefac_scale)*(scalefac->ch[ch].l[cb] + gr_info->preflag * pretab[cb])];
        //xr[sb][ss] =floatmul(xr[sb][ss], powertable2[(1+gr_info->scalefac_scale)
            /*(scalefac->ch[ch].l[cb] + gr_info->preflag * pretab[cb])]);

        //printf(" power for long block is %f \n", (1.0+gr_info->scalefac_scale)
            // *(scalefac->ch[ch].l[cb] + gr_info->preflag * pretab[cb]));
    }

    sign = (is[sb][ss]<0) ? 1 : 0;

    if(abs(is[sb][ss])==0)xr[sb][ss] =0.0;
    else if(abs(is[sb][ss])==1){}
    else
        xr[sb][ss] *=powertable3[abs(is[sb][ss])];
        //xr[sb][ss] =floatmul( xr[sb][ss],powertable3[abs(is[sb][ss])]);
        //printf("abs is %d\n",abs(is[sb][ss]));
        //temp=pow( (float) abs(is[sb][ss]), ((float)4.0/3.0));

        //printf("test power is %.16f\n",temp);
        if (sign) xr[sb][ss] = -xr[sb][ss];
    }
}

```

6.6 joint_stereo.h

```
#define MPG_MD_JOINT_STEREO 1
```

```
static void i_stereo_k_values(float io,int is_pos,int i,float k[2][576])
```

```

{
    if(is_pos == 0)
    {
        k[0][i] = 1;
        k[1][i] = 1;
    }
    else if ((is_pos % 2) == 1)
    {
        k[0][i] = pow(io,(float)((is_pos + 1)/2));
        k[1][i] = 1;
    }
    else
    {
        k[0][i] = 1;
        k[1][i] = pow(io,(float)(is_pos/2));
    }
}

```

```

inline void join_stereo(
    float xr[2][SBLIMIT][SSLIMIT],
    float lr[2][SBLIMIT][SSLIMIT],
    scalefac_type *scalefac,
    struct gr_info_type *gr_info,
    header_info_type *header_info
)

```



```

        }
        lines--;
        i--;
    }
}
sfb = sfbcnt + 1;

if ( sfb > max_sfb )
    max_sfb = sfb;

while( sfb<12 )
{
    sb = sfBandIndex[sfreq].s[sfb+1]-sfBandIndex[sfreq].s[sfb];
    i = 3*sfBandIndex[sfreq].s[sfb] + j * sb;
    for ( ; sb > 0; sb-- )
    {
        is_pos[i] = scalefac->ch[1].s[j][sfb];
        if ( is_pos[i] != 7 )
            if ( !sf )
            {
                i_stereo_k_values(is_pos[i],io,i,k);
            }
            else
            {
                is_ratio[i] = tan((float)is_pos[i] * (PI / 12));
            }
            i++;
        }
        sfb++;
    }
}

sb = sfBandIndex[sfreq].s[12]-sfBandIndex[sfreq].s[11];
sfb = 3*sfBandIndex[sfreq].s[11] + j * sb;
sb = sfBandIndex[sfreq].s[13]-sfBandIndex[sfreq].s[12];

i = 3*sfBandIndex[sfreq].s[11] + j * sb;
for ( ; sb > 0; sb-- )
{
    is_pos[i] = is_pos[sfb];
    is_ratio[i] = is_ratio[sfb];
    k[0][i] = k[0][sfb];
    k[1][i] = k[1][sfb];
    i++;
}
}
if ( max_sfb <= 3 )
{
    i = 2;
    ss = 17;
    sb = -1;
    while ( i >= 0 )
    {
        if ( xr[1][i][ss] != 0.0 )
        {
            sb = i*18+ss;
            i = -1;
        }
        else
        {
            ss--;
            if ( ss < 0 )

```

```

        { i--;
          ss = 17;
        }
      }
    }
  i = 0;
  while ( sfBandIndex[sfreq].l[i] <= sb )
    i++;
  sfb = i;
  i = sfBandIndex[sfreq].l[i];
  for ( ; sfb < 8; sfb++ )
  { sb = sfBandIndex[sfreq].l[sfb+1] - sfBandIndex[sfreq].l[sfb];
    for ( ; sb > 0; sb-- )
    { is_pos[i] = scalefac->ch[1].l[sfb];
      if ( is_pos[i] != 7 )
        if ( !sf )
          {
            i_stereo_k_values(is_pos[i],io,i,k);
          }
        else
          {
            is_ratio[i] = tan((float)is_pos[i] * (PI / 12));
          }
        i++;
      }
    }
  }
} //end mixed block
else // short block
{ for (j=0; j<3; j++)
  { int sfbcnt;
    sfbcnt = -1;
    for( sfb=12; sfb >=0; sfb-- )
    { int lines;
      lines = sfBandIndex[sfreq].s[sfb+1] - sfBandIndex[sfreq].s[sfb];
      i = 3*sfBandIndex[sfreq].s[sfb] + (j+1) * lines - 1;
      while ( lines > 0 )
      { if ( xr[1][i/SSLIMIT][i%SSLIMIT] != 0.0 )
        { sfbcnt = sfb;
          sfb = -10;
          lines = -10;
        }
        lines--;
        i--;
      }
    }
  }
  sfb = sfbcnt + 1;
  while( sfb < 12 )
  { sb = sfBandIndex[sfreq].s[sfb+1] - sfBandIndex[sfreq].s[sfb];
    i = 3*sfBandIndex[sfreq].s[sfb] + j * sb;
    for ( ; sb > 0; sb-- )
    { is_pos[i] = scalefac->ch[1].s[j][sfb];
      if ( is_pos[i] != 7 )

```

```

        if( !sf )
        {
            i_stereo_k_values(is_pos[i],io,i,k);
        }
        else
        {
            is_ratio[i] = tan( (float)is_pos[i] * (PI / 12));
        }
        i++;
    }
    sfb++;
}

sb = sfBandIndex[sfreq].s[12]-sfBandIndex[sfreq].s[11];
sfb = 3*sfBandIndex[sfreq].s[11] + j * sb;
sb = sfBandIndex[sfreq].s[13]-sfBandIndex[sfreq].s[12];

i = 3*sfBandIndex[sfreq].s[11] + j * sb;
for ( ; sb > 0; sb-- )
{
    is_pos[i] = is_pos[sfb];
    is_ratio[i] = is_ratio[sfb];
    k[0][i] = k[0][sfb];
    k[1][i] = k[1][sfb];
    i++;
}
}
} // end short block
} // end short block and mixed block

else// long block
{
    i = 31;
    ss = 17;
    sb = 0;
    while ( i >= 0 )
    {
        if ( xr[1][i][ss] != 0.0 )
        {
            sb = i*18+ss;
            i = -1;
        }
        else
        {
            ss--;
            if ( ss < 0 )
            {
                i--;
                ss = 17;
            }
        }
    }
}
i = 0;
while ( sfBandIndex[sfreq].l[i] <= sb )
    i++;
sfb = i;
i = sfBandIndex[sfreq].l[i];
for ( ; sfb<21; sfb++ )
{
    sb = sfBandIndex[sfreq].l[sfb+1] - sfBandIndex[sfreq].l[sfb];
    for ( ; sb > 0; sb-- )

```

```

    { is_pos[i] = scalefac->ch[1].l[sfb];
      if ( is_pos[i] != 7 )
        if( lsf )
          {
            i_stereo_k_values(is_pos[i],io,i,k);
          }
        else
          {
            is_ratio[i] = tan((float)is_pos[i] * (PI / 12));
          }
        i++;
      }
    }
  }
  sfb = sfBandIndex[sfreq].l[20];
  for ( sb = 576 - sfBandIndex[sfreq].l[21]; sb > 0; sb-- )
  { is_pos[i] = is_pos[sfb];
    is_ratio[i] = is_ratio[sfb];
    k[0][i] = k[0][sfb];
    k[1][i] = k[1][sfb];
    i++;
  }
}
}
}

//end if ((stereo == 2) && i_stereo )

```

```

for(ch=0;ch<2;ch++)
  for(sb=0;sb<SBLIMIT;sb++)
    for(ss=0;ss<SSLIMIT;ss++)
      lr[ch][sb][ss] = 0;

```

```

if (stereo==2) // stereo
  for(sb=0;sb<SBLIMIT;sb++)
    for(ss=0;ss<SSLIMIT;ss++) {
      i = (sb*18)+ss;
      if ( is_pos[i] == 7 ) {
        if ( ms_stereo ) {
          lr[0][sb][ss] = (xr[0][sb][ss]+xr[1][sb][ss])/(float)1.41421356;
          lr[1][sb][ss] = (xr[0][sb][ss]-xr[1][sb][ss])/(float)1.41421356;
        }
        else {
          lr[0][sb][ss] = xr[0][sb][ss];
          lr[1][sb][ss] = xr[1][sb][ss];
        }
      }
    }
  else if (i_stereo) {
    if ( lsf )

```

```

        {
            lr[0][sb][ss] = xr[0][sb][ss] * k[0][i];
            lr[1][sb][ss] = xr[0][sb][ss] * k[1][i];
        }
    else
    {
        lr[0][sb][ss] = xr[0][sb][ss] * (is_ratio[i]/(1+is_ratio[i]));
        lr[1][sb][ss] = xr[0][sb][ss] * (1/(1+is_ratio[i]));
    }
}
else {
    printf("Error in stereo processing\n");
}
}
else /* mono , bypass xr[0][sb][ss] to lr[0][sb][ss] */
for(sb=0;sb<SBLIMIT;sb++)
for(ss=0;ss<SSLIMIT;ss++)
    lr[0][sb][ss] = xr[0][sb][ss];
}

```

6.7 antialias.h

```

float Ci[8]={-0.6,-0.535,-0.33,-0.185,-0.095,-0.041,-0.0142,-0.0037};
#include "float.h"

```

```

inline void antialias(xr, hybridIn, gr_info, header_info)

```

```

float xr[SBLIMIT][SSLIMIT];

```

```

float hybridIn[SBLIMIT][SSLIMIT];

```

```

struct gr_info_type *gr_info;

```

```

header_info_type *header_info;

```

```

{
    static int    init = 1;
    static float ca[8],cs[8];
    float        bu,bd; /* upper and lower butterfly inputs */
    int          ss,sb,sblim;

```

```

    if (init) {
        int i;
        float sq;
        for (i=0;i<8;i++) {
            sq=sqrt(1.0+Ci[i]*Ci[i]);
            cs[i] = 1.0/sq;
            ca[i] = Ci[i]/sq;
        }
        init = 0;
    }
}

```

```

/* clear all inputs */

```

```

for(sb=0;sb<SBLIMIT;sb++)
for(ss=0;ss<SSLIMIT;ss++)
    hybridIn[sb][ss] = xr[sb][ss];

```

```

if (gr_info->window_switching_flag && (gr_info->block_type == 2) &&
    !gr_info->mixed_block_flag) return;

if (gr_info->window_switching_flag && gr_info->mixed_block_flag &&
    (gr_info->block_type == 2))
    sblim = 1;
else
    sblim = SBLIMIT-1;

/* 31 alias-reduction operations between each pair of sub-bands */
/* with 8 butterflies between each pair */

for(sb=0;sb<sblim;sb++)
    for(ss=0;ss<8;ss++) {
        bu = xr[sb][17-ss];
        bd = xr[sb+1][ss];
        //float temp1=cs[ss];
        //float temp2=ca[ss];
        // hybridln[sb][17-ss] = floatadd(floatmul(bu,temp1), -floatmul(bd, temp2));
        //hybridln[sb+1][ss] = floatadd(floatmul(bd,temp1), floatmul(bu, temp2));
        hybridln[sb][17-ss] = (bu * cs[ss]) - (bd * ca[ss]);
        hybridln[sb+1][ss] = (bd * cs[ss]) + (bu * ca[ss]);
    }
}

```

6.8 reorder.h

```

inline void reorder
(float xr[SBLIMIT][SSLIMIT],
 float ro[SBLIMIT][SSLIMIT],
 struct gr_info_type *gr_info,
 header_info_type *header_info)
{
    int sfreq;
    int sfb, sfb_start, sfb_lines;
    int sb, ss, window, freq, src_line, des_line;

    sfreq=header_info->sampling_frequency_index + (header_info->ID * 3);

    for(sb=0;sb<SBLIMIT;sb++)
        for(ss=0;ss<SSLIMIT;ss++)
            ro[sb][ss] = 0;

    if (gr_info->window_switching_flag && (gr_info->block_type == 2)) {
        if (gr_info->mixed_block_flag) {
            /* NO REORDER FOR LOW 2 SUBBANDS */
            for (sb=0 ; sb < 2 ; sb++)
                for (ss=0 ; ss < SSLIMIT ; ss++) {
                    ro[sb][ss] = xr[sb][ss];
                }
            /* REORDERING FOR REST SWITCHED SHORT */
            for(sfb=3,sfb_start=sfBandIndex[sfreq].s[3],
                sfb_lines=sfBandIndex[sfreq].s[4] - sfb_start;

```

```

sfb < 13; sfb++,sfb_start=sfBandIndex[sfreq].s[sfb],
(sfb_lines=sfBandIndex[sfreq].s[sfb+1] - sfb_start)
for(window=0; window<3; window++)
for(freq=0;freq<sfb_lines;freq++) {
src_line = sfb_start*3 + window*sfb_lines + freq;
des_line = (sfb_start*3) + window + (freq*3);
ro[des_line/SSLIMIT][des_line%SSLIMIT] =
xr[src_line/SSLIMIT][src_line%SSLIMIT];
}
}
else { /* pure short */
for(sfb=0,sfb_start=0,sfb_lines=sfBandIndex[sfreq].s[1];
sfb < 13; sfb++,sfb_start=sfBandIndex[sfreq].s[sfb],
(sfb_lines=sfBandIndex[sfreq].s[sfb+1] - sfb_start)
for(window=0; window<3; window++)
for(freq=0;freq<sfb_lines;freq++) {
src_line = sfb_start*3 + window*sfb_lines + freq;
des_line = (sfb_start*3) + window + (freq*3);
ro[des_line/SSLIMIT][des_line%SSLIMIT] =
xr[src_line/SSLIMIT][src_line%SSLIMIT];
}
}
}
else { /*long blocks */
for (sb=0 ; sb < SBLIMIT ; sb++)
for (ss=0 ; ss < SSLIMIT ; ss++)
ro[sb][ss] = xr[sb][ss];
}
}

```

6.9 hybrid.h

```
//type0: win[0][0]=sin( PI/36 *(0+0.5)
```

```
float win[4][36]=
```

```
{
```

```
{0.0436193873653360,0.1305261922200515,0.2164396139381027,0.3007057995042728,0.3826834323650894,0.4617486132350335,0.537
2996083468233,0.6087614290087201,0.6755902076156597,
0.7372773368101234,0.7933533402912346,0.8433914458128852,0.8870108331782213,0.9238795325112863,0.9537169507482265,0.9762
960071199330,0.9914448613738103,0.9990482215818577,
0.9990482215818578,0.9914448613738106,0.9762960071199338,0.9537169507482275,0.9238795325112875,0.8870108331782226,0.8433
914458128868,0.7933533402912365,0.7372773368101255,
0.6755902076156620,0.6087614290087225,0.5372996083468260,0.4617486132350363,0.3826834323650923,0.3007057995042758,0.2164
396139381058,0.1305261922200547,0.0436193873653392},
```

```
//type1:win[1][i]
```

```
{0.0436193873653360,0.1305261922200515,0.2164396139381027,0.3007057995042728,0.3826834323650894,0.4617486132350335,0.537
2996083468233,0.6087614290087201,0.6755902076156597,
0.7372773368101234,0.7933533402912346,0.8433914458128852,0.8870108331782213,0.9238795325112863,0.9537169507482265,0.9762
960071199330,0.9914448613738103,0.9990482215818577,
1.0000000000000000,1.0000000000000000,1.0000000000000000,1.0000000000000000,1.0000000000000000,1.0000000000000000,0.9914
448613738106,0.9238795325112875,0.7933533402912365,
0.6087614290087225,0.3826834323650923,0.1305261922200547,0.0000000000000000,0.0000000000000000,0.0000000000000000,0.0000
0000000000,0.0000000000000000,0.0000000000000000},
```



```

0.5372996083468193,0.5735764363510417,0.6087614290087163,0.6427876096865351,0.6755902076156562,0.7071067811865436,0.7372
773368101203,0.7660444431189745,
0.7933533402912317,0.8191520442889885,0.8433914458128826,0.8660254037844357,0.8870108331782189,0.9063077870366475,0.9238
795325112844,0.9396926207859063,
0.9537169507482250,0.9659258262890666,0.9762960071199320,0.9848077530122070,0.9914448613738096,0.9961946980917450,0.9990
482215818575};
//Done COS

```

```

//block type=2 , cos( PI/(2*N)*(2*p(variable)+1+N/2)*(2*m(variable)+1)): cos_tab_1[p][m]
float cos_tab_1[12][6]=
{
{0.6087614290087214,-0.9238795325112857,-0.1305261922200560,0.9914448613738113,-0.3826834323650817,-0.7933533402912418},
{0.3826834323650908,-0.9238795325112882,0.9238795325112844,-0.3826834323650817,-0.3826834323651004,0.9238795325112915},
{0.1305261922200530,-0.3826834323650936,0.6087614290087267,-0.7933533402912418,0.9238795325112915,-0.9914448613738125},
{-0.1305261922200498,0.3826834323650851,-0.6087614290087134,0.7933533402912272,-0.9238795325112810,0.9914448613738079},
{-0.3826834323650878,0.9238795325112844,-0.9238795325112908,0.3826834323651022,0.3826834323650731,-0.9238795325112787},
{-0.6087614290087189,0.9238795325112894,0.1305261922200396,-0.9914448613738084,0.3826834323651073,0.7933533402912194},
{-0.7933533402912336,0.3826834323650970,0.9914448613738086,0.1305261922200323,-0.9238795325112957,-0.6087614290086988},
{-0.9238795325112857,-0.3826834323650817,0.3826834323651022,0.9238795325112944,0.9238795325112774,0.3826834323650595},
{-0.9914448613738100,-0.9238795325112831,-0.7933533402912261,-0.6087614290087046,-0.3826834323650630,-0.1305261922200211},
{-0.9914448613738108,-0.9238795325112908,-0.7933533402912463,-0.6087614290087398,-0.3826834323651175,-0.1305261922200872},
{-0.9238795325112882,-0.3826834323651004,0.3826834323650731,0.9238795325112774,0.9238795325112986,0.3826834323651277},
{-0.7933533402912376,0.3826834323650783,0.9914448613738127,0.1305261922200798,-0.9238795325112745,-0.6087614290087573}
};
//Blocktype2Done!

```

```

void inv_mdct
(float in[18],
 float out[36],
 int block_type)
{
int i,m,N,p;
float tmp[12],sum;
for(i=0;i<36;i++)
out[i]=0;

if(block_type == 2){
N=12;
for(i=0;i<3;i++){
for(p=0;p<N;p++){
sum = 0.0;
for(m=0;m<N/2;m++)
{
int k = (m) + (m<<1);
sum += in[i+k] * (cos_tab_1[p][m]);
/*if(((cos( PI/(2*N)*(2*p+1+N/2)*(2*m+1)))-(cos_tab_1[p][m]))>=0.0000001)
{printf("idmv error!\n");
exit(10);}*/
}
tmp[p] = sum * win[block_type][p] ;
}
for(p=0;p<N;p++){
int k = (i<<1) + (i<<2);

```

```

        out[k+p+6] += tmp[p];
    }
}
else{
    N=36;
    for(p= 0;p<N;p++){
        sum = 0.0;
        for(m=0;m<N/2;m++){
            int k = ((p*m)<<2) + (p<<1) + (m<<5)+(m<<2)+(m<<1)+19;
            while(k>=144){
                k = k - 144;
            }
            sum += in[m] * COS[k];
        }
        out[p] = sum * win[block_type][p];
    }
}
}

```

```

void hybrid
(float fsIn[SSLIMIT], /* freq samples per subband in */
float tsOut[SSLIMIT], /* time samples per subband out */
int sb,
int ch,
struct gr_info_type *gr_info,
header_info_type *header_info)
{
    int ss;
    float rawout[36];
    static float prevblk[2][SBLIMIT][SSLIMIT];
    static int init = 1;
    int bt;

    if (init) {
        int i,j,k;

        for(i=0;i<2;i++)
            for(j=0;j<SBLIMIT;j++)
                for(k=0;k<SSLIMIT;k++)
                    prevblk[i][j][k]=0.0;
        init = 0;
    }

    bt = (gr_info->window_switching_flag && gr_info->mixed_block_flag &&
        (sb < 2)) ? 0 : gr_info->block_type;

    inv_mdct( fsIn, rawout, bt);

    /* overlap addition */
    for(ss=0; ss<SSLIMIT; ss++) {
        tsOut[ss] = rawout[ss] + prevblk[ch][sb][ss];
    }
}

```

```

    prevbck[ch][sb][ss] = rawout[ss+18];
}
}

```

6.10 subbandsynthesis.h

```

#include "dewindowarray.h"
#include "syn_costable.h"
#define SCALE 32768

```

```

inline void create_syn_filter(double doublefilter[64][SBLIMIT]) {
    register int i, k;

    for (i = 0; i < 64; i++)
        for (k = 0; k < 32; k++) {
            //if ((filter[i][k] = 1e9*cos((float)((PI/64*i+PI/4)*(2*k+1)))) >= 0)
            if ((doublefilter[i][k] = syn_costable[(i << 5) + k]) >= 0)
                modf(doublefilter[i][k] + 0.5, &doublefilter[i][k]);
            else
                modf(doublefilter[i][k] - 0.5, &doublefilter[i][k]);

            //printf("filter before is %.16f\n", filter[i][k]);
            doublefilter[i][k] *= 1e-9;
            //printf("syn_costable[(i<<5)+k] is %.16f\n", syn_costable[(i<<5)+k]);
            //printf("filter after is %.16f\n", doublefilter[i][k]);
        }
}

```

```

inline void SubBandSynthesis(float *bandPtr, int ch, short *samples) {
    register int i, j, k;
    int iii, jjj;
    register float *bufOffsetPtr, sum;
    register long foo;
    static int init = 1;
    typedef float NN[64][32];
    typedef double MM[64][32];
    static NN *filter;
    static MM *doublefilter;
    typedef float BB[2][1024];
    static BB *buf;
    static int bufOffset[2] = { 64, 64 };
    if (init) {
        buf = (BB *) malloc(sizeof(BB));
        doublefilter = (MM *) malloc(sizeof(MM));
        filter = (NN *) malloc(sizeof(NN));
        create_syn_filter(*doublefilter);
        for (iii = 0; iii < 64; iii++) {
            for (jjj = 0; jjj < 32; jjj++)
                (*filter)[iii][jjj] = (float) (*doublefilter)[iii][jjj];
        }
    }
    init = 0;
}

```

```

}

bufOffset[ch] = (bufOffset[ch] - 64) & 0x3ff;
bufOffsetPtr = &((*buf)[ch][bufOffset[ch]]);

for (i = 0; i < 64; i++) {
    sum = 0;
    for (k = 0; k < 32; k++)
        sum += bandPtr[k] * (*filter)[i][k];
    bufOffsetPtr[i] = sum;
}

for (j = 0; j < 32; j++) {
    sum = 0;
    for (i = 0; i < 16; i++) {
        k = j + (i << 5);
        sum += dewindow[k] * (*buf)[ch][((k + (((i + 1) >> 1) << 6))
            + bufOffset[ch]) & 0x3ff];
    }

    if (sum > 0) {
        foo = (long) (sum * (float) SCALE + (float) 0.5);
    } else {
        foo = (long) (sum * (float) SCALE - (float) 0.5);
    }

    if (foo >= (long) SCALE) {
        samples[j] = (short) (SCALE - 1);
    } else if (foo < (long) -SCALE) {
        samples[j] = (short) (-SCALE);
    } else
        samples[j] = (short) foo;

    //pcm_output[frameNum-1][gr_global][ch_global][ss_global][j]=samples[j];
}
}

```

6.11 de2_vag_raster.vhd

```
library ieee;
```

```
use ieee.std_logic_1164.all;
```

```
use ieee.numeric_std.all;
```

```
entity de2_vga_raster is
```

```
port (
```

```
    reset_n : in std_logic;
```

```
    clk      : in std_logic;          -- Should be 25.125 MHz
```

```
    write   : in std_logic;-----
```

```
    chipselect : in std_logic;-----
```

```
    address  : in unsigned(6 downto 0);-----
```

```
    writedata : in unsigned(15 downto 0);-----
```

```
    VGA_CLK,
```

```
    -- Clock
```

```

VGA_HS,                -- H_SYNC
VGA_VS,                -- V_SYNC
VGA_BLANK,             -- BLANK
VGA_SYNC : out std_logic; -- SYNC
VGA_R,                -- Red[9:0]
VGA_G,                -- Green[9:0]
VGA_B : out unsigned(9 downto 0) -- Blue[9:0]
);

```

end de2_vga_raster;

architecture rtl of de2_vga_raster is

```

-- Video parameters
signal clk25: std_logic;
constant HTOTAL      : integer := 800;
constant HSYNC       : integer := 96;
constant HBACK_PORCH : integer := 48;
constant HACTIVE     : integer := 640;
constant HFRONT_PORCH : integer := 16;

constant VTOTAL      : integer := 525;
constant VSYNC       : integer := 2;
constant VBACK_PORCH : integer := 33;
constant VACTIVE     : integer := 480;
constant VFRONT_PORCH : integer := 10;

--type matrix is array (0 to 63) of integer;
--constant RECTANGLE_HSTART : matrix;
--  RECTANGLE_HSTART[0]<=0;
--
--  FOR i IN 1 to 64 LOOP
--RECTANGLE_HSTART[i]<=(RECTANGLE[i]+10);
--i=i+1;
--end loop
--
--
constant RECTANGLE_HSTART1 : integer := 1;
constant RECTANGLE_HEND1   : integer := 8;
constant RECTANGLE_HSTART2 : integer := 10;
constant RECTANGLE_HEND2   : integer := 18;
constant RECTANGLE_HSTART3 : integer := 20;
constant RECTANGLE_HEND3   : integer := 28;
constant RECTANGLE_HSTART4 : integer := 30;
constant RECTANGLE_HEND4   : integer := 38;
constant RECTANGLE_HSTART5 : integer := 40;
constant RECTANGLE_HEND5   : integer := 48;
constant RECTANGLE_HSTART6 : integer := 50;
constant RECTANGLE_HEND6   : integer := 58;
constant RECTANGLE_HSTART7 : integer := 60;
constant RECTANGLE_HEND7   : integer := 68;

```

```
constant RECTANGLE_HSTART8 : integer := 70;
constant RECTANGLE_HEND8   : integer := 78;
constant RECTANGLE_HSTART9 : integer := 80;
constant RECTANGLE_HEND9   : integer := 88;
constant RECTANGLE_HSTART10 : integer := 90;
constant RECTANGLE_HEND10  : integer := 98;
```

```
constant RECTANGLE_HSTART11 : integer := 100;
constant RECTANGLE_HEND11   : integer := 108;
constant RECTANGLE_HSTART12 : integer := 110;
constant RECTANGLE_HEND12   : integer := 118;
constant RECTANGLE_HSTART13 : integer := 120;
constant RECTANGLE_HEND13   : integer := 128;
constant RECTANGLE_HSTART14 : integer := 130;
constant RECTANGLE_HEND14   : integer := 138;
constant RECTANGLE_HSTART15 : integer := 140;
constant RECTANGLE_HEND15   : integer := 148;
constant RECTANGLE_HSTART16 : integer := 150;
constant RECTANGLE_HEND16   : integer := 158;
constant RECTANGLE_HSTART17 : integer := 160;
constant RECTANGLE_HEND17   : integer := 168;
constant RECTANGLE_HSTART18 : integer := 170;
constant RECTANGLE_HEND18   : integer := 178;
constant RECTANGLE_HSTART19 : integer := 180;
constant RECTANGLE_HEND19   : integer := 188;
constant RECTANGLE_HSTART20 : integer := 190;
constant RECTANGLE_HEND20   : integer := 198;
```

```
constant RECTANGLE_HSTART21 : integer := 200;
constant RECTANGLE_HEND21   : integer := 208;
constant RECTANGLE_HSTART22 : integer := 210;
constant RECTANGLE_HEND22   : integer := 218;
constant RECTANGLE_HSTART23 : integer := 220;
constant RECTANGLE_HEND23   : integer := 228;
constant RECTANGLE_HSTART24 : integer := 230;
constant RECTANGLE_HEND24   : integer := 238;
constant RECTANGLE_HSTART25 : integer := 240;
constant RECTANGLE_HEND25   : integer := 248;
constant RECTANGLE_HSTART26 : integer := 250;
constant RECTANGLE_HEND26   : integer := 258;
constant RECTANGLE_HSTART27 : integer := 260;
constant RECTANGLE_HEND27   : integer := 268;
constant RECTANGLE_HSTART28 : integer := 270;
constant RECTANGLE_HEND28   : integer := 278;
constant RECTANGLE_HSTART29 : integer := 280;
constant RECTANGLE_HEND29   : integer := 288;
constant RECTANGLE_HSTART30 : integer := 290;
constant RECTANGLE_HEND30   : integer := 298;
```

```
constant RECTANGLE_HSTART31 : integer := 300;
constant RECTANGLE_HEND31   : integer := 308;
```

constant RECTANGLE_HSTART32 : integer := 310;
constant RECTANGLE_HEND32 : integer := 318;
constant RECTANGLE_HSTART33 : integer := 320;
constant RECTANGLE_HEND33 : integer := 328;
constant RECTANGLE_HSTART34 : integer := 330;
constant RECTANGLE_HEND34 : integer := 338;
constant RECTANGLE_HSTART35 : integer := 340;
constant RECTANGLE_HEND35 : integer := 348;
constant RECTANGLE_HSTART36 : integer := 350;
constant RECTANGLE_HEND36 : integer := 358;
constant RECTANGLE_HSTART37 : integer := 360;
constant RECTANGLE_HEND37 : integer := 368;
constant RECTANGLE_HSTART38 : integer := 370;
constant RECTANGLE_HEND38 : integer := 378;
constant RECTANGLE_HSTART39 : integer := 380;
constant RECTANGLE_HEND39 : integer := 388;
constant RECTANGLE_HSTART40 : integer := 390;
constant RECTANGLE_HEND40 : integer := 398;

constant RECTANGLE_HSTART41 : integer := 400;
constant RECTANGLE_HEND41 : integer := 408;
constant RECTANGLE_HSTART42 : integer := 410;
constant RECTANGLE_HEND42 : integer := 418;
constant RECTANGLE_HSTART43 : integer := 420;
constant RECTANGLE_HEND43 : integer := 428;
constant RECTANGLE_HSTART44 : integer := 430;
constant RECTANGLE_HEND44 : integer := 438;
constant RECTANGLE_HSTART45 : integer := 440;
constant RECTANGLE_HEND45 : integer := 448;
constant RECTANGLE_HSTART46 : integer := 450;
constant RECTANGLE_HEND46 : integer := 458;
constant RECTANGLE_HSTART47 : integer := 460;
constant RECTANGLE_HEND47 : integer := 468;
constant RECTANGLE_HSTART48 : integer := 470;
constant RECTANGLE_HEND48 : integer := 478;
constant RECTANGLE_HSTART49 : integer := 480;
constant RECTANGLE_HEND49 : integer := 488;
constant RECTANGLE_HSTART50 : integer := 490;
constant RECTANGLE_HEND50 : integer := 498;

constant RECTANGLE_HSTART51 : integer := 500;
constant RECTANGLE_HEND51 : integer := 508;
constant RECTANGLE_HSTART52 : integer := 510;
constant RECTANGLE_HEND52 : integer := 518;
constant RECTANGLE_HSTART53 : integer := 520;
constant RECTANGLE_HEND53 : integer := 528;
constant RECTANGLE_HSTART54 : integer := 530;
constant RECTANGLE_HEND54 : integer := 538;
constant RECTANGLE_HSTART55 : integer := 540;
constant RECTANGLE_HEND55 : integer := 548;
constant RECTANGLE_HSTART56 : integer := 550;
constant RECTANGLE_HEND56 : integer := 558;


```
constant RECTANGLE_HSTART57 : integer := 560;
constant RECTANGLE_HEND57   : integer := 568;
constant RECTANGLE_HSTART58 : integer := 570;
constant RECTANGLE_HEND58   : integer := 578;
constant RECTANGLE_HSTART59 : integer := 580;
constant RECTANGLE_HEND59   : integer := 588;
constant RECTANGLE_HSTART60 : integer := 590;
constant RECTANGLE_HEND60   : integer := 598;
```

```
constant RECTANGLE_HSTART61 : integer := 600;
constant RECTANGLE_HEND61   : integer := 608;
constant RECTANGLE_HSTART62 : integer := 610;
constant RECTANGLE_HEND62   : integer := 618;
constant RECTANGLE_HSTART63 : integer := 620;
constant RECTANGLE_HEND63   : integer := 628;
constant RECTANGLE_HSTART64 : integer := 630;
constant RECTANGLE_HEND64   : integer := 638;
```

```
constant RECTANGLE_VEND1,
RECTANGLE_VEND2,
RECTANGLE_VEND3,
RECTANGLE_VEND4,
RECTANGLE_VEND5,
RECTANGLE_VEND6,
RECTANGLE_VEND7,
RECTANGLE_VEND8,
RECTANGLE_VEND9,
RECTANGLE_VEND10,
RECTANGLE_VEND11,
RECTANGLE_VEND12,
RECTANGLE_VEND13,
RECTANGLE_VEND14,
RECTANGLE_VEND15,
RECTANGLE_VEND16,
RECTANGLE_VEND17,
RECTANGLE_VEND18,
RECTANGLE_VEND19,
RECTANGLE_VEND20,
RECTANGLE_VEND21,
RECTANGLE_VEND22,
RECTANGLE_VEND23,
RECTANGLE_VEND24,
RECTANGLE_VEND25,
RECTANGLE_VEND26,
RECTANGLE_VEND27,
RECTANGLE_VEND28,
RECTANGLE_VEND29,
RECTANGLE_VEND30,
RECTANGLE_VEND31,
RECTANGLE_VEND32,
RECTANGLE_VEND33,
RECTANGLE_VEND34,
RECTANGLE_VEND35,
```

RECTANGLE_VEND36,
RECTANGLE_VEND37,
RECTANGLE_VEND38,
RECTANGLE_VEND39,
RECTANGLE_VEND40,
RECTANGLE_VEND41,
RECTANGLE_VEND42,
RECTANGLE_VEND43,
RECTANGLE_VEND44,
RECTANGLE_VEND45,
RECTANGLE_VEND46,
RECTANGLE_VEND47,
RECTANGLE_VEND48,
RECTANGLE_VEND49,
RECTANGLE_VEND50,
RECTANGLE_VEND51,
RECTANGLE_VEND52,
RECTANGLE_VEND53,
RECTANGLE_VEND54,
RECTANGLE_VEND55,
RECTANGLE_VEND56,
RECTANGLE_VEND57,
RECTANGLE_VEND58,
RECTANGLE_VEND59,
RECTANGLE_VEND60,
RECTANGLE_VEND61,
RECTANGLE_VEND62,
RECTANGLE_VEND63,
RECTANGLE_VEND64: integer:=380;

signal RECTANGLE_VSTART1,
RECTANGLE_VSTART2,
RECTANGLE_VSTART3,
RECTANGLE_VSTART4,
RECTANGLE_VSTART5,
RECTANGLE_VSTART6,
RECTANGLE_VSTART7,
RECTANGLE_VSTART8,
RECTANGLE_VSTART9,
RECTANGLE_VSTART10,
RECTANGLE_VSTART11,
RECTANGLE_VSTART12,
RECTANGLE_VSTART13,
RECTANGLE_VSTART14,
RECTANGLE_VSTART15,
RECTANGLE_VSTART16,
RECTANGLE_VSTART17,
RECTANGLE_VSTART18,
RECTANGLE_VSTART19,
RECTANGLE_VSTART20,
RECTANGLE_VSTART21,
RECTANGLE_VSTART22,

RECTANGLE_VSTART23,
RECTANGLE_VSTART24,
RECTANGLE_VSTART25,
RECTANGLE_VSTART26,
RECTANGLE_VSTART27,
RECTANGLE_VSTART28,
RECTANGLE_VSTART29,
RECTANGLE_VSTART30,
RECTANGLE_VSTART31,
RECTANGLE_VSTART32,
RECTANGLE_VSTART33,
RECTANGLE_VSTART34,
RECTANGLE_VSTART35,
RECTANGLE_VSTART36,
RECTANGLE_VSTART37,
RECTANGLE_VSTART38,
RECTANGLE_VSTART39,
RECTANGLE_VSTART40,
RECTANGLE_VSTART41,
RECTANGLE_VSTART42,
RECTANGLE_VSTART43,
RECTANGLE_VSTART44,
RECTANGLE_VSTART45,
RECTANGLE_VSTART46,
RECTANGLE_VSTART47,
RECTANGLE_VSTART48,
RECTANGLE_VSTART49,
RECTANGLE_VSTART50,
RECTANGLE_VSTART51,
RECTANGLE_VSTART52,
RECTANGLE_VSTART53,
RECTANGLE_VSTART54,
RECTANGLE_VSTART55,
RECTANGLE_VSTART56,
RECTANGLE_VSTART57,
RECTANGLE_VSTART58,
RECTANGLE_VSTART59,
RECTANGLE_VSTART60,
RECTANGLE_VSTART61,
RECTANGLE_VSTART62,
RECTANGLE_VSTART63,
RECTANGLE_VSTART64: unsigned(15 downto 0);

signal vs1:unsigned(15 downto 0);
signal vs2:unsigned(15 downto 0);
signal vs3:unsigned(15 downto 0);
signal vs4:unsigned(15 downto 0);
signal vs5:unsigned(15 downto 0);
signal vs6:unsigned(15 downto 0);
signal vs7:unsigned(15 downto 0);
signal vs8:unsigned(15 downto 0);
signal vs9:unsigned(15 downto 0);
signal vs10:unsigned(15 downto 0);

signal vs11:unsigned(15 downto 0);
signal vs12:unsigned(15 downto 0);
signal vs13:unsigned(15 downto 0);
signal vs14:unsigned(15 downto 0);
signal vs15:unsigned(15 downto 0);
signal vs16:unsigned(15 downto 0);
signal vs17:unsigned(15 downto 0);
signal vs18:unsigned(15 downto 0);
signal vs19:unsigned(15 downto 0);
signal vs20:unsigned(15 downto 0);
signal vs21:unsigned(15 downto 0);
signal vs22:unsigned(15 downto 0);
signal vs23:unsigned(15 downto 0);
signal vs24:unsigned(15 downto 0);
signal vs25:unsigned(15 downto 0);
signal vs26:unsigned(15 downto 0);
signal vs27:unsigned(15 downto 0);
signal vs28:unsigned(15 downto 0);
signal vs29:unsigned(15 downto 0);
signal vs30:unsigned(15 downto 0);
signal vs31:unsigned(15 downto 0);
signal vs32:unsigned(15 downto 0);
signal vs33:unsigned(15 downto 0);
signal vs34:unsigned(15 downto 0);
signal vs35:unsigned(15 downto 0);
signal vs36:unsigned(15 downto 0);
signal vs37:unsigned(15 downto 0);
signal vs38:unsigned(15 downto 0);
signal vs39:unsigned(15 downto 0);
signal vs40:unsigned(15 downto 0);
signal vs41:unsigned(15 downto 0);
signal vs42:unsigned(15 downto 0);
signal vs43:unsigned(15 downto 0);
signal vs44:unsigned(15 downto 0);
signal vs45:unsigned(15 downto 0);
signal vs46:unsigned(15 downto 0);
signal vs47:unsigned(15 downto 0);
signal vs48:unsigned(15 downto 0);
signal vs49:unsigned(15 downto 0);
signal vs50:unsigned(15 downto 0);
signal vs51:unsigned(15 downto 0);
signal vs52:unsigned(15 downto 0);
signal vs53:unsigned(15 downto 0);
signal vs54:unsigned(15 downto 0);
signal vs55:unsigned(15 downto 0);
signal vs56:unsigned(15 downto 0);
signal vs57:unsigned(15 downto 0);
signal vs58:unsigned(15 downto 0);
signal vs59:unsigned(15 downto 0);
signal vs60:unsigned(15 downto 0);
signal vs61:unsigned(15 downto 0);
signal vs62:unsigned(15 downto 0);
signal vs63:unsigned(15 downto 0);

```
signal vs64:unsigned(15 downto 0);
```

```
-- Signals for the video controller
```

```
signal Hcount : unsigned(9 downto 0); -- Horizontal position (0-800)
```

```
signal Vcount : unsigned(9 downto 0); -- Vertical position (0-524)
```

```
signal EndOfLine, EndOfField : std_logic;
```

```
signal vga_hblank, vga_hsync, vga_vblank, vga_vsync : std_logic; -- Sync. signals
```

```
signal rectangle_h1, rectangle_v1, rectangle1 : std_logic; -- rectangle area
```

```
signal rectangle_h2, rectangle_v2, rectangle2 : std_logic; -- rectangle area
```

```
signal rectangle_h3, rectangle_v3, rectangle3 : std_logic; -- rectangle area
```

```
signal rectangle_h4, rectangle_v4, rectangle4 : std_logic; -- rectangle area
```

```
signal rectangle_h5, rectangle_v5, rectangle5 : std_logic; -- rectangle area
```

```
signal rectangle_h6, rectangle_v6, rectangle6 : std_logic; -- rectangle area
```

```
signal rectangle_h7, rectangle_v7, rectangle7 : std_logic; -- rectangle area
```

```
signal rectangle_h8, rectangle_v8, rectangle8 : std_logic; -- rectangle area
```

```
signal rectangle_h9, rectangle_v9, rectangle9 : std_logic; -- rectangle area
```

```
signal rectangle_h10, rectangle_v10, rectangle10 : std_logic; -- rectangle area
```

```
signal rectangle_h11, rectangle_v11, rectangle11 : std_logic; -- rectangle area
```

```
signal rectangle_h12, rectangle_v12, rectangle12 : std_logic; -- rectangle area
```

```
signal rectangle_h13, rectangle_v13, rectangle13 : std_logic; -- rectangle area
```

```
signal rectangle_h14, rectangle_v14, rectangle14 : std_logic; -- rectangle area
```

```
signal rectangle_h15, rectangle_v15, rectangle15 : std_logic; -- rectangle area
```

```
signal rectangle_h16, rectangle_v16, rectangle16 : std_logic; -- rectangle area
```

```
signal rectangle_h17, rectangle_v17, rectangle17 : std_logic; -- rectangle area
```

```
signal rectangle_h18, rectangle_v18, rectangle18 : std_logic; -- rectangle area
```

```
signal rectangle_h19, rectangle_v19, rectangle19 : std_logic; -- rectangle area
```

```
signal rectangle_h20, rectangle_v20, rectangle20 : std_logic; -- rectangle area
```

```
signal rectangle_h21, rectangle_v21, rectangle21 : std_logic; -- rectangle area
```

```
signal rectangle_h22, rectangle_v22, rectangle22 : std_logic; -- rectangle area
```

```
signal rectangle_h23, rectangle_v23, rectangle23 : std_logic; -- rectangle area
```

```
signal rectangle_h24, rectangle_v24, rectangle24 : std_logic; -- rectangle area
```

```
signal rectangle_h25, rectangle_v25, rectangle25 : std_logic; -- rectangle area
```

```
signal rectangle_h26, rectangle_v26, rectangle26 : std_logic; -- rectangle area
```

```
signal rectangle_h27, rectangle_v27, rectangle27 : std_logic; -- rectangle area
```

```
signal rectangle_h28, rectangle_v28, rectangle28 : std_logic; -- rectangle area
```

```
signal rectangle_h29, rectangle_v29, rectangle29 : std_logic; -- rectangle area
```

```
signal rectangle_h30, rectangle_v30, rectangle30 : std_logic; -- rectangle area
```

```
signal rectangle_h31, rectangle_v31, rectangle31 : std_logic; -- rectangle area
```

```
signal rectangle_h32, rectangle_v32, rectangle32 : std_logic; -- rectangle area
```

```
signal rectangle_h33, rectangle_v33, rectangle33 : std_logic; -- rectangle area
```

```
signal rectangle_h34, rectangle_v34, rectangle34 : std_logic; -- rectangle area
```

```
signal rectangle_h35, rectangle_v35, rectangle35 : std_logic; -- rectangle area
```

```
signal rectangle_h36, rectangle_v36, rectangle36 : std_logic; -- rectangle area
```

```
signal rectangle_h37, rectangle_v37, rectangle37 : std_logic; -- rectangle area
```

```
signal rectangle_h38, rectangle_v38, rectangle38 : std_logic; -- rectangle area
```

```
signal rectangle_h39, rectangle_v39, rectangle39 : std_logic; -- rectangle area
```

```
signal rectangle_h40, rectangle_v40, rectangle40 : std_logic; -- rectangle area
```

```
signal rectangle_h41, rectangle_v41, rectangle41 : std_logic; -- rectangle area
```

```

signal rectangle_h42, rectangle_v42, rectangle42 : std_logic; -- rectangle area
signal rectangle_h43, rectangle_v43, rectangle43 : std_logic; -- rectangle area
signal rectangle_h44, rectangle_v44, rectangle44 : std_logic; -- rectangle area
signal rectangle_h45, rectangle_v45, rectangle45 : std_logic; -- rectangle area
signal rectangle_h46, rectangle_v46, rectangle46 : std_logic; -- rectangle area
signal rectangle_h47, rectangle_v47, rectangle47 : std_logic; -- rectangle area
signal rectangle_h48, rectangle_v48, rectangle48 : std_logic; -- rectangle area
signal rectangle_h49, rectangle_v49, rectangle49 : std_logic; -- rectangle area
signal rectangle_h50, rectangle_v50, rectangle50 : std_logic; -- rectangle area
signal rectangle_h51, rectangle_v51, rectangle51 : std_logic; -- rectangle area
signal rectangle_h52, rectangle_v52, rectangle52 : std_logic; -- rectangle area
signal rectangle_h53, rectangle_v53, rectangle53 : std_logic; -- rectangle area
signal rectangle_h54, rectangle_v54, rectangle54 : std_logic; -- rectangle area
signal rectangle_h55, rectangle_v55, rectangle55 : std_logic; -- rectangle area
signal rectangle_h56, rectangle_v56, rectangle56 : std_logic; -- rectangle area
signal rectangle_h57, rectangle_v57, rectangle57 : std_logic; -- rectangle area
signal rectangle_h58, rectangle_v58, rectangle58 : std_logic; -- rectangle area
signal rectangle_h59, rectangle_v59, rectangle59 : std_logic; -- rectangle area
signal rectangle_h60, rectangle_v60, rectangle60 : std_logic; -- rectangle area
signal rectangle_h61, rectangle_v61, rectangle61 : std_logic; -- rectangle area
signal rectangle_h62, rectangle_v62, rectangle62 : std_logic; -- rectangle area
signal rectangle_h63, rectangle_v63, rectangle63 : std_logic; -- rectangle area
signal rectangle_h64, rectangle_v64, rectangle64 : std_logic; -- rectangle area

```

```
begin
```

```

clk0: process (clk)
begin
    if rising_edge(clk) then
        clk25 <= not clk25;
    end if;
end process;

```

```

peripheral: process(clk)
begin
    if rising_edge(clk) then
        if reset_n = '0' then

            vs1<=(others=>'0');
            vs2<=(others=>'0');
            vs3<=(others=>'0');
            vs4<=(others=>'0');
            vs5<=(others=>'0');
            vs6<=(others=>'0');
            vs7<=(others=>'0');
            vs8<=(others=>'0');
            vs9<=(others=>'0');
            vs10<=(others=>'0');
            vs11<=(others=>'0');
            vs12<=(others=>'0');
            vs13<=(others=>'0');
            vs14<=(others=>'0');
            vs15<=(others=>'0');

```

```
vs16<=(others=>'0');
vs17<=(others=>'0');
vs18<=(others=>'0');
vs19<=(others=>'0');
vs20<=(others=>'0');
vs21<=(others=>'0');
vs22<=(others=>'0');
vs23<=(others=>'0');
vs24<=(others=>'0');
vs25<=(others=>'0');
vs26<=(others=>'0');
vs27<=(others=>'0');
vs28<=(others=>'0');
vs29<=(others=>'0');
vs30<=(others=>'0');
vs31<=(others=>'0');
vs32<=(others=>'0');
vs33<=(others=>'0');
vs34<=(others=>'0');
vs35<=(others=>'0');
vs36<=(others=>'0');
vs37<=(others=>'0');
vs38<=(others=>'0');
vs39<=(others=>'0');
vs40<=(others=>'0');
vs41<=(others=>'0');
vs42<=(others=>'0');
vs43<=(others=>'0');
vs44<=(others=>'0');
vs45<=(others=>'0');
vs46<=(others=>'0');
vs47<=(others=>'0');
vs48<=(others=>'0');
vs49<=(others=>'0');
vs50<=(others=>'0');
vs51<=(others=>'0');
vs52<=(others=>'0');
vs53<=(others=>'0');
vs54<=(others=>'0');
vs55<=(others=>'0');
vs56<=(others=>'0');
vs57<=(others=>'0');
vs58<=(others=>'0');
vs59<=(others=>'0');
vs60<=(others=>'0');
vs61<=(others=>'0');
vs62<=(others=>'0');
vs63<=(others=>'0');
vs64<=(others=>'0');
else
  if chipselect = '1' then
    if write = '1' then
      if address="0000000"
```

```

then vs1 <= writedata(15 downto 0);
elsif address="0000001"
then vs2<= writedata(15 downto 0);
elsif address="0000010"
then vs3<= writedata(15 downto 0);
elsif address="0000011"
then vs4<= writedata(15 downto 0);
elsif address="0000100"
then vs5<= writedata(15 downto 0);
elsif address="0000101"
then vs6<= writedata(15 downto 0);
elsif address="0000110"
then vs7<= writedata(15 downto 0);
elsif address="0000111"
then vs8<= writedata(15 downto 0);
elsif address="0001000"
  then vs9 <= writedata(15 downto 0);
elsif address="0001001"
  then vs10<= writedata(15 downto 0);
elsif address="0001010"
  then vs11<= writedata(15 downto 0);
elsif address="0001011"
  then vs12<= writedata(15 downto 0);
elsif address="0001100"
  then vs13<= writedata(15 downto 0);
elsif address="0001101"
  then vs14<= writedata(15 downto 0);
elsif address="0001110"
  then vs15<= writedata(15 downto 0);
elsif address="0001111"
  then vs16<= writedata(15 downto 0);

elsif address="0010000"
then vs17 <= writedata(15 downto 0);
elsif address="0010001"
then vs18<= writedata(15 downto 0);
elsif address="0010010"
then vs19<= writedata(15 downto 0);
elsif address="0010011"
then vs20<= writedata(15 downto 0);
elsif address="0010100"
then vs21<= writedata(15 downto 0);
elsif address="0010101"
then vs22<= writedata(15 downto 0);
elsif address="0010110"
then vs23<= writedata(15 downto 0);
elsif address="0010111"
then vs24<= writedata(15 downto 0);
elsif address="0011000"
  then vs25 <= writedata(15 downto 0);
elsif address="0011001"
  then vs26<= writedata(15 downto 0);

```



```
elseif address="0011010"
then vs27 <= writedata(15 downto 0);
elseif address="0011011"
then vs28<= writedata(15 downto 0);
elseif address="0011100"
then vs29<= writedata(15 downto 0);
elseif address="0011101"
then vs30<= writedata(15 downto 0);
elseif address="0011110"
then vs31<= writedata(15 downto 0);
elseif address="0011111"
then vs32<= writedata(15 downto 0);
elseif address="0100000"
then vs33<= writedata(15 downto 0);
elseif address="0100001"
then vs34<= writedata(15 downto 0);
elseif address="0100010"
then vs35 <= writedata(15 downto 0);
elseif address="0100011"
then vs36<= writedata(15 downto 0);

elseif address="0100100"
then vs37 <= writedata(15 downto 0);
elseif address="0100101"
then vs38<= writedata(15 downto 0);
elseif address="0100110"
then vs39<= writedata(15 downto 0);
elseif address="0100111"
then vs40<= writedata(15 downto 0);
elseif address="0101000"
then vs41<= writedata(15 downto 0);
elseif address="0101001"
then vs42<= writedata(15 downto 0);
elseif address="0101010"
then vs43<= writedata(15 downto 0);
elseif address="0101011"
then vs44<= writedata(15 downto 0);
elseif address="0101100"
then vs45 <= writedata(15 downto 0);
elseif address="0101101"
then vs46<= writedata(15 downto 0);

elseif address="0101110"
then vs47 <= writedata(15 downto 0);
elseif address="0101111"
then vs48<= writedata(15 downto 0);
elseif address="0110000"
then vs49<= writedata(15 downto 0);
elseif address="0110001"
then vs50<= writedata(15 downto 0);
elseif address="0110010"
then vs51<= writedata(15 downto 0);
elseif address="0110011"
```

```

    then vs52<= writedata(15 downto 0);
    elsif address="0110100"
    then vs53<= writedata(15 downto 0);
    elsif address="0110101"
    then vs54<= writedata(15 downto 0);
    elsif address="0110110"
    then vs55 <= writedata(15 downto 0);
elseif address="0110111"
    then vs56<= writedata(15 downto 0);

    elsif address="0111000"
    then vs57 <= writedata(15 downto 0);
elseif address="0111001"
    then vs58<= writedata(15 downto 0);
elseif address="0111010"
    then vs59<= writedata(15 downto 0);
elseif address="0111011"
    then vs60<= writedata(15 downto 0);
    elsif address="0111100"
    then vs61<= writedata(15 downto 0);
elseif address="0111101"
    then vs62<= writedata(15 downto 0);
    elsif address="0111110"
    then vs63<= writedata(15 downto 0);
    elsif address="0111111"
    then vs64<= writedata(15 downto 0);
    end if;

end if;
end if;
if EndOfLine='1' and EndOfField='1'
then RECTANGLE_VSTART1<=vs1;
RECTANGLE_VSTART2<=vs2;
RECTANGLE_VSTART3<=vs3;
RECTANGLE_VSTART4<=vs4;
RECTANGLE_VSTART5<=vs5;
RECTANGLE_VSTART6<=vs6;
RECTANGLE_VSTART7<=vs7;
RECTANGLE_VSTART8<=vs8;
RECTANGLE_VSTART9<=vs9;
RECTANGLE_VSTART10<=vs10;
RECTANGLE_VSTART11<=vs11;
RECTANGLE_VSTART12<=vs12;
RECTANGLE_VSTART13<=vs13;
RECTANGLE_VSTART14<=vs14;
RECTANGLE_VSTART15<=vs15;
RECTANGLE_VSTART16<=vs16;

RECTANGLE_VSTART17<=vs17;
RECTANGLE_VSTART18<=vs18;
RECTANGLE_VSTART19<=vs19;
RECTANGLE_VSTART20<=vs20;

```

```
RECTANGLE_VSTART21<=vs21;  
RECTANGLE_VSTART22<=vs22;  
RECTANGLE_VSTART23<=vs23;  
RECTANGLE_VSTART24<=vs24;  
RECTANGLE_VSTART25<=vs25;  
RECTANGLE_VSTART26<=vs26;  
RECTANGLE_VSTART27<=vs27;  
RECTANGLE_VSTART28<=vs28;  
RECTANGLE_VSTART29<=vs29;  
RECTANGLE_VSTART30<=vs30;
```

```
RECTANGLE_VSTART31<=vs31;  
RECTANGLE_VSTART32<=vs32;  
RECTANGLE_VSTART33<=vs33;  
RECTANGLE_VSTART34<=vs34;  
RECTANGLE_VSTART35<=vs35;  
RECTANGLE_VSTART36<=vs36;  
RECTANGLE_VSTART37<=vs37;  
RECTANGLE_VSTART38<=vs38;  
RECTANGLE_VSTART39<=vs39;  
RECTANGLE_VSTART40<=vs40;
```

```
RECTANGLE_VSTART41<=vs41;  
RECTANGLE_VSTART42<=vs42;  
RECTANGLE_VSTART43<=vs43;  
RECTANGLE_VSTART44<=vs44;  
RECTANGLE_VSTART45<=vs45;  
RECTANGLE_VSTART46<=vs46;  
RECTANGLE_VSTART47<=vs47;  
RECTANGLE_VSTART48<=vs48;  
RECTANGLE_VSTART49<=vs49;  
RECTANGLE_VSTART50<=vs50;
```

```
RECTANGLE_VSTART51<=vs51;  
RECTANGLE_VSTART52<=vs52;  
RECTANGLE_VSTART53<=vs53;  
RECTANGLE_VSTART54<=vs54;  
RECTANGLE_VSTART55<=vs55;  
RECTANGLE_VSTART56<=vs56;  
RECTANGLE_VSTART57<=vs57;  
RECTANGLE_VSTART58<=vs58;  
RECTANGLE_VSTART59<=vs59;  
RECTANGLE_VSTART60<=vs60;  
RECTANGLE_VSTART61<=vs61;  
RECTANGLE_VSTART62<=vs62;  
RECTANGLE_VSTART63<=vs63;  
RECTANGLE_VSTART64<=vs64;
```

```
    end if;  
  end if;  
end if;  
end process peripheral;
```

-- Horizontal and vertical counters

```
HCounter : process (clk25)
begin
  if rising_edge(clk25) then
    if reset_n = '0' then
      Hcount <= (others => '0');
    elsif EndOfLine = '1' then
      Hcount <= (others => '0');
    else
      Hcount <= Hcount + 1;
    end if;
  end if;
end process HCounter;
```

EndOfLine <= '1' when Hcount = HTOTAL - 1 else '0';

```
VCounter: process (clk25)
begin
  if rising_edge(clk25) then
    if reset_n = '0' then
      Vcount <= (others => '0');
    elsif EndOfLine = '1' then
      if EndOfField = '1' then
        Vcount <= (others => '0');
      else
        Vcount <= Vcount + 1;
      end if;
    end if;
  end if;
end process VCounter;
```

EndOfField <= '1' when Vcount = VTOTAL - 1 else '0';

-- State machines to generate HSYNC, VSYNC, HBLANK, and VBLANK

```
HSyncGen : process (clk25)
begin
  if rising_edge(clk25) then
    if reset_n = '0' or EndOfLine = '1' then
      vga_hsync <= '1';
    elsif Hcount = HSYNC - 1 then
      vga_hsync <= '0';
    end if;
  end if;
end process HSyncGen;
```

```

HBlankGen : process (clk25)
begin
  if rising_edge(clk25) then
    if reset_n = '0' then
      vga_hblank <= '1';
    elsif Hcount = HSYNC + HBACK_PORCH then
      vga_hblank <= '0';
    elsif Hcount = HSYNC + HBACK_PORCH + HACTIVE then
      vga_hblank <= '1';
    end if;
  end if;
end process HBlankGen;

```

```

VSyncGen : process (clk25)
begin
  if rising_edge(clk25) then
    if reset_n = '0' then
      vga_vsync <= '1';
    elsif EndOfLine = '1' then
      if EndOfField = '1' then
        vga_vsync <= '1';
      elsif Vcount = VSYNC - 1 then
        vga_vsync <= '0';
      end if;
    end if;
  end if;
end process VSyncGen;

```

```

VBlankGen : process (clk25)
begin
  if rising_edge(clk25) then
    if reset_n = '0' then
      vga_vblank <= '1';
    elsif EndOfLine = '1' then
      if Vcount = VSYNC + VBACK_PORCH - 1 then
        vga_vblank <= '0';
      elsif Vcount = VSYNC + VBACK_PORCH + VACTIVE - 1 then
        vga_vblank <= '1';
      end if;
    end if;
  end if;
end process VBlankGen;

```

-- Rectangle generator

```

RectangleHGen : process (clk25)
begin
  if rising_edge(clk25) then

    if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART1) then
      rectangle_h1 <= '1';
    elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND1 then
      rectangle_h1 <= '0';
    end if;
  end if;
end process RectangleHGen;

```

end if;

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART2) then
    rectangle_h2 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND2 then
    rectangle_h2 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART3) then
    rectangle_h3 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND3 then
    rectangle_h3 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART4) then
    rectangle_h4 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND4 then
    rectangle_h4 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART5) then
    rectangle_h5 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND5 then
    rectangle_h5 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART6) then
    rectangle_h6 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND6 then
    rectangle_h6 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART7) then
    rectangle_h7 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND7 then
    rectangle_h7 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART8) then
    rectangle_h8 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND8 then
    rectangle_h8 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART9) then
    rectangle_h9 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND9 then
    rectangle_h9 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART10) then
    rectangle_h10 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND10 then
```

```
rectangle_h10 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART11) then  
rectangle_h11 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND11 then  
rectangle_h11 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART12) then  
rectangle_h12 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND12 then  
rectangle_h12 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART13) then  
rectangle_h13 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND13 then  
rectangle_h13 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART14) then  
rectangle_h14 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND14 then  
rectangle_h14 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART15) then  
rectangle_h15 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND15 then  
rectangle_h15 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART16) then  
rectangle_h16 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND16 then  
rectangle_h16 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART17) then  
rectangle_h17 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND17 then  
rectangle_h17 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART18) then  
rectangle_h18 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND18 then  
rectangle_h18 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART19) then  
rectangle_h19 <= '1';
```

```
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND19 then
    rectangle_h19 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART20) then
    rectangle_h20 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND20 then
    rectangle_h20 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART21) then
    rectangle_h21 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND21 then
    rectangle_h21 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART22) then
    rectangle_h22 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND22 then
    rectangle_h22 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART23) then
    rectangle_h23 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND23 then
    rectangle_h23 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART24) then
    rectangle_h24 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND24 then
    rectangle_h24 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART25) then
    rectangle_h25 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND25 then
    rectangle_h25 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART26) then
    rectangle_h26 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND26 then
    rectangle_h26 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART27) then
    rectangle_h27 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND27 then
    rectangle_h27 <= '0';
end if;
```



```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART28) then
    rectangle_h28 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND28 then
    rectangle_h28 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART29) then
    rectangle_h29 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND29 then
    rectangle_h29 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART30) then
    rectangle_h30 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND30 then
    rectangle_h30 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART31) then
    rectangle_h31 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND31 then
    rectangle_h31 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART32) then
    rectangle_h32 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND32 then
    rectangle_h32 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART33) then
    rectangle_h33 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND33 then
    rectangle_h33 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART34) then
    rectangle_h34 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND34 then
    rectangle_h34 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART35) then
    rectangle_h35 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND35 then
    rectangle_h35 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART36) then
    rectangle_h36 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND36 then
    rectangle_h36 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART37) then
    rectangle_h37 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND37 then
    rectangle_h37 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART38) then
    rectangle_h38 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND38 then
    rectangle_h38 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART39) then
    rectangle_h39 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND39 then
    rectangle_h39 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART40) then
    rectangle_h40 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND40 then
    rectangle_h40 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART41) then
    rectangle_h41 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND41 then
    rectangle_h41 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART42) then
    rectangle_h42 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND42 then
    rectangle_h42 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART43) then
    rectangle_h43 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND43 then
    rectangle_h43 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART44) then
    rectangle_h44 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND44 then
    rectangle_h44 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART45) then
    rectangle_h45 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND45 then
    rectangle_h45 <= '0';
end if;
```

end if;

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART46) then
  rectangle_h46 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND46 then
  rectangle_h46 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART47) then
  rectangle_h47 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND47 then
  rectangle_h47 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART48) then
  rectangle_h48 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND48 then
  rectangle_h48 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART49) then
  rectangle_h49 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND49 then
  rectangle_h49 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART50) then
  rectangle_h50 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND50 then
  rectangle_h50 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART51) then
  rectangle_h51 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND51 then
  rectangle_h51 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART52) then
  rectangle_h52 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND52 then
  rectangle_h52 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART53) then
  rectangle_h53 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND53 then
  rectangle_h53 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART54) then
  rectangle_h54 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND54 then
```

```
rectangle_h54 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART55) then  
rectangle_h55 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND55 then  
rectangle_h55 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART56) then  
rectangle_h56 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND56 then  
rectangle_h56 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART57) then  
rectangle_h57 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND57 then  
rectangle_h57 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART58) then  
rectangle_h58 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND58 then  
rectangle_h58 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART59) then  
rectangle_h59 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND59 then  
rectangle_h59 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART60) then  
rectangle_h60 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND60 then  
rectangle_h60 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART61) then  
rectangle_h61 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND61 then  
rectangle_h61 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART62) then  
rectangle_h62 <= '1';  
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND62 then  
rectangle_h62 <= '0';  
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART63) then
    rectangle_h63 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND63 then
    rectangle_h63 <= '0';
end if;
```

```
if reset_n = '0' or (Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HSTART64) then
    rectangle_h64 <= '1';
elsif Hcount = HSYNC + HBACK_PORCH + RECTANGLE_HEND64 then
    rectangle_h64 <= '0';
end if;
```

```
end if;
```

```
end process RectangleHGen;
```

```
RectangleVGen : process (clk25)
```

```
begin
```

```
if rising_edge(clk25) then
```

```
if reset_n = '0' then
```

```
    rectangle_v1 <= '0';
    rectangle_v2 <= '0';
    rectangle_v3 <= '0';
    rectangle_v4 <= '0';
    rectangle_v5 <= '0';
    rectangle_v6 <= '0';
    rectangle_v7 <= '0';
    rectangle_v8 <= '0';
    rectangle_v9 <= '0';
    rectangle_v10 <= '0';
    rectangle_v11 <= '0';
    rectangle_v12 <= '0';
    rectangle_v13 <= '0';
    rectangle_v14 <= '0';
    rectangle_v15 <= '0';
    rectangle_v16 <= '0';
    rectangle_v17 <= '0';
    rectangle_v18 <= '0';
    rectangle_v19 <= '0';
    rectangle_v20 <= '0';
    rectangle_v21 <= '0';
    rectangle_v22 <= '0';
    rectangle_v23 <= '0';
    rectangle_v24 <= '0';
    rectangle_v25 <= '0';
    rectangle_v26 <= '0';
    rectangle_v27 <= '0';
    rectangle_v28 <= '0';
    rectangle_v29 <= '0';
    rectangle_v30 <= '0';
    rectangle_v31 <= '0';
```

```
rectangle_v32 <= '0';
rectangle_v33 <= '0';
rectangle_v34 <= '0';
rectangle_v35 <= '0';
rectangle_v36 <= '0';
rectangle_v37 <= '0';
rectangle_v38 <= '0';
rectangle_v39 <= '0';
rectangle_v40 <= '0';
rectangle_v41 <= '0';
rectangle_v42 <= '0';
rectangle_v43 <= '0';
rectangle_v44 <= '0';
rectangle_v45 <= '0';
rectangle_v46 <= '0';
rectangle_v47 <= '0';
rectangle_v48 <= '0';
rectangle_v49 <= '0';
rectangle_v50 <= '0';
rectangle_v51 <= '0';
rectangle_v52 <= '0';
rectangle_v53 <= '0';
rectangle_v54 <= '0';
rectangle_v55 <= '0';
rectangle_v56 <= '0';
rectangle_v57 <= '0';
rectangle_v58 <= '0';
rectangle_v59 <= '0';
rectangle_v60 <= '0';
rectangle_v61 <= '0';
rectangle_v62 <= '0';
rectangle_v63 <= '0';
rectangle_v64 <= '0';
```

```
elsif EndOfLine = '1' then
```

```
  if Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART1 then
    rectangle_v1 <= '1';
  elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND1 then
    rectangle_v1 <= '0';
  end if;
```

```
  if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART2) then
    rectangle_v2 <= '1';
  elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND2 then
    rectangle_v2 <= '0';
  end if;
```

```
  if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART3) then
    rectangle_v3 <= '1';
  elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND3 then
```

```
rectangle_v3 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART4) then  
rectangle_v4 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND4 then  
rectangle_v4 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART5) then  
rectangle_v5 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND5 then  
rectangle_v5 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART6) then  
rectangle_v6 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND6 then  
rectangle_v6 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART7) then  
rectangle_v7 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND7 then  
rectangle_v7 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART8) then  
rectangle_v8 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND8 then  
rectangle_v8 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART9) then  
rectangle_v9 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND9 then  
rectangle_v9 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART10) then  
rectangle_v10 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND10 then  
rectangle_v10 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART11) then  
rectangle_v11 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND11 then  
rectangle_v11 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART12) then  
rectangle_v12 <= '1';
```

```
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND12 then
    rectangle_v12 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART13) then
    rectangle_v13 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND13 then
    rectangle_v13 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART14) then
    rectangle_v14 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND14 then
    rectangle_v14 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART15) then
    rectangle_v15 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND15 then
    rectangle_v15 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART16) then
    rectangle_v16 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND16 then
    rectangle_v16 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART17) then
    rectangle_v17 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND17 then
    rectangle_v17 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART18) then
    rectangle_v18 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND18 then
    rectangle_v18 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART19) then
    rectangle_v19 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND19 then
    rectangle_v19 <= '0';
end if;

if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART20) then
    rectangle_v20 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND20 then
    rectangle_v20 <= '0';
end if;
```



```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART21) then
  rectangle_v21 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND21 then
  rectangle_v21 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART22) then
  rectangle_v22 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND22 then
  rectangle_v22 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART23) then
  rectangle_v23 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND23 then
  rectangle_v23 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART24) then
  rectangle_v24 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND24 then
  rectangle_v24 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART25) then
  rectangle_v25 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND25 then
  rectangle_v25 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART26) then
  rectangle_v26 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND26 then
  rectangle_v26 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART27) then
  rectangle_v27 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND27 then
  rectangle_v27 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART28) then
  rectangle_v28 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND28 then
  rectangle_v28 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART29) then
  rectangle_v29 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND29 then
  rectangle_v29 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART30) then
    rectangle_v30 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND30 then
    rectangle_v30 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART31) then
    rectangle_v31 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND31 then
    rectangle_v31 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART32) then
    rectangle_v32 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND32 then
    rectangle_v32 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART33) then
    rectangle_v33 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND33 then
    rectangle_v33 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART34) then
    rectangle_v34 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND34 then
    rectangle_v34 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART35) then
    rectangle_v35 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND35 then
    rectangle_v35 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART36) then
    rectangle_v36 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND36 then
    rectangle_v36 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART37) then
    rectangle_v37 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND37 then
    rectangle_v37 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART38) then
    rectangle_v38 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND38 then
    rectangle_v38 <= '0';
end if;
```

end if;

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART39) then
  rectangle_v39 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND39 then
  rectangle_v39 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART40) then
  rectangle_v40 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND40 then
  rectangle_v40 <= '0';
end if;
```

```
if(Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART41) then
  rectangle_v41 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND41 then
  rectangle_v41 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART42) then
  rectangle_v42 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND42 then
  rectangle_v42 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART43) then
  rectangle_v43 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND43 then
  rectangle_v43 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART44) then
  rectangle_v44 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND44 then
  rectangle_v44 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART45) then
  rectangle_v45 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND45 then
  rectangle_v45 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART46) then
  rectangle_v46 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND46 then
  rectangle_v46 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART47) then
  rectangle_v47 <= '1';
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND47 then
```

```
rectangle_v47 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART48) then  
rectangle_v48 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND48 then  
rectangle_v48 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART49) then  
rectangle_v49 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND49 then  
rectangle_v49 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART50) then  
rectangle_v50 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND50 then  
rectangle_v50 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART51) then  
rectangle_v51 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND51 then  
rectangle_v51 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART52) then  
rectangle_v52 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND52 then  
rectangle_v52 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART53) then  
rectangle_v53 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND53 then  
rectangle_v53 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART54) then  
rectangle_v54 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND54 then  
rectangle_v54 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART55) then  
rectangle_v55 <= '1';  
elsif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND55 then  
rectangle_v55 <= '0';  
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART56) then  
rectangle_v56 <= '1';
```

```
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND56 then
    rectangle_v56 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART57) then
    rectangle_v57 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND57 then
    rectangle_v57 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART58) then
    rectangle_v58 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND58 then
    rectangle_v58 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART59) then
    rectangle_v59 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND59 then
    rectangle_v59 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART60) then
    rectangle_v60 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND60 then
    rectangle_v60 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART61) then
    rectangle_v61 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND61 then
    rectangle_v61 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART62) then
    rectangle_v62 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND62 then
    rectangle_v62 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART63) then
    rectangle_v63 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND63 then
    rectangle_v63 <= '0';
end if;
```

```
if (Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VSTART64) then
    rectangle_v64 <= '1';
elseif Vcount = VSYNC + VBACK_PORCH-1 + RECTANGLE_VEND64 then
    rectangle_v64 <= '0';
end if;
```

```
end if;
```

```
end if;  
end process RectangleVGen;
```

```
rectangle1 <= rectangle_h1 and rectangle_v1;  
rectangle2 <= rectangle_h2 and rectangle_v2;  
rectangle3 <= rectangle_h3 and rectangle_v3;  
rectangle4 <= rectangle_h4 and rectangle_v4;  
rectangle5 <= rectangle_h5 and rectangle_v5;  
rectangle6 <= rectangle_h6 and rectangle_v6;  
rectangle7 <= rectangle_h7 and rectangle_v7;  
rectangle8 <= rectangle_h8 and rectangle_v8;  
rectangle9 <= rectangle_h9 and rectangle_v9;  
rectangle10 <= rectangle_h10 and rectangle_v10;  
rectangle11 <= rectangle_h11 and rectangle_v11;  
rectangle12 <= rectangle_h12 and rectangle_v12;  
rectangle13 <= rectangle_h13 and rectangle_v13;  
rectangle14 <= rectangle_h14 and rectangle_v14;  
rectangle15 <= rectangle_h15 and rectangle_v15;  
rectangle16 <= rectangle_h16 and rectangle_v16;  
rectangle17 <= rectangle_h17 and rectangle_v17;  
rectangle18 <= rectangle_h18 and rectangle_v18;  
rectangle19 <= rectangle_h19 and rectangle_v19;  
rectangle20 <= rectangle_h20 and rectangle_v20;  
rectangle21 <= rectangle_h21 and rectangle_v21;  
rectangle22 <= rectangle_h22 and rectangle_v22;  
rectangle23 <= rectangle_h23 and rectangle_v23;  
rectangle24 <= rectangle_h24 and rectangle_v24;  
rectangle25 <= rectangle_h25 and rectangle_v25;  
rectangle26 <= rectangle_h26 and rectangle_v26;  
rectangle27 <= rectangle_h27 and rectangle_v27;  
rectangle28 <= rectangle_h28 and rectangle_v28;  
rectangle29 <= rectangle_h29 and rectangle_v29;  
rectangle30 <= rectangle_h30 and rectangle_v30;  
rectangle31 <= rectangle_h31 and rectangle_v31;  
rectangle32 <= rectangle_h32 and rectangle_v32;  
rectangle33 <= rectangle_h33 and rectangle_v33;  
rectangle34 <= rectangle_h34 and rectangle_v34;  
rectangle35 <= rectangle_h35 and rectangle_v35;  
rectangle36 <= rectangle_h36 and rectangle_v36;  
rectangle37 <= rectangle_h37 and rectangle_v37;  
rectangle38 <= rectangle_h38 and rectangle_v38;  
rectangle39 <= rectangle_h39 and rectangle_v39;  
rectangle40 <= rectangle_h40 and rectangle_v40;  
rectangle41 <= rectangle_h41 and rectangle_v41;  
rectangle42 <= rectangle_h42 and rectangle_v42;  
rectangle43 <= rectangle_h43 and rectangle_v43;  
rectangle44 <= rectangle_h44 and rectangle_v44;  
rectangle45 <= rectangle_h45 and rectangle_v45;  
rectangle46 <= rectangle_h46 and rectangle_v46;  
rectangle47 <= rectangle_h47 and rectangle_v47;  
rectangle48 <= rectangle_h48 and rectangle_v48;  
rectangle49 <= rectangle_h49 and rectangle_v49;
```

```

rectangle50 <= rectangle_h50 and rectangle_v50;
rectangle51 <= rectangle_h51 and rectangle_v51;
rectangle52 <= rectangle_h52 and rectangle_v52;
rectangle53 <= rectangle_h53 and rectangle_v53;
rectangle54 <= rectangle_h54 and rectangle_v54;
rectangle55 <= rectangle_h55 and rectangle_v55;
rectangle56 <= rectangle_h56 and rectangle_v56;
rectangle57 <= rectangle_h57 and rectangle_v57;
rectangle58 <= rectangle_h58 and rectangle_v58;
rectangle59 <= rectangle_h59 and rectangle_v59;
rectangle60 <= rectangle_h60 and rectangle_v60;
rectangle61 <= rectangle_h61 and rectangle_v61;
rectangle62 <= rectangle_h62 and rectangle_v62;
rectangle63 <= rectangle_h63 and rectangle_v63;
rectangle64 <= rectangle_h64 and rectangle_v64;
-- Registered video signals going to the video DAC

```

```

VideoOut: process (clk25, reset_n)

```

```

begin

```

```

    if reset_n = '0' then

```

```

        VGA_R <= "0000000000";

```

```

        VGA_G <= "0000000000";

```

```

        VGA_B <= "0000000000";

```

```

    elsif clk25'event and clk25 = '1' then

```

```

if (rectangle1 = '1' or rectangle2 = '1' or rectangle3 = '1' or rectangle4 = '1' or rectangle5 = '1'
or rectangle6 = '1' or rectangle7 = '1' or rectangle8 = '1' or rectangle9 = '1'
or rectangle10 = '1' or rectangle11 = '1' or rectangle12 = '1' or rectangle13 = '1'
or rectangle14 = '1' or rectangle15 = '1' or rectangle16 = '1' or rectangle17 = '1'
or rectangle18 = '1' or rectangle19 = '1' or rectangle20 = '1' or rectangle21 = '1'
or rectangle22 = '1' or rectangle23 = '1' or rectangle24 = '1' or rectangle25 = '1'
or rectangle26 = '1' or rectangle27 = '1' or rectangle28 = '1' or rectangle29 = '1'
or rectangle30 = '1' or rectangle31 = '1' or rectangle32 = '1' or rectangle33 = '1'
or rectangle34 = '1' or rectangle35 = '1' or rectangle36 = '1' or rectangle37 = '1'
or rectangle38 = '1' or rectangle39 = '1' or rectangle40 = '1' or rectangle41 = '1'
or rectangle42 = '1' or rectangle43 = '1' or rectangle44 = '1' or rectangle45 = '1'
or rectangle46 = '1' or rectangle47 = '1' or rectangle48 = '1' or rectangle49 = '1'
or rectangle50 = '1' or rectangle51 = '1' or rectangle52 = '1' or rectangle53 = '1'
or rectangle54 = '1' or rectangle55 = '1' or rectangle56 = '1' or rectangle57 = '1'
or rectangle58 = '1' or rectangle59 = '1' or rectangle60 = '1'
or rectangle61 = '1' or rectangle62 = '1'
or rectangle63 = '1' or rectangle64 = '1') then

```

```

    VGA_R <= "1111111111";

```

```

    VGA_G <= "1111111111";

```

```

    VGA_B <= "1111111111";

```

```

    elsif vga_hblank = '0' and vga_vblank = '0' then

```

```

        VGA_R <= "0000000000";

```

```

        VGA_G <= "0000000000";

```

```

        VGA_B <= "0000000000";

```

```

    else

```

```

    VGA_R <= "0000000000";
    VGA_G <= "0000000000";
    VGA_B <= "0000000000";
end if;
end if;
end process VideoOut;

```

```

VGA_CLK <= clk25;
VGA_HS <= not vga_hsync;
VGA_VS <= not vga_vsync;
VGA_SYNC <= '0';
VGA_BLANK <= not (vga_hsync or vga_vsync);

```

```
end rtl;
```

6.12 Pseudo Code for double precision multiplication

Double x, Double y;

If x==0 || y==0;

 Return 0.0;

Else

 Bit operation to extract sign,exponent,fraction;

 fraction |= 0x0010000000000000;

 temp=(fraction_x>>27)*(fraction_y>>27); // integer multiplication

 test=(0x800000000000&temp)>>51; // test the position of first 1;

 If test==1{

 fraction_result=(0x7FFFFFFFFF&temp)<<1;

 exp_result=exp_x+exp_y-1022;

 }

 Else {

 fraction_result=(0x3FFFFFFFFF&temp)<<2;

 exp_result=exp_x+exp_y-1023;

 }

 Return outcome=(sign_result<<63) | (exp_result<<52) | fraction_result;

6.13 Pseudo Code for double precision add/subtraction:

Double x, Double y;

If x==0; return y;

Else if y==0; return x;

Else

 If sign_x == sign_y; do addition;

 Bit operation to extract sign,exponent,fraction;

 fraction |= 0x0010000000000000;


```

        temp=(fraction_x)+(fraction_y>>(exp_x-exp_y));
    find the position of first '1' in temp to adjust exp_result and fraction_result;
else do subtraction;
    Bit operation to extract sign,exponent,fraction;
    Determine which exponent is bigger; // here we suppose exp_x is bigger;
    sign_result=signx;
        temp=(fraction_x)-(fraction_y>>(exp_x-exp_y));
    find the position of first '1' in temp to adjust exp_result and fraction_result;
Return outcome=(sign_result<<63)| (exp_result<<52)| fraction_result;

```

6.14 C code for custom floating point multiplication and addition/substraction

```

inline double doubleadd(double x1, double y1){

    if(x1==0.0)
    {
        return y1;
    }

    else if(y1==0.0)
    {
        return x1;
    }
    else//none of 0
    {

int test,acc,sign_result;
    long long temp,checkx,checky;
    unsigned long long mask_result=0x0000000000000000;
    unsigned long long exp_result;

    unsigned long long realx;
    unsigned long long * maskx=(unsigned long long *) &x1;
    long long signx,expx;
    signx=(0x8000000000000000&*maskx)>>63;
    expx= (0x7FF0000000000000&*maskx)>>52;
    realx= 0x000FFFFFFFFFFFFFFF&*maskx;
    //checkx=realx+expx;
    realx|=0x0010000000000000;

    unsigned long long realy;
    unsigned long long * masky=(unsigned long long *) &y1;
    long long signy,expy;
    signy=(0x8000000000000000&*masky)>>63;
    expy= (0x7FF0000000000000&*masky)>>52;
    realy= 0x000FFFFFFFFFFFFFFF&*masky;
    //checky=realy+expy;

```

```
realy|=0x0010000000000000;
```

```
if(signx==signy)
{
    sign_result=signx;
    if(expx>=expy)
    {
        temp=(realx)+(realy>>(expx-expy));
        test=(0x200000000000000&temp)>>53;
        if(test==1){
            mask_result=(0x1FFFFFFFFFFFFFFF&temp)>>1;
            exp_result=expx+1;
        }
        else{
            mask_result=(0x0FFFFFFFFFFFFFFF&temp);
            exp_result=expx;
        }
    }
}

else//expx<expy
{
    temp=(realx)+(realy>>(expy-expx));
    test=(0x200000000000000&temp)>>53;
    if(test==1){
        mask_result=(0x1FFFFFFFFFFFFFFF&temp)>>1;
        exp_result=expy+1;
    }
    else{
        mask_result=(0x0FFFFFFFFFFFFFFF&temp);
        exp_result=expy;
    }
}
}

else//signx!=signy
{
    if(expx>expy)
    {

        sign_result=signx;
        temp=(realx)-(realy>>(expx-expy));
        if(temp==0) return 0;
        acc=0;test=(0x100000000000000&temp)>>52;
        while(test!=1){
            temp<<=1;
            test=(0x100000000000000&temp)>>52;
            acc++;
        }
    }
}
```

```

mask_result=(0x0FFFFFFFFFFFF&temp);
exp_result=expx-acc;
/*test=(0x1000000000000&temp)>>52;
if(test==0){
    mask_result=(0x07FFFFFFFFFFFF&temp)<<1;
    exp_result=expx-1;
}
else{
    mask_result=(0x0FFFFFFFFFFFF&temp);
    exp_result=expx;
}*/
}
else if(expx<expy)
{
    sign_result=signy;
    temp=(realy)-(realx>>(expy-expx));
    if(temp==0) return 0;
    acc=0;test=(0x1000000000000&temp)>>52;
    while(test!=1){
        temp<<=1;
        test=(0x1000000000000&temp)>>52;
        acc++;
    }
    mask_result=(0x0FFFFFFFFFFFF&temp);
    exp_result=expy-acc;
}
else//expx=expy
{
    if(realx>=realy){
        sign_result=signx;
        temp=(realx)-(realy);
        if(temp==0) return 0;
        acc=0;test=(0x1000000000000&temp)>>52;
        while(test!=1){
            temp<<=1;
            test=(0x1000000000000&temp)>>52;
            acc++;
        }
        mask_result=(0x0FFFFFFFFFFFF&temp);
        exp_result=expx-acc;
    }
    else{
        sign_result=signy;
        temp=(realy)-(realx);
        if(temp==0) return 0;
        acc=0;test=(0x1000000000000&temp)>>52;
        while(test!=1){

```

```

        temp<<=1;
        test=(0x10000000000000&temp)>>52;
        acc++;
    }
    mask_result=(0x0FFFFFFFFFFFF&temp);
    exp_result=expx-acc;
}
}
}
exp_result=(exp_result<<52);
mask_result|=exp_result;
if(sign_result==1)
    mask_result|=0x8000000000000000;
else
    mask_result&=0x7FFFFFFFFFFFFFFF;
double *result=(double *)&mask_result;
double outcome=*result;
return outcome;
}
}

/*printf("x1 is %f, dewindow is %f,y1 is %f, buf is %f\n",
    x1,dewindow[k],y1,((*buf) [ch] [(k + ((i+1)>>1) <<6) ) + bufOffset[ch] & 0x3ff]));*/

```

```

inline double doublemul(double x1, double y1){

    if(x1==0.0 || y1==0.0){
        double outcome=0;
        return outcome;
    }
    else
    {
int test,sign_result;
    long long temp,checkx,checky;
    unsigned long long mask_result=0x0000000000000000;
    unsigned long long exp_result;

    unsigned long long realx;
    unsigned long long * maskx=(unsigned long long *) &x1;
    long signx,expx;
    signx=(0x8000000000000000&*maskx)>>63;
    expx= (0x7FF0000000000000&*maskx)>>52;
    realx= 0x000FFFFFFFFFFFFFFF&*maskx;

```

```

//checkx=realx+expx;
realx|=0x0010000000000000;

unsigned long long realy;
unsigned long long * masky=(unsigned long long *) &y1;
long signy,expy;
signy=(0x8000000000000000&*masky)>>63;
expy= (0x7FF0000000000000&*masky)>>52;
realy= 0x00FFFFFFFFFFFF&*masky;
//checky=realy+expy;
realy|=0x0010000000000000;

temp=(realx>>27)*(realy>>27);
test=(0x80000000000000&temp)>>51;
if(test==1){
    mask_result=(0x7FFFFFFFFFFFF&temp)<<1;
    exp_result=expx+expy-1022;//1074;
}
else{
    mask_result=(0x3FFFFFFFFFFFF&temp)<<2;
    exp_result=expx+expy-1023;//1075;
}

exp_result=(exp_result<<52);
mask_result|=exp_result;
sign_result=signx+signy;
if(sign_result==1)
    mask_result|=0x8000000000000000;
else
    mask_result&=0x7FFFFFFFFFFFF;

//double* result=&mask_result;
double *result=(double *)&mask_result;
double outcome=*result;
return outcome;
}
}

```