

*Preliminary Call for Papers*

# 11<sup>TH</sup> INTERNATIONAL WORKSHOP ON LOGIC & SYNTHESIS JUNE 4-7, 2002

*[IMMEDIATELY PRECEDING DAC-02]*

**LOCATION : CHATEAU LEMOYNE, NEW ORLEANS, LOUISIANA  
(IN FRENCH QUARTER)**

*General Chair:*  
**Soha Hassoun**  
Tufts University

*Program Chair:*  
**Steven Nowick**  
Columbia University

*Panel Chair:*  
**Diana Marculescu**  
CMU

*Focus Group Chair:*  
**Yuji Kukimoto**  
Silicon Perspective

*Benchmark Chair:*  
**Andreas Kuehlmann**  
Cadence

The International Workshop on Logic and Synthesis provides an international forum to promote research and exchange ideas about all aspects of integrated circuit and system synthesis, optimization, and verification. The workshop encourages early dissemination of ideas and results. Accepted papers are distributed only to IWLS participants.

The workshop format includes short talks, posters, a panel discussion, and a social evening gathering. To further simulate interaction among participants, there will be exercises in **collaborative problem solving**. Attendees will be divided into groups, each tackling a challenging problem submitted through the web and selected by the focus group chair. The EDA community is encouraged to submit logic and synthesis problems through <http://www.iwls.org/>. Example problems and details will be available in early January.

**Topics of interest** range across a variety of system description levels, from hardware/software, through behavioral, architectural, RTL, logic-level and transistor-level. Topics include, but are not limited to: (a) architectures & compilation; (b) synthesis & optimization; (c) power & timing analysis; (d) design validation & verification; and (e) design experiences. These topics span both synchronous and asynchronous domains in all technologies, including CMOS, GaAS, ECL, and Adiabatic.

Authors should submit **extended abstracts** for their proposed presentation. These must be no less than 1000 words and no more than 2500 words (i.e., 5 pages double column, 10pt font). We especially encourage submissions in the early stages of research which may highlight important new problems without necessarily providing complete solutions. Only electronic submissions will be accepted. See <http://www.iwls.org> for submission details. For questions, contact **program\_chair@iwls.org**. For **travel grants**, apply to ACM/SIGDA's travel grant program at <http://www.sigda.acm.org/Programs/TravelGrant/>.



The workshop is sponsored by the IEEE Computer Society and by ACM/SIGDA.



**Submission deadline for PAPERS and PROBLEMS:** March 15, 2002

*Notice of acceptance:* April 15, 2002

*Final version due:* May 8, 2002

**Technical Program Committee:**

*I. Bahar (Brown University)*  
*M. Berkelaar (Magma Design Automation)*  
*R. Brayton (U.C. Berkeley)*  
*J. Cortadella (UPC, Spain)*  
*E. Dubrova (KTH, Sweden)*  
*M. Fujita (University of Tokyo)*  
*E. Jacobs (TU Eindhoven)*  
*T. Kam (Intel)*  
*V. Kravets (IBM T.J. Watson)*  
*A. Kuehlmann (Cadence Berkeley Labs)*  
*Y. Kukimoto (Silicon Perspective)*  
*W. Kunz (University of Frankfurt)*

*D. Marculescu (CMU)*  
*Y. Matsunaga (Kyushu University)*  
*C. Meinel (University of Trier)*  
*S. Minato (NTT, Japan)*  
*R. Murgai (Fujitsu Labs of America)*  
*K. Sakallah (University of Michigan)*  
*T. Sasao (Kyushu Inst. of Technology)*  
*H. Savoj (Magma Design Automation)*  
*E. Sentovich (Cadence Berkeley Labs)*  
*N. Shenoy (Synopsys)*  
*L. Stok (IBM)*  
*T. Villa (PARADES, Italy)*

**For more information, visit us on the web at <http://www.iwls.org>**