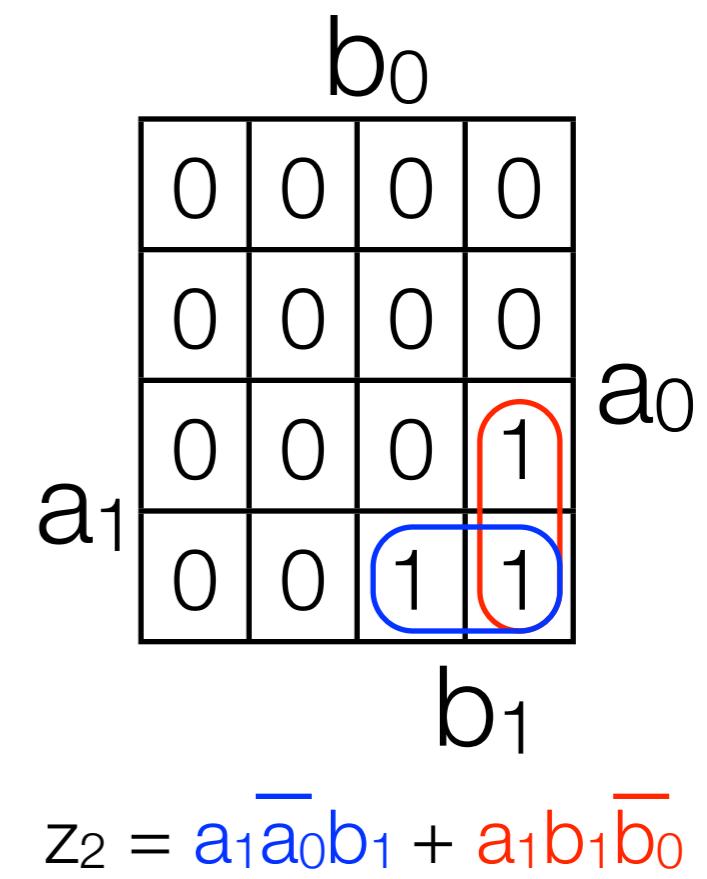
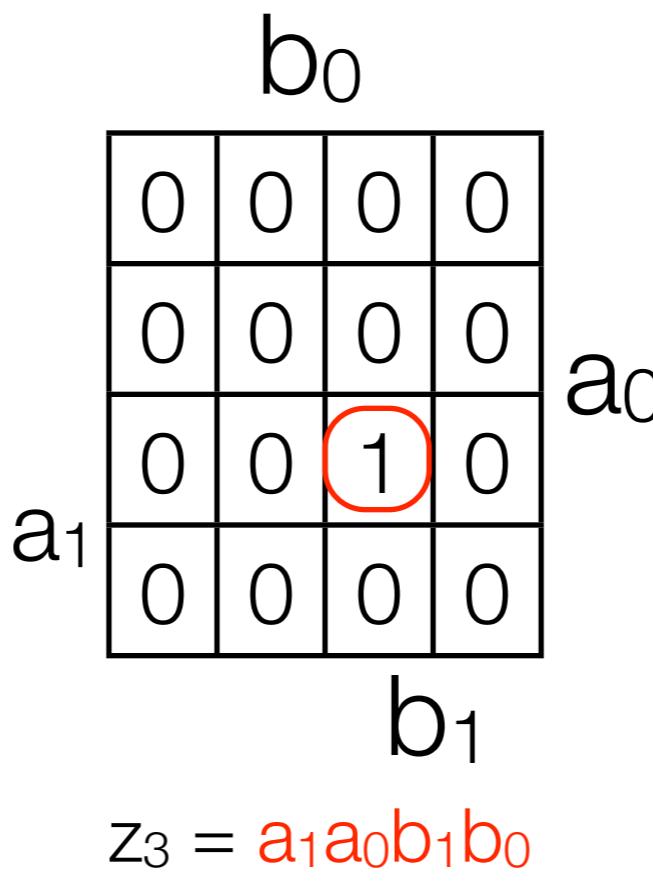


Design example : 2-bit multiplier (SOLUTION)

a1	a0	b1	b0	z3	z2	z1	z0
0	0	0	0				
0	0	0	1				
0	0	1	0				
0	0	1	1				
0	1	0	0				
0	1	0	1		1		
0	1	1	0	1			
0	1	1	1		1	1	
1	0	0	0				
1	0	0	1		1		
1	0	1	0	1			
1	0	1	1		1	1	
1	1	0	0				
1	1	0	1		1	1	
1	1	1	0		1	1	
1	1	1	1	1	1	1	1



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a1	a0	b1	b0	z3	z2	z1	z0
0	0	0	0				
0	0	0	1				
0	0	1	0				
0	0	1	1				
0	1	0	0				
0	1	0	1			1	
0	1	1	0		1		
0	1	1	1			1	1
1	0	0	0				
1	0	0	1			1	
1	0	1	0		1		
1	0	1	1		1	1	
1	1	0	0				
1	1	0	1			1	1
1	1	1	0		1	1	
1	1	1	1	1	1	1	1

b_0

0	0	0	0
0	0	1	1
0	1	0	1
0	1	1	0

a_1 a_0

b_1

$z_1 =$

$$a_1 \bar{a}_0 b_0 + \\ a_1 \bar{b}_1 b_0 + \\ \bar{a}_1 a_0 b_1 + \\ a_0 b_1 \bar{b}_0$$

b_0

0	0	0	0
0	1	1	0
0	1	1	0
0	0	0	0

a_1 a_0

b_1

$z_0 = a_0 b_0$