Fundamentals of Computer Systems Boolean Logic

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Boolean Logic

AN INVESTIGATION

THE LAWS OF THOUGHT,

ON WHICH ARE POUNDED

THE MATHEMATICAL THEORIES OF LOGIC AND PROBABILITIES.

BY

GEORGE BOOLE, LL.D.

LONDON:
WALTON AND MABERLY,
UPPER COWER-STREET, AND LYY-LANE, PATERNOSTER-ROW.
CAMBRIDGE: MACMILLAN AND CO.
1854.



George Boole 1815–1864

Boole's Intuition Behind Boolean Logic

Variables X, Y, \ldots represent classes of things No imprecision: A thing either is or is not in a class

If X is "sheep" and Y is "white things," XY are all white sheep,

$$XY = YX$$

and

$$XX = X$$
.

If X is "men" and Y is "women," X + Y is "both men and women,"

$$X + Y = Y + X$$

and

$$X + X = X$$
.

If X is "men," Y is "women," and Z is "European," Z(X+Y) is "European men and women" and

$$Z(X+Y) = ZX+ZY.$$

The Axioms of (Any) Boolean Algebra

A Boolean Algebra consists of

A set of values A
An "and" operator "."
An "or" operator "+"

A "not" operator \overline{X} A "false" value $0 \in A$ A "true" value $1 \in A$

The Axioms of (Any) Boolean Algebra

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An "and" operator "·" A "I

An "or" operator "+" A "I

A "not" operator \overline{X} A "false" value $0 \in A$ A "true" value $1 \in A$

Axioms

$$X + Y = Y + X \qquad X \cdot Y = Y \cdot X$$

$$X + (Y + Z) = (X + Y) + Z \qquad X \cdot (Y \cdot Z) = (X \cdot Y) \cdot Z$$

$$X + (X \cdot Y) = X \qquad X \cdot (X + Y) = X$$

$$X \cdot (Y + Z) = (X \cdot Y) + (X \cdot Z) \qquad X + (Y \cdot Z) = (X + Y) \cdot (X + Z)$$

$$X + \overline{X} = 1 \qquad X \cdot \overline{X} = 0$$

The Axioms of (Any) Boolean Algebra

A Boolean Algebra consists of

A set of values A A "not" operator \overline{X} An "and" operator " \cdot " A "false" value $0 \in A$ An "or" operator "+" A "true" value $1 \in A$

Axioms

We will use the first non-trivial Boolean Algebra: $A = \{0, 1\}$. This adds the law of excluded middle: if $X \neq 0$ then X = 1 and if $X \neq 1$ then X = 0.

"You are a New Yorker if you were born in New York or were not born in New York and lived here ten years."

$$X + (\overline{X} \cdot Y)$$

$$X \cdot 1 = X \cdot (X + \overline{X})$$

= $X \cdot (X + Y)$ if $Y = \overline{X}$
= X

"You are a New Yorker if you were born in New York or were not born in New York and lived here ten years."

$$X + (\overline{X} \cdot Y) = (X + \overline{X}) \cdot (X + Y)$$

Axioms
$$X + Y = Y + X$$

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$$X + (X \cdot Y) = X$$

$$X \cdot (X + Y) = X$$

$$X \cdot (Y + Z) = (X \cdot Y) + (X \cdot Z)$$

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"You are a New Yorker if you were born in New York or were not born in New York and lived here ten years."

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$$= (X + \overline{X}) \cdot (X + Y)$$

$$= 1 \cdot (X + Y)$$

Axioms
$$X + Y = Y + X$$

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$$X \cdot (X + Y) = X$$

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$$X + (Y \cdot Z) = (X + Y) \cdot (X + Z)$$

$$X + \overline{X} = 1$$

$$X \cdot \overline{X} = 0$$

$$X \cdot 1 = X \cdot (X + \overline{X})$$

= $X \cdot (X + Y)$ if $Y = \overline{X}$
= X

"You are a New Yorker if you were born in New York or were not born in New York and lived here ten years."

$$X + (\overline{X} \cdot Y)$$

$$= (X + \overline{X}) \cdot (X + Y)$$

$$= 1 \cdot (X + Y)$$

$$= X + Y$$

Axioms
$$X+Y=Y+X \\ X \cdot Y=Y \cdot X$$

$$X+(Y+Z)=(X+Y)+Z$$

$$X \cdot (Y \cdot Z)=(X \cdot Y) \cdot Z$$

$$X+(X \cdot Y)=X$$

$$X \cdot (X+Y)=X$$

$$X \cdot (Y+Z)=(X \cdot Y)+(X \cdot Z)$$

$$X+(Y \cdot Z)=(X+Y) \cdot (X+Z)$$

$$X+\overline{X}=1$$

$$X \cdot \overline{X}=0$$

$$X \cdot 1 = X \cdot (X + \overline{X})$$

= $X \cdot (X + Y)$ if $Y = \overline{X}$
= X

More properties

0 + 0 =	0	0.0	=	0
0 + 1 =	1	0 · 1	=	0
1 + 0 =	1	1.0	=	0
1 + 1 =	1	1.1	=	1
$1 + 1 + \cdots + 1 =$	1	$1\cdot 1 \cdot \dots \cdot 1$	=	1
X + 0 =	X	<i>X</i> · 0	=	0
X + 1 =	1	<i>X</i> · 1	=	Χ
X + X =	X	$X \cdot X$	=	Χ
X + XY =	X	$X \cdot (X + Y)$	=	Χ
$X + \overline{X}Y =$	X + Y	$X \cdot (\overline{X} + Y)$	=	XY

More Examples (1)

$$XY + YZ(Y + Z) = XY + YZY + YZZ$$

$$= XY + YZ$$

$$= Y(X + Z)$$

$$X + Y(X + Z) + XZ = X + YX + YZ + XZ$$

$$= X + YZ + XZ$$

$$= X + YZ$$

More Examples (2)

$$\begin{array}{lll} XYZ+X(\overline{Y}+\overline{Z}) &=& XYZ+X\overline{Y}+X\overline{Z} & \text{Expand} \\ &=& X(YZ+\overline{Y}+\overline{Z}) & \text{Factor w.r.t. } X \\ &=& X(YZ+\overline{Y}+\overline{Z}+Y\overline{Z}) & \overline{Z}\to \overline{Z}+Y\overline{Z} \\ &=& X(YZ+Y\overline{Z}+\overline{Y}+\overline{Z}) & \text{Reorder} \\ &=& X\big(Y(Z+\overline{Z})+\overline{Y}+\overline{Z}\big) & \text{Factor w.r.t. } Y \\ &=& X(Y+\overline{Y}+\overline{Z}) & Y+\overline{Y}=1 \\ &=& X(1+\overline{Z}) & 1+\overline{Z}=1 \\ &=& X & X1=X \end{array}$$

$$(X + \overline{Y} + \overline{Z})(X + \overline{Y}Z) = XX + X\overline{Y}Z + \overline{Y}X + \overline{Y}\overline{Y}Z + \overline{Z}X + \overline{Z}\overline{Y}Z$$
$$= X + X\overline{Y}Z + X\overline{Y} + \overline{Y}Z + X\overline{Z}$$
$$= X + \overline{Y}Z$$

Sum-of-products form

Can always reduce a complex Boolean expression to a sum of product terms:

$$\begin{array}{rcl} XY + \overline{X}\big(X + Y(Z + X\overline{Y}) + \overline{Z}\big) & = & XY + \overline{X}(X + YZ + YX\overline{Y} + \overline{Z}) \\ & = & XY + \overline{X}X + \overline{X}YZ + \overline{X}YX\overline{Y} + \overline{X}\overline{Z} \\ & = & XY + \overline{X}YZ + \overline{X}\overline{Z} \\ & \text{(can do better)} \\ & = & Y(X + \overline{X}Z) + \overline{X}\overline{Z} \\ & = & Y(X + Z) + \overline{X}\overline{Z} \\ & = & Y\overline{X}\overline{Z} + \overline{X}\overline{Z} \\ & = & Y + \overline{X}\overline{Z} \end{array}$$

What Does This Have To Do With Logic Circuits?

A SYMBOLIC ANALYSIS

RELAY AND SWITCHING CIRCUITS

bу

Claude Elwood Shannon B.S., University of Michigan

1956

Submitted in Partial Fulfillment of the Requirements for the Degree of

MASTER OF SCIENCE

Massachusetts Institute of Technology



Claude Shannon 1916–2001

Shannon's MS Thesis

"We shall limit our treatment to circuits containing only relay contacts and switches, and therefore at any given time the circuit between any two terminals must be either open (infinite impedance) or closed (zero impedance).





Shannon's MS Thesis

"It is evident that with the above definitions the following postulates hold.

$0 \cdot 0 = 0$	A closed circuit in parallel with a closed circuit is a closed circuit.
1 + 1 = 1	An open circuit in series with an open circuit is an open circuit.
1 + 0 = 0 + 1 = 1	An open circuit in series with a closed circuit in either order is an open circuit.
$0\cdot 1=1\cdot 0=0$	A closed circuit in parallel with an open circuit in either order is an closed circuit.
0 + 0 = 0	A closed circuit in series with a closed circuit is a closed circuit.
$1\cdot 1=1$	An open circuit in parallel with an open circuit is an open circuit.
	At any give time either $X = 0$ or $X = 1$

Alternate Notations for Boolean Logic

Operator	Math	Engineer	Schematic
Сору	X	X	x- or $x-$
Complement	$\neg x$	\overline{X}	$x-\overline{y}$
AND	$x \wedge y$	XY or $X \cdot Y$	X — — XY
OR	$x \vee y$	X + Y	X — X + Y

Definitions

Literal: a Boolean variable or its complement

E.g.,
$$X \overline{X} Y \overline{Y}$$

Implicant: A product of literals

E.g.,
$$X XY X\overline{Y}Z$$

Minterm: An implicant with each variable once

E.g.,
$$X\overline{Y}Z$$
 XYZ $\overline{X}\overline{Y}Z$

Maxterm: A sum of literals with each variable once

E.g.,
$$X + \overline{Y} + Z$$
 $X + Y + Z$ $\overline{X} + \overline{Y} + Z$

Be Careful with Bars



Be Careful with Bars

$$\overline{X}\,\overline{Y}\neq\overline{XY}$$

Let's check all the combinations of X and Y:

X	Υ	\overline{X}	Y	$\overline{X} \cdot \overline{Y}$	XY	XY
0	0	1	1	1	0	1
0	1	1	0	0	0	1
1	0	0	1	0	0	1
1	1	0	0	0	1	0

Truth Tables

A *truth table* is a canonical representation of a Boolean function

Χ	Υ	XY
0	0	0
0	1	0
1	0	0
1	1	1

Minterms

Each row has a unique minterm

X	Υ	Minterm	$\overline{X}\overline{Y}$	Χ̈Υ	ΧŸ	XY
0	0	$\overline{X}\overline{Y}$	1	0	0	0
0	1	$\overline{X}Y$	0	1	0	0
1	0	$X\overline{Y}$	0	0	1	0
1	1	XY	0	0	0	1

The minterm is the product term that is 1 for only its row

Maxterms

Each row has a unique maxterm

X	Y	Maxterm	X + Y	$X + \overline{Y}$	$\overline{X} + Y$	$\overline{X} + \overline{Y}$
0	0	X + Y	0	1	1	1
0	1	$X + \overline{Y}$	1	0	1	1
1	0	$\overline{X} + Y$	1	1	0	1
1	1	$\overline{X} + \overline{Y}$	1	1	1	0

The maxterm is the sum term that is 1 for only its row

Sum-of-minterms and Product-of-maxterms

Two mechanical ways to translate a function's truth table into an expression:

X	Υ	Minterm	Maxterm	F
0	0	$\overline{X}\overline{Y}$	X + Y	0
0	1	$\overline{X}Y$	$X + \overline{Y}$	1
1	0	$X\overline{Y}$	$\overline{X} + Y$	1
1	1	XY	$\overline{X} + \overline{Y}$	0

The sum of the minterms where the function is 1:

$$F = \overline{X}Y + X\overline{Y}$$

The product of the maxterms where the function is 0:

$$F = (X + Y)(\overline{X} + \overline{Y})$$

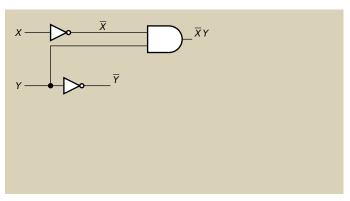
$$F = \overline{X}Y + X\overline{Y}$$
 X
 Y

$$F = \overline{X}Y + X\overline{Y}$$

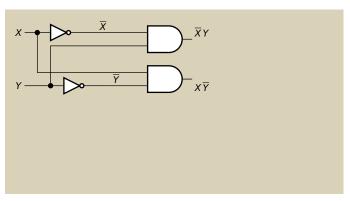
$$X \longrightarrow \overline{X}$$

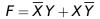
$$Y \longrightarrow \overline{Y}$$

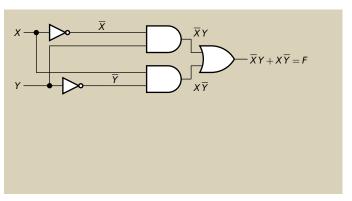




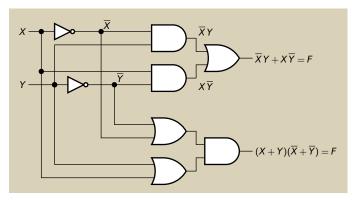








$$F = \overline{X}Y + X\overline{Y} = (X + Y)(\overline{X} + \overline{Y})$$



Minterms and Maxterms: Another Example

The minterm and maxterm representation of functions may look very different:

X	Υ	Minterm	Maxterm	F
0	0	$\overline{X}\overline{Y}$	X + Y	0
0	1	$\overline{X}Y$	$X + \overline{Y}$	1
1	0	$X\overline{Y}$	$\overline{X} + Y$	1
1	1	XY	$\overline{X} + \overline{Y}$	1

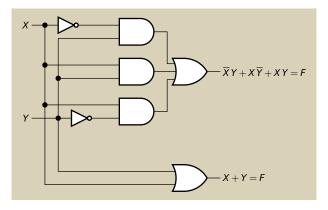
The sum of the minterms where the function is 1:

$$F = \overline{X}Y + X\overline{Y} + XY$$

The product of the maxterms where the function is 0:

$$F = X + Y$$

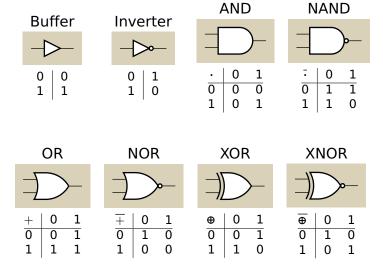
$$F = \overline{X}Y + X\overline{Y} + XY = X + Y$$



The Menagerie of Gates



The Menagerie of Gates



De Morgan's Theorem

$$\overline{X + Y} = \overline{X} \cdot \overline{Y}$$
 $\overline{X \cdot Y} = \overline{X} + \overline{Y}$

Proof by Truth Table:

X	Υ	X + Y	$\overline{X} \cdot \overline{Y}$	$X \cdot Y$	$\overline{X} + \overline{Y}$
0	0	0	1	0	1
0	1	1	0	0	1
1	0	1	0	0	1
1	1	1	0	1	0

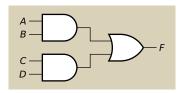
De Morgan's Theorem in Gates

$$\overline{AB} = \overline{A} + \overline{B}$$

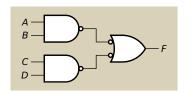
$$\overline{A} + \overline{B} = \overline{A} \cdot \overline{B}$$

$$\overline{A} + \overline{B} = \overline{A} \cdot \overline{B}$$

Bubble Pushing

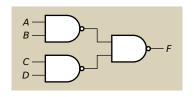


Bubble Pushing



Two bubbles on a wire cancel

Bubble Pushing



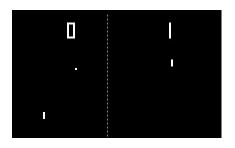
Two bubbles on a wire cancel

Apply De Morgan's Theorem (i.e., push the bubbles through the gates)

Transform OR with inverted inputs into NAND

PONG





PONG, Atari 1973

Built from TTL logic gates; no computer, no software Launched the video arcade game revolution

М	L	R	Α	В
0	0	0	Χ	Χ
0	0	1	0	1
0	1	0	0	1
0	1	1	Χ	Χ
1	0	0	Χ	Χ
1	0	1	1	0
1	1	0	1	1
1	1	1	Х	Χ

The ball moves either left or right.

Part of the control circuit has three inputs: *M* ("move"), *L* ("left"), and *R* ("right").

It produces two outputs A and B.

Here, "X" means "I don't care what the output is; I never expect this input combination to occur."

Μ	L	R	Α	В
0	0	0	0	0
0	0	1	0	1
0	1	0	0	1
0	1	1	0	0
1	0	0	0	0
1	0	1	1	0
1	1	0	1	1
1	1	1	0	0

E.g., assume all the X's are 0's and use Minterms:

$$A = M\overline{L}R + ML\overline{R}$$

$$B = \overline{M}\overline{L}R + \overline{M}L\overline{R} + ML\overline{R}$$

$$3 \text{ inv} + 4 \text{ AND3} + 1 \text{ OR2} + 1 \text{ OR3}$$

Μ	L	R	Α	В
0	0	0	1	1
0	0	1	0	1
0	1	0	0	1
0	1	1	1	1
1	0	0	1	1
1	0	1	1	0
1	1	0	1	1
1	1	1	1	1

Assume all the X's are 1's and use Maxterms:

$$A = (M + L + \overline{R})(M + \overline{L} + R)$$

$$B=\overline{M}+L+\overline{R}$$

$$3 \text{ inv} + 3 \text{ OR3} + 1 \text{ AND2}$$

Μ	L	R	Α	В
0	0	0	0	1
0	0	1	0	1
0	1	0	0	1
0	1	1	0	1
1	0	0	1	1
1	0	1	1	0
1	1	0	1	1
1	1	1	1	0

Choosing better values for the X's and being much more clever:

$$A = M$$

$$B = \overline{MR}$$

1 NAND2 (!)

Basic trick: put "similar" variable values near each other so simple functions are obvious

L	R	Α	В
0	0	Χ	Χ
0	1	0	1
1	0	0	1
1	1	Χ	Χ
0	0	Χ	Χ
0	1	1	0
1	0	1	1
1	1	Χ	Χ
	0 0 1 1 0 0	0 0 0 1 1 0 0 0 0 1 1 0	0 0 X 0 1 0 1 0 0 1 1 X 0 0 X 0 1 1 1 0 1

The *M*'s are already arranged nicely

М	L	R		Α	В			
0	0	0		Χ	Χ	Let	's re	arrange the
0	0	1		0	1	L's	by p	permuting two
0	1	0		0	1	pai	irs of	frows
0	1	1		Χ	Χ	-		
1	0	0		Χ	Χ			
1	0	1		1	0			
			1	1	0	1	1	
			1	1	1	Χ	Χ	

					_				
Μ	L	R	Α	В					
0	0	0	Χ	Χ	_	Let	's re	arran	ge the
0	0	1	0	1		L's	by p	bermu	iting tw
0	1	0	0	1		pai	rs o	f rows	5
0	1	1	Χ	Χ					
1	0	0	Χ	Χ					
1	0	1	1	0					
					1	1	0	1	1
					1	1	1	Χ	Χ

М	L	R	Α	В	
0	0	0	Χ	Χ	Let's rearrange the
0	0	1	0	1	L's by permuting two
0	1	0	0	1	pairs of rows
0	1	1	Χ	Χ	•
1	0	0 1	X 1	X 0 1	1 0 1 1 1 1 X X

М	L	R	Α	В	
0	0	0	Χ	Χ	Let's rearrange the
0	0	1	0	1	L's by permuting two
0	1	0	0	1	pairs of rows
0	1	1	Χ	Χ	•
1	0	0 1	X 1	X 1 0	1 0 1 1 1 1 X X

					_				
М	L	R	Α	В					
0	0	0	Χ	Χ		Let	's re	arran	ge the
0	0	1	0	1		L's	by p	bermu	iting two
0	1	0	0	1		pai	irs o	f rows	;
0	1	1	Χ	Χ		-			
					1	1	0	1	1
					1	1	1	Χ	Χ
1	0	0	Χ	Χ					
1	0	1	1	0					
					-				

					_			
Μ	L	R	Α	В				
0	0	0	Χ	Χ	_	Let's i	earr	ange the
0	0	1	0	1		L's by	per	muting tw
0	1	0	0	1		pairs	of ro	WS
0	1	1	Χ	Χ		-		
			1	1	0	1	1	
			1	1	1	Χ	Χ	
1	0	0	Χ	Χ				
1	0	1	1	0				
					_			

М	L	R	Α	В	
0	0	0	Χ	Χ	Let's rearrange the
0	0	1	0	1	L's by permuting two
0	1	0	0	1	pairs of rows
0	1	1	Χ	Χ	
		1	1 0	1	1
		1	1 1	Χ	Χ
1	0	0	Χ	Χ	
1	0	1	1	0	

Basic trick: put "similar" variable values near each other so simple functions are obvious

Μ	L	R	Α	В
0	0	0	Χ	Χ
0	0	1	0	1
0	1	0	0	1
0	1	1	Χ	Χ
1	1	0	1	1
1	1	1	Χ	Χ
1	0	0	Χ	Χ
1	0	1	1	0

Let's rearrange the L's by permuting two pairs of rows

Basic trick: put "similar" variable values near each other so simple functions are obvious

М	L	R	Α	В
0	0	0	Χ	Χ
0	0	1	0	1
0	1	0	0	1
0	1	1	Χ	Χ
1	1	0	1	1
1	1	1	Χ	Χ
1	0	0	Χ	Χ
1	0	1	1	0

The R's are really crazy; let's use the second dimension

Basic trick: put "similar" variable values near each other so simple functions are obvious

М	L	R	Α	В
0	0	0	χ ₀	Ϋ́ı
0	1	0	о _Х	¹ _X
1	1	0	1 _X	¹ _X
1	0	01	× ₁	χ _δ

The R's are really crazy; let's use the second dimension

М	L	R	Α	В	
00	00	01	X0	X1	
00	11	01	0 X	1 X	MR
11	11	01	1X	1 X	
11	00	01	X1	X0	J
					- IVI

Maurice Karnaugh's Maps

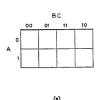
The Map Method for Synthesis of Combinational Logic Circuits

M. KARNAUGH

THE SEARCH for simple abstract techniques to be applied to the design of switching systems is still, despite some recent advances, in its early stages. The problem in this area which has been attacked most energetically is that of the synthesis of efficient combinational that is, nonsequential, logic circuits.

be convenient to describe other methods in terms of Boolean algebra. Whenever the term "algebra" is used in this paper, it will refer to Boolean algebra, where addition corresponds to the logical connective "or," while multiplication corresponds to "and."

The minimizing chart,2 developed at



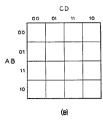


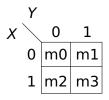
Fig. 2. Graphical representations of the input conditions for three and for four variables

Transactions of the AIEE, 1953

Karnaugh maps (a.k.a., K-maps)

All functions can be expressed with a map. There is one square for each minterm in a function's truth table

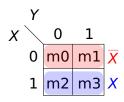
X	Y	minterm	
0	0	$\overline{X}\overline{Y}$	m0
0	1	$\overline{X}Y$	m1
1	0	$X\overline{Y}$	m2
1	1	XY	m3



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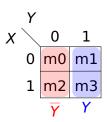
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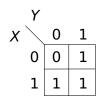
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Karnaugh maps (a.k.a., K-maps) – Cont. 1

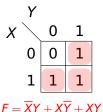
Fill out the table with the values of some function.

X	Υ	F
0	0	0
0	1	1
1	0	1
1	1	1



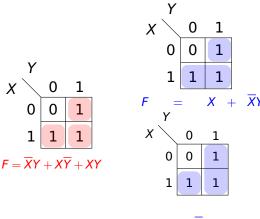
Karnaugh maps (a.k.a., K-maps) - Cont. 2

When two cells share an edge and both are 1, those two terms can be combined to form a single, simpler term.



Karnaugh maps (a.k.a., K-maps) – Cont. 2

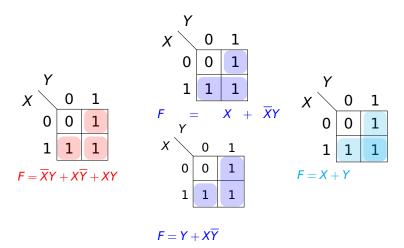
When two cells share an edge and both are 1, those two terms can be combined to form a single, simpler term.



$$F = Y + X\overline{Y}$$

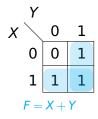
Karnaugh maps (a.k.a., K-maps) – Cont. 2

When two cells share an edge and both are 1, those two terms can be combined to form a single, simpler term.



Karnaugh maps (a.k.a., K-maps) – Summary So Far

- Circle contiguous groups of 1s (circle sizes must be a power of 2)
- There is a correspondence between circles on a k-map and terms in a function expression
- ▶ The bigger the circle, the simpler the term
- ▶ Add circles (and terms) until all 1s on the k-map are circled
- Prime implicant: circles that can be no bigger (smallest product term)
- Essential prime implicant: circles that uniquely covers a 1 is "essential"



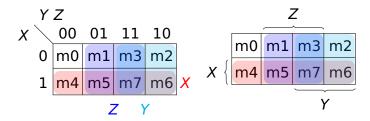
3-Variable Karnaugh Maps

- Use gray ordering on edges with multiple variables
- Gray encoding: order of values such that only one bit changes at a time
- Two minterms are considered adjacent if they differ in only one variable (this means maps "wrap")

	Y	Z			
Χ		00	01	11	10
	0	m0	m1	m3	m2
	1	m4	m5	m7	m6

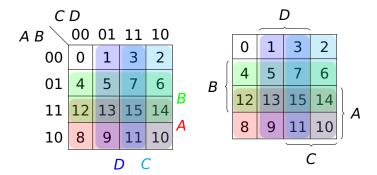
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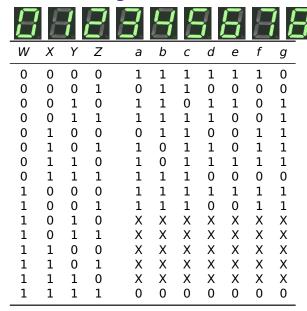


4-Variable Karnaugh Maps

An extension of 3-variable maps.



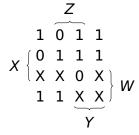
The Seven-Segment Decoder Example





Karnaugh Map for Seg. a

W	Χ	Y	Z	а
0	0	0	0	1
0	0	0	1	0
0	0	1	0	1
0	0	1	1 0	1 1
0	1	0	0	0
0 0 0	1 1 1	0	1	1
0	1	1	1 0	1
	1	1		1
1	1 0	1 0	1 0	1
1	0	0		1
1	0	1	1 0	Χ
1 1 1 1 1 1	0	1	1	Χ
1	1	0	1 0	Χ
1	1	0	1	Χ
	1	1	0	1 1 1 1 X X X X X
1	1	1	1	0



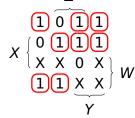
The Karnaugh Map Sum-of-Products Challenge

Cover all the 1's and none of the 0's using as few literals (gate inputs) as possible.

Few, large rectangles are good.

Covering X's is optional.

W	Χ	Y	Z	а
0	0	0	0	1
0	0	0	1	0
0	0	1	0	1
0 0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1 1 1	0	0	1	1
1	0	1	0	Χ
1	0	1	1	Χ
1	1	0	0	Χ
1	1	0	1	1 X X X X
1	1	1	0	Χ
1	1	1	1	0



The minterm solution: cover each 1 with a single implicant.

$$a = \overline{W}\overline{X}\overline{Y}\overline{Z} + \overline{W}\overline{X}YZ + \overline{W}\overline{X}Y\overline{Z} + \overline{W}X\overline{Y}Z + \overline{W}XY\overline{Z} + \overline{W}X\overline{Y}Z + \overline{W$$

$$8 \times 4 = 32$$
 literals

4 inv + 8 AND4 + 1 OR8

W	Χ	Υ	Z	а
0	0	0	0	1
0	0	0	1	0
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
1	0	1	0	Χ
1 1 1	0	1	1	Χ
1	1	0	0	Χ
1 1	1	0	1	Χ
1	1	1	0	X X X X
1	1	1	1	0

$$\begin{array}{c|cccc}
 & 1 & 0 & 1 & 1 \\
 & 0 & 1 & 1 & 1 \\
 & X & X & 0 & X \\
 & 1 & 1 & X & X
\end{array}$$

Merging implicants helps

Recall the distributive law:

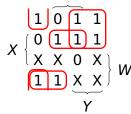
$$AB + AC = A(B + C)$$

$$a = \overline{W}\overline{X}\overline{Y}\overline{Z} + \overline{W}Y + \overline{W}XZ + W\overline{X}\overline{Y}$$

$$4+2+3+3=12$$
 literals

4 inv + 1 AND4 + 2 AND3 + 1 AND2 + 1 OR4

W	Χ	Y	Z	а
0	0	0	0	1
0	0	0	1	1 0
0	0	1	1 0	
0	0 0 0	1		1 1 0
0	1	0	1 0	0
0 0 0 0 0 0 0 0 1 1 1 1	1 1 1 0 0 0	1 0 0		
0	1	1	1 0	1
0	1	1	1	1
1	0	1 0 0	1 0	1
1	0	0	1	1
1	0	1	1 0	Χ
1	0	1	1	Χ
1	1	1 0	1 0	Χ
1	1 1	0		Χ
1	1	1	1 0	1 1 1 1 X X X X X
1	1	1	1	0

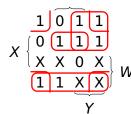


Missed one: Remember this is actually a torus.

$$a = \overline{X}\overline{Y}\overline{Z} + \overline{W}Y + \overline{W}XZ + W\overline{X}\overline{Y}$$

$$3+2+3+3=11$$
 literals
4 inv + 3 AND3 + 1 AND2 + 1 OR4

W	Χ	Y	Z	а
0	0	0	0	1
0	0	0	1	0
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	1
1	0	1	0	Χ
1	0	1	1 0	Χ
1	1	0	0	Χ
1	1	0	1	Χ
1	1	1	0	Χ
1	1	1	1	0



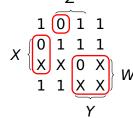
Taking don't-cares into account, we can enlarge two implicants:

$$a = \overline{X}\overline{Z} + \overline{W}Y + \overline{W}XZ + W\overline{X}$$

$$2 + 2 + 3 + 2 = 9$$
 literals

$$3 \text{ inv} + 1 \text{ AND3} + 3 \text{ AND2} + 1 \text{ OR4}$$

W	Χ	Y	Z	а
0	0	0	0	1
0	0	0	1	0
0	0	1	0	1
0	0	1	1	1
0	1	0	0	0
0	1	0	1	1
0 0 0 0 0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1 1	0	0	1	1 1
1	0	1	0	Χ
1	0	1	1	Χ
1	1	0	0	Χ
1	1	0	1	X X X X
1	1	1	0	Χ
1	1	1	1	0



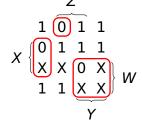
Can also compute the complement of the function and invert the result.

Covering the 0's instead of the 1's:

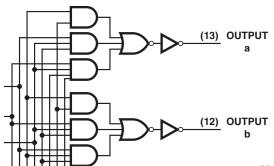
$$\overline{a} = \overline{W}\overline{X}\overline{Y}Z + X\overline{Y}\overline{Z} + WY$$

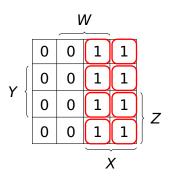
$$4 + 3 + 2 = 9$$
 literals

W	X	Y	Z	а
0	0	0	0	1
0	0	0	1	1 0
0	0		1 0	1
0	0 0 0 0	1 1 0	1	1 1 0
0		0	1 0	0
0 0 0 0 0 0	1 1 1 0 0	0	1	
0	1		1 0	1
0	1	1 1 0	1	1
1	0	0	1 0 1 0	1
1 1 1 1	0	0	1	1
1	0		0	Χ
1	0	1		Χ
1		1 1 0	1 0	Χ
1	1 1	0		1 1 1 1 X X X X X
1	1	1	1 0	Χ
1	1	1	1	0

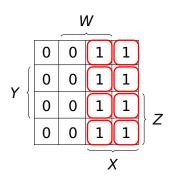


To display the score, PONG used a TTL chip with this solution in it:





$$WX\overline{Y}\overline{Z} + \overline{W}X\overline{Y}\overline{Z} + WXY\overline{Z} + WXY\overline{Z} + WXYZ + WXYZ + WX\overline{Y}Z + WX\overline{Y}Z$$
Factor out the W 's



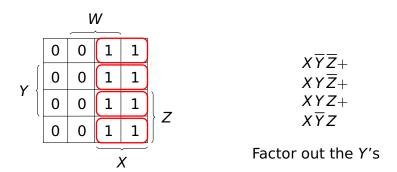
$$(W + \overline{W}) X \overline{Y} \overline{Z} + (W + \overline{W}) X Y \overline{Z} + (W + \overline{W}) X Y Z + (W + \overline{W}) X \overline{Y} Z$$

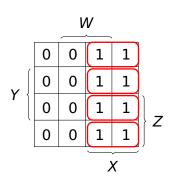
Use the identities

$$W + \overline{W} = 1$$

and

$$1X = X$$
.





$$(\overline{Y} + Y)X\overline{Z} + (\overline{Y} + Y)XZ$$

Apply the identities again

