

RECITATION #1

Sept 20, 2002

DIGITAL LOGIC

Topics Covered:

B.1 Introduction

B.2 Gates, Truth Table and Logic Equations

- Truth Table
- Boolean Algebra
- Gates

B.3 Combinational Logic

- Decoders
- Multiplexors
- Two level logic and PLAs
- Sum of Product
- ROMs
- Don't Cares
- Array of Logic Elements

B.4 Clocks

B.5 Memory Elements

- SR Latch
- Flip Flop and Latches
- Register Files

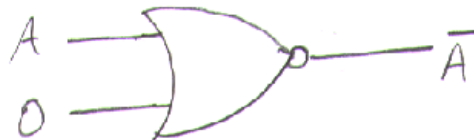
EXERCISES

B.7.

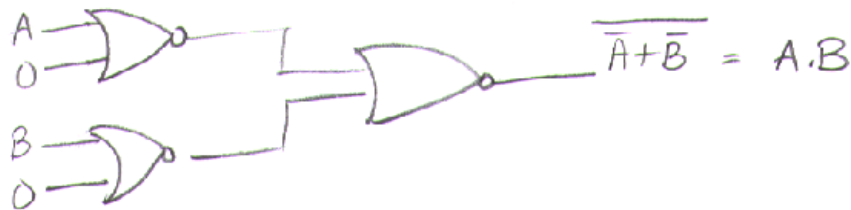
$$\begin{aligned} E &= ((A \cdot B) + (A \cdot C) + (B \cdot C)) \cdot (\overline{A \cdot B \cdot C}) \\ &= ((A \cdot B) + (A \cdot C) + (B \cdot C)) \cdot (\overline{A} + \overline{B} + \overline{C}) \text{ - DeMorgan} \\ &= (\overline{A} + \overline{B} + \overline{C}) \cdot (A \cdot B) + (\overline{A} + \overline{B} + \overline{C}) (A \cdot C) \\ &\quad + (\overline{A} + \overline{B} + \overline{C}) (B \cdot C) \text{ - Distributive Law} \\ &= \overline{A}AB + \overline{B}AB + \overline{C}AB + \overline{A}AC + \overline{B}AC + \overline{C}AC \\ &\quad + \overline{A}BC + \overline{B}BC + \overline{C}BC \text{ - Distributive} \\ &= \overline{A}B\overline{C} + \overline{B}AC + \overline{A}BC \text{ - using Inverse Laws} \end{aligned}$$

B.3

NOT gate using NOR



AND gate using NOR

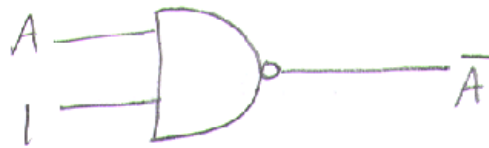


OR gate using NOR



B.4

NOT gate using NAND



Try AND using NAND yourself
Try OR using NAND yourself

B.8

$$E = (A \cdot B \cdot \bar{C}) + (A \cdot C \cdot \bar{B}) + (B \cdot C \cdot \bar{A})$$

$$= \overline{(A \cdot B \cdot \bar{C}) + (A \cdot C \cdot \bar{B}) + (B \cdot C \cdot \bar{A})}$$

$$= \overline{(\overline{A \cdot B \cdot \bar{C}}) \cdot (\overline{A \cdot C \cdot \bar{B}}) \cdot (\overline{B \cdot C \cdot \bar{A}})} \text{ using De Morgan's Law}$$

$$= \overline{(\bar{A} + \bar{B} + C) \cdot (\bar{A} + \bar{C} + B) \cdot (\bar{B} + \bar{C} + A)} \text{ using De Morgan's Law}$$