CSEE E6861y Prof. Steven Nowick Handout 4 January 21, 2016

Name:

E-mail:

School/Degree (MS/PhD/other):

Year (which year MS/PhD):

Major/Department:

Credit/Audit/Undecided:

Background: Have you taken the following (*or equivalent*)?:

- **CSEE W3827:** Fundamentals of Computer Systems (*or equivalent*) [1/2 course on basic digital logic, 1/2 course on computer organization]
- CS W4823: Advanced Logic Design (or equivalent)
- CS W3134/3136/3137: Data Structures and Algorithms (or equivalent)
- CS W4824: Computer Architecture (or equivalent)
- **EE4321:** Introduction to VLSI (*or equivalent*)
- CS4231: Analysis of Algorithms (or equivalent)

How familiar are you with the following? (very, somewhat, a little, not familiar)

- Karnaugh maps, 2-level (AND/OR) logic, truth tables?
- laws of Boolean algebra (associative, distributive laws, etc.)?
- basic components: multiplexers, demultiplexers, encoders?
- flipflops, latches, registers?
- adders, ALU's (arithmetic-logic units), multipliers?
- PLAs, ROMs?
- state machines (Mealy/Moore)?
- VHDL or Verilog?

What is your background in programming, data structures, algorithms? (incl. languages, programming experience, familiarity with trees/queues/linked lists, etc.)

Any experience using CAD tools, or learning about CAD algorithms?

Anything else you want to tell me about your background, research or interests?