

**Name:**

**E-mail:**

**School/Degree (BA/BS/MS/PhD/other):**

**Year (freshman/sophomore/etc., or which year MS/PhD):**

**Major/Department:**

**Credit/Audit/Undecided:**

**Background:** Have you taken the following (*or equivalent*):

- **introductory digital logic course:** CSEE3827 Fundamentals of Computer Systems (*or equivalent*)
- **data structures/algorithms:** CS W3134/3137 (*or equivalent*)
- **other relevant digital courses:** indicate level and content briefly

How familiar are you with the following? (*you do not need to know all of these, I am interested in calibrating your background*):

- Karnaugh maps, 2-level (AND/OR) logic, truth tables?
- 2's complement representation?
- transistor-level models of gates?
- negative logic (using NANDs/NORs)?
- laws of Boolean algebra (associative, distributive laws, etc.)?
- basic combinational components: multiplexers, demultiplexers, encoders?
- latches, flipflops, registers?
- basic adders, ALU's (arithmetic-logic units), multipliers?
- PLA's, PAL's, ROMs?
- state machines (Mealy/Moore)?
- VHDL or Verilog?
- programming, data structures, algorithms?

Any experience using CAD tools, or doing digital design in industry?

Anything else you want to tell me about your background, research or interests?