Wavetable Synth

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Wavetable Synthesis

- A sound wave signature is stored in memory
- Loop through this wave to make a sound







Different Notes

- Suppose a wave is sampled at 440 Hz and stored
- To sample at 880 Hz, skip every other address
- 1320 Hz = every third





MIDI Instruments

• Send status of key press and release

Table 1: MIDI 1.0 Specification Message Summary											
Status D7D0	Data Byte(s) D7D0	Description									
1000nnnn	Okkkkkk Ovvvvvv	Note Off event. This message is sent when a note is released (ended). (kkkkkkk) is the key (note) number. (vvvvvvv) is the velocity.									
1001nnnn	Okkkkkk Ovvvvvv	Note On event. This message is sent when a note is depressed (start). (kkkkkkk) is the key (note) number. (vvvvvvv) is the velocity.									



Our Design

- Send MIDI packets over USB to software synth program
- Synth converts MIDI signal data into calls to a hardware driver
- The driver accepts configuration of pitches (up to 10 notes)
- Each note requests samples from sampler
- Sampler can take samples from 2 wavetables and combine them with different coefficients
- The current samples can be configured from the software program
- The samples from all the 10 notes are combined and sent via interface to audio codec



Synth Software

Software: MIDI Decoder

- The MIDI decoder program is responsible for taking the MIDI Protocol messages utilizing the Libusb software library and translating the instructions into logic output:
 - MIDI Packets are 64 bytes
 - note, attack velocity, modulation
- Wave tales generated through Matlab and conversion script in python for any .wav files which normalizes the audio format..
 - o 48kHz
 - 16bit

Matlab Generated Audio Waves sine_wave pulse_wave saw_wave triangle_wave	
Python Audio Waves Converter Use existing .wav files and convert to 48kHz, 16bit audio MIDI_SW_Driver Libusb Input MIDI Data send_note send_wave start_wave	Hardware
MIDI Input Device Keyboard Device which sends in MIDI Commands	

Synth Hardware







ADSR enveloping

- Attack
- Decay
- Synthesis
- Release

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Performance Constraints

Time to write from memory to wavetables in BRAM is negligible

Longest portion of hardware is taken by the arbiter

Still responds to Codec requests about ~10 times faster than necessary



What works:

Testbenches for each individual module

Testbenches for each major module (wavetables, note_top)

Software for interpreting MIDI signals

Software driver for the synth hardware

Interfacing with the Audio Codec

What doesn't:

When they all come together