W4995-02 Languages for Embedded System Design

Fall 2001 Prof. Stephen A. Edwards

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Overview

- What are embedded systems?
 - · Computers masquerading as non-computers







Nokia 7110 Browser Phone







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Embedded System Challenges

- Differs from general-purpose computing
 - · Real-time constraints
 - · Power constraints
 - · Exotic hardware
 - Concurrency
 - · Control systems
 - · Signal processing
 - User interface
 - Physics

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The Role of Languages

- Language shapes how you solve a problem.
- Java, C, C++ and their ilk designed for general-purpose systems programming.
- Do not address timing, concurrency.
- Domain-specific languages much more concise.
- Problem must fit the language.



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Syllabus

- Software languages
 - Assembly
 - · c
 - · C++
- Java
- Real-time operating systems
 - Concurrency
 - · Meeting deadlines
- Dataflow languages
 - · Signal processing

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Syllabus

- Synchronous Languages
 - Global clock
- Hardware languages
 - · Discrete-event modeling
- SystemC
 - · Modeling hardware in C





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Goal of the Class

- Breadth
 - Knowledge of many different languages
 - Languages embody design methodologies
 - Broader knowledge, bigger "bag of tricks"



Empire State Building

Depth

- Big design project
- Gives you in-depth experience with one of the languages

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How to Listen to a Lecture

- Ask questions
- Trick: Presenters do a better job when they think someone is listening



"And should there be a sudden loss of consciousness during this meeting, oxygen masks will drop from the ceiling.

I'm from Berkeley

Every VW bus there sports this bumper sticker:

QUESTION AUTHORITY

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Required Text

- Languages for Digital Embedded Systems
- Available at Papyrus, 114th and Broadway
- Textbooks are downstairs
- Bookstore may run out: more can be ordered



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Class Website

www.cs.columbia.edu/~sedwards/classes/2001/w4995-02

- Contains
 - · Lecture slides
 - · More project ideas
 - Pointers elsewhere
 - PDF/PS files
 - · Detailed syllabus



Shortcut from www.cs.columbia.edu/~sedwards/

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Class Structure

- Four homework assignments
 - · Collaboration permitted, but work must be your own
- Two exams
 - · One covering first half of class
 - One covering second half
- One big project
 - · Project proposal due in two weeks
 - Literature review
 - · Presentation of literature review
 - · Presentation of final project
 - · Final write-up



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The Project

- Goal is to produce a workshop-caliber paper
 - · You don't have to submit it
 - · But aim for that level



- Introduction
- Literature survey
- Technical details
- Experimental resultsConclusions
- Literature survey due at midterm time

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Project Ideas

- "Use the languages"
 - Compare the simulation performance of Verilog and System C
 - Verilog and System C

 Compare the performance of an RTOS and
 - · Model a wristwatch in different languages
- "Analyze or implement the languages"
 - · Verilog Hierarchy browser
 - · Implement Kahn Process Networks
 - · A Java-to-C translator
 - Compiled event-driven simulator for Esterel
- More ideas on the class web site

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Pantheon, Interior

Project Proposal

- One-paragraph description of what you plan to do
- Due soon: September 26



- Use the web site for more ideas
- "Related Classes" lists classes at other institutions with additional project ideas
- · Visit during office hours to discuss ideas

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Collaboration

- You may collaborate on homework, but whatever you turn in must be your own
- Project teams should be two or three people



Frank O. Gehry, Chiat/Day Headquarters

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Late Policy

- No credit for late assignments unless you've made prior arrangements with me
- Homework is due at the beginning of class

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One-minute Feedback

- Spend a minute at the end of each class writing a sentence or two.
- Examples of desired feedback:
 - "I really didn't understand nondeterminism."
 - "You spent too much time talking about structural Verilog."
 - "I found the part about Ritchie's hatred of Pascal really interesting."
- Won't be graded, but sign your name.

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